

MS[™]-DOS V2.05 Technical Documentation

First Printing, November 1984

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Recommended Documents

Contains a list of additional reading materials.

Rainbow MS™-DOS V2.11 Update Notes

These update notes contain information about the MS–DOS Version 2.11 operating system. Use them in conjunction with the *Rainbow MS–DOS V2.05 Programmer's Guide* if you are using the MS–DOS Version 2.11 operating system.

Rainbow MS™-DOS V2.05 Programmer's Guide

This programmer's guide describes the Rainbow computer hardware and firmware. It also describes the MS-DOS Version 2.05 operating system, and differences between Version 2.01 and Version 2.05. The guide describes the BIOS, and the additional functions under the MS-DOS Version 2.05 operating system.

The appendix lists the Rainbow specifications for on-disk structures, plus additional information about the MS-DOS Version 2.05 operating system functions.

Rainbow MS™-DOS V2.05 BIOS Listings

These listings contain the MS-DOS V2.05 Basic Input/Output System (BIOS).

Microsoft MS™-DOS Operating System Programmer's Reference Manual

This manual (for system programmers) contains descriptions and examples of MS–DOS system calls and interrupts. It covers installation information for device drivers. There is also technical information about disk allocation, control blocks, work areas, EXE file structure and loading.

Microsoft MS™-DOS Operating System Macro Assembler Manual

This manual describes Microsoft's utility programs used for developing assembly language programs. They include the:

- Macro Assembler
- LINK Linker
- LIB Library Manager
- CREF Cross Reference
- DEBUG

Rainbow Guidelines for Producing Translatable Products

This document explains how to design and build software and write text so that they can be easily translated into other languages.



Other Technical Documentation Kits

- 1. Rainbow CP/M-86/80 V2.0 Technical Documentation (QV067-GZ)
- 2. Rainbow 100+/100B Technical Documentation (QV069–GZ)

Additional Documents

1. Letterprinter 100 User Documentation Package (EK-LP100-UG-001)

Includes:

Letterprinter 100 Operator Guide

LA100-Series Programmer Reference Manual

Letterprinter 100 Installation Guide

Letterprinter 100 Operator and Programmer Reference Card

2. Letterwriter 100 User Documentation Package (EK-LW100-UG-001)

Includes:

Letterwriter 100 Operator Guide

Letterwriter 100 Installation Guide

LA100-Series Programmer Reference Manual

- 3. Installing and Using the LQP02 Printer (AA-L662B-TK)
- 4. Installing and Using the LA50 Printer (EK-0LA50-UG-001)

Includes:

LA50 Printer Programmer Reference Manual

- 5. Rainbow 100 Extended Communications Option Programmer's Reference Guide (AA-V172A-TV)
- 6. PC100 Rainbow 100B System Unit IPB (EK-SB100-IP)

Includes:

EK-ORX50-IP

EK-LK201-IP

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- 7. VT102 Video Terminal User Guide (EK-VT102-UG-003)
- 8. CP/M Operating System Manual (AA-X637A-TV)

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MS[™]-DOS Version 2.11 Update Notes

November 1984

This update note contains some additional information for those users of the MS–DOS Version 2.11 operating system.

If you are currently using the MS–DOS Version 2.05 operating system disregard the information contained in this update note. However, we recommend you save this update note in case you ever purchase the MS–DOS Version 2.11 operating system.

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The PRINT Command

The first time you use the PRINT command after starting the MS-DOS operating system, it prompts you for the name of the list device. Because the default is "PRN:", (which is the normal printer port), you just need to press the "Return" key to continue.

The FORMAT Utility

If you use the FORMAT utility to format an MS-DOS disk and specify a volume identification, you can use the SYS command to create a new MS-DOS system volume.

Directory Paths

The MS-DOS Version 2.11 operating system supports a feature known as "directory paths", which allows you to create and maintain private areas or "sub-directories" on the same volume. The following are some more "hints" on how to use sub-directories and paths:

- If you specify a directory path in a command line other than COPY, TYPE or DIR, it is ignored, because COMMAND.COM does not process directory paths. COPY, DIR and TYPE process paths themselves.
- If you mistakenly type a "\" in front of an otherwise legitimate command (for example, \bin\masm), the MS-DOS operating system returns immediately to the prompt without any message being displayed.

Using the EDLIN Editor

When using the EDLIN editor, use the "Interrupt" key to generate an "escape" character when required instead of the Escape key (FII (ESC)).

Using the CTTY Command

If you use the "CTTY" command to change the console from device "CON:" to device "AUX:", for example, explicit references to device "CON:" in subsequent commands continue to be honored. For example, the command "COPY CON:TEXT.TXT" takes input from device "CON:" rather than device "AUX:", as expected.

Using the RECOVER Utility Program

The MS-DOS Version 2.11 operating system includes a utility program called "RECOVER" for recovering "bad spots" on your diskettes. It is described in the Chapter 5 of the Rainbow MS-DOS Version 2.11 Advanced User's Guide contained in your operating system kit. DIGITAL recommends, however, that you use this command only as a "last resort" to recover files from a bad diskette. This is because RECOVER could misinterpret data that may NOT be corrupted, which could lead to unpredictable results.

Using the PROMPT Command

When you use the PROMPT command with the \$P option, be sure that you specify an existing drive. Using this option with a non-existent drive requires that you reset the system.

Interrupt Vectors

The MS-DOS Version 2.11 operating system allows you to get or set all the MS-DOS interrupt vectors, 20H through 27H, using DOS function request 25H and 35H.

Serial 1/0 Functions

The MS-DOS Version 2.11 operating system implements:

- 1. The serial I/O function 14 "Set/Clear Modem Signals."
- 2. The serial I/O function 21 "Program Device Interrupt."

Using Ports

The MS-DOS Version 2.11 operating system supports 7M for the communications port, printer port, and the extended communications port.

Buffer Overflows

When you use the communication IOCTL functions and the serial receive buffer overflows, the last character is SUB (1Ah). The MS-DOS Version 2.11 operating system uses a bit to flag whether or not a real SUB character has been received or an actual overflow has occurred. The most significant bit of the character status byte (CHAR_STAT) is set when the serial receive buffer overflows.

Device Number

With the MS-DOS Version 2.11 operating system, the device number for the communication IOCTL functions 0, 3, 17, 19, 21, and 23 is placed into the communication control blocks or the interrupt service routine description.

10CTL Communication Driver

The MS-DOS Version 2.11 operating system contains a new IOCTL communication driver, function 26. Function 26 returns a double word pointer to a direct high performance entrance into the communications drivers. It bypasses the normal front end of MS-DOS. This direct entrance avoids the overhead of MS-DOS and the lack of MS-DOS re-entry.

NOTE

This function is not available in previous versions of the MS-DOS operating system. Therefore, any application programs using function 26 will not work under previous versions of DIGITAL's MS-DOS operating system.

Use function 26 only in situations that require faster re-entry access to the communications drivers. To use function 26:

ENTRY

AX = 4402H

BX = File handle

DS:DX = Packet address

FUNCTION in packet = 26 (no other entries used)

Invoke with INT 21H

EXIT

FUNCRET = FFH

BUFFER = Double word pointer to the

entry of the communications driver

NOTE

Call function 26 only once when initializing your application.

After function 26 gives you the entry address, you use all the other IOCTL functions by calling the drivers as follows:

ES:DI = Packet

DL = Device number

1 = Communications port

2 = Printer port

2 = Extended communications port

Invoke the function by performing a:

CALL DWORD PTR (buffer)

Where buffer = buffer of the packet returned by your previous function 26 call

All returns in the packet and actions performed are identical to those functions invoked through MS-DOS INT 21H.



MS™-DOS V2.05 Programmer's Guide

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The postage-prepaid READER'S COMMENTS form on the last page of this document requests the user's critical evaluation to assist us in preparing future documentation.

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PREFACE

INTENDED READERS

This guide is intended for experienced programmers who wish to write applications and programs for the Rainbow 100, 100+, and 100B computers. It provides an overview of the various documents that form the Rainbow MS-DOS Version 2.05 Technical Documentation Kit.

Readers who wish to learn how to use Microsoft's Macro Assembler should study the $\underline{\text{Microsoft}}$ $\underline{\text{MS-DOS}}$ $\underline{\text{Operating System Macro}}$ $\underline{\text{Macro Assembler}}$ Manual contained in this kit.

Guide Organization

Chapter 1 describes the Rainbow's hardware.

Chapter 2 discusses the Rainbow's firmware, (the features provided in its Read Only Memory (ROM)).

Chapter 3 introduces the MS-DOS operating system: its components, and how it stores and retrieves disk files. Chapter 3 also mentions some differences between MS-DOS Version 2.05 and Version 2.01.

Chapter 4 describes the MS-DOS BIOS, and the additional functions provided in the BIOS of MS-DOS Version 2.05.

Chapter 5 describes some of the differences between the Rainbow computer and the IBM PC. Use this chapter if you want to convert programs from one system to the other, or if you want to write programs that will run on either system. This chapter also includes a list of caveats and some useful programming examples.

Appendix A contains Rainbow specifications for on-disk structures.

Appendix B contains several Microsoft articles relating to MS-DOS functions.

CHAPTER 1

RAINBOW HARDWARE

1.1 INTRODUCTION

This chapter describes the Rainbow computer hardware, emphasizing those features of interest to programmers. This chapter assumes that you know the operation and characteristics of the Rainbow hardware by studying the manuals shipped with every Rainbow computer.

The Installation Guide provided with each Rainbow computer describes the system components. The guide illustrates the system unit, monitor unit and keyboard unit.

This chapter describes the hardware in general. Detailed descriptions are given for items relevant to programmers. The definitive hardware specification, however, is the system specification for the Rainbow computer you are using.

1.2 GENERAL DESCRIPTION

Figure 1-1 shows the Rainbow computer. The base system consists of three parts:

- System unit
- Keyboard
- Monitor

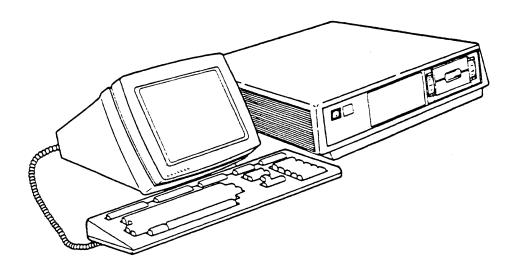


Figure 1-1: Rainbow Computer

The system unit contains the system module, a large printed circuit board where the logic circuits of the system are located. The Rainbow 100+ computer includes a Winchester hard disk drive. Figures 1-2 and 1-3 show the system block diagrams for the Rainbow 100 and Rainbow 100+/100B computers.

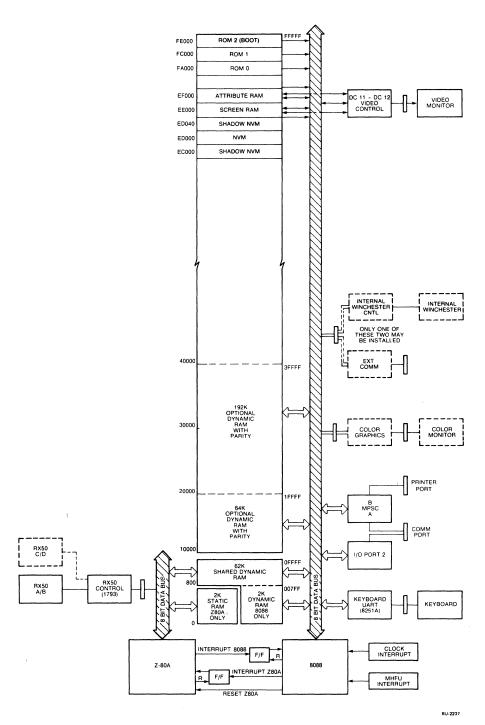


Figure 1-2: Rainbow 100 System Block Diagram

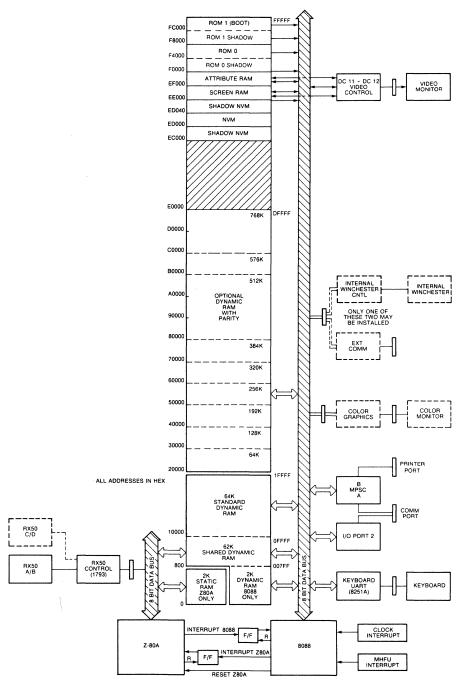


Figure 1-3: Rainbow 100+/100B System Block Diagram

1.3 PROCESSORS

The Rainbow computer uses dual microprocessors, an 8088 and a Z80A. These work together to provide the Rainbow computer's functionality.

System functions are divided between the 8088 and Z80A. The Z80A controls the disk drives, while the 8088 controls the video output, keyboard, communications input/output (1/0), printer 1/0, and all optional devices.

Both microprocessors can access certain shared random access memory (RAM), and each can also access private memory. The 8088 clock rate is 4.815 Mhz, and the Z80A clock rate is 4.012 Mhz.

1.4 RANDOM ACCESS MEMORY

The Rainbow 100A contains 64K bytes of RAM. When DIGITAL first shipped the Rainbow computer you could increase this RAM by inserting an optional memory expansion card. This optional RAM card was available in two sizes: 64K bytes and 192K bytes for a system total of either 128K bytes or 256K bytes. A 64K byte card is not upgradable to a larger size.

DIGITAL has since announced an adapter card for Rainbow computers that permits it to use the expansion RAM option. This makes it possible for all Rainbow computers to add a total of 768K bytes of memory.

The basic Rainbow 100B contains 128K bytes of RAM. You can expand the memory from 192K bytes to 896K bytes in multiples of 64K bytes.

The expansion memory is contained on a printed circuit card that holds from one to three "banks" of nine memory chips each. All chip positions are socketed so that the chips can be field installed. Each bank can be filled with nine memory chips yielding either of two capacities:

- 64K bytes
- 256K bytes

As a result, the card can have any of nine possible configurations from 64K bytes to 768K bytes. Table 1-1 shows various configurations. You can purchase the option in 128K byte or 256K byte configuration. You can also purchase expansion kits containing nine 64K-bit or nine 256K-bit RAM chips (sufficient for one bank).

Table 1-1: Rainbow 100B Optional Memory

SIZE (K)		CHIP SIZES IN	
	BANK	1 BANK 2	BANK 3
128 (Base Configuration)	64K	64K	-
192	64K	64K	64K
256 (Base Configuration)	256K	-	-
320	256K	64K	-
384	256K	64K	64K
512	256K	256K	-
576	256K	256K	64K
768	256K	256K	256K

The optional expansion memory contains parity checking circuitry. A non-maskable interrupt is generated if a parity error occurs. Its handler (which is in the firmware) normally causes the system to display an error message, then halt. Should an application program perform some other form of error trapping, it must "take over" the interrupt and provide its own handler.

All of the memory except for the Z80A's 2K-Private RAM (see next paragraph) is of the dynamic type. Such memory must be "refreshed" every few milliseconds to insure that its data is not lost. Refreshing is done by special circuits that use some of the available memory access cycles. The 8088, Z80A, direct memory access (DMA) and refresh circuitry all access this memory independently of one another. When one device attempts to access memory while another is in the process of doing so, the attempting device must wait until the memory is available. This is known as "contention." Notice that such contention usually makes it impossible to program precise timing loops.

The Z80A can address a total of only 64K bytes, and therefore cannot access memory locations at addresses greater than OFFFFH.

Both microprocessors can address the 62K bytes of RAM in their address space of 00800H through OFFFFH. This shared RAM allows the two microprocessors to pass data to one another, a necessary requirement of the Rainbow computer's architecture.

Both the 8088 and Z80A have 2K bytes of private RAM in the address space of 00000 to 007FFH. The 8088 private RAM is part of the 64K dynamic RAM, of which the top 62K is shared. The Z80A private RAM is of the static type. As such, it neither needs refreshing nor experiences contention for access cycles with the 8088 or other devices. This memory should be used for time-critical Z80A routines, such as programmed diskette reading and writing.

The extended communications option contains Direct Memory Access capabilities. Such DMA operations can only be performed to and from the dynamic RAM in Address space 00000 through OFFFFH.

1.5 NON-VOLATILE MEMORY (NVM)

The Rainbow computer has 256 nibbles of non-volatile memory, which retains stored values even when you turn the power off. The system uses this memory to store its Set-Up values. It is located at address ED000H through ED0FFH, but should not be directly accessed by application programs.

The Rainbow 100B firmware provides a slightly different set of NVM parameters than the Rainbow 100's NVM parameters. Also, the Rainbow 100B's Set-Up automatically determines and displays the size of installed RAM, whereas the Rainbow 100's Set-Up simply stores whatever value you enter there, whether it accurately reflects the size of installed RAM or not.

Although the NVM is addressed as bytes of data, only the low four bits of the data have meaning. This is due to the 256×4 organization of the NVM chip. The four most significant bits of each NVM data byte are ignored when written, and unpredictable when read.

1.6 VIDEO DISPLAY MEMORY

The video logic can use up to 4K bytes of screen RAM and 4K bytes of attribute RAM. Data to be displayed is placed in this RAM by firmware routines. This memory should not be directly accessed by application programs. The firmware provides functions for modifying the display memory. Portions of the Screen and Attribute RAM are also used for firmware variables, flags, pointers, stack, buffer and so forth. Accessing these RAM locations can cause a system reset or cause the system to stop.

1.7 READ ONLY MEMORY

The Rainbow 100 contains 24K bytes, and the Rainbow 100B 32K bytes, of Read Only Memory (ROM). This ROM contains diagnostic, VT102 terminal emulation, and VT102 console routines. These routines are also known as the system firmware.

The diagnostics are described in the manuals you received with your computer. The VT102 terminal emulation is described in the Terminal Emulation Manual. Read the "Functional Anomalies" found in the system specification if you want to program a remote host to work with the Rainbow computer as a terminal.

1.8 DISKETTES

The basic Rainbow computers include a dual-disk drive that can store 400K bytes of data on each of two 5 1/4 inch diskettes. You can add a second dual-disk drive, bringing the total on-line diskette storage capacity to 1600K bytes, or 1.6 megabytes. A Rainbow computer can contain either a Winchester type hard disk or the second RX50 drive, not both. (See "Hard Disk Option".) You install the optional disk drive in the system module housing.

A separate card containing a Western Digital 1793 Disk Controller chip, and related logic circuits provides control of the diskettes. This diskette controller card is part of the basic system.

You must format diskettes before can write on them. DIGITAL-type RX50K diskettes have been preformatted for ten 512-byte sectors per track and can be used immediately. Non-DIGITAL diskettes (and those that have lost format data) must be formatted for the Rainbow computer before you can use them.

The MS-DOS operating system also requires that diskettes be "soft" formatted for its use. This is a different operation than the physical formatting required by the hardware. The MS-DOS Version 2.05 operating system contains a special format utility (FORMAT) that you can use to physically and "soft" format your diskettes.

The 1793 Disk Controller chip has been hard-wired to read and write only in double-density mode. The RX50 accesses 80 tracks spaced at 96 tracks per inch (96 TPI). The RX50 can also read 48 TPI diskettes with proper software, as can diskettes having different numbers of sectors (for example, IBM 8 and 9 sector diskettes).

1.9 KEYBOARD

The keyboard communicates with the system module through an 8251A Universal Asynchronous Receiver Transmitter (UART) by means of a cable. The cable attaches to the monitor module, using a modular telephone connector. The keyboard signals then pass through the monitor's cable to the system unit.

Pressing a key causes a single uniquely coded byte to be serially transmitted to the system unit where it generates an interrupt 26H. The normal interrupt handler is contained in the firmware. It places the incoming keycode into a buffer, from which it can be obtained by the application software. This routine is complex and is used by both the Terminal mode and Console mode firmware. Applications should NOT attempt to do keyboard processing, but should use the functions provided in the firmware for accessing keyboard characters.

1.10 INTERRUPTS

Both the 8088 and Z80A microprocessors have interrupt capabilities. Hardware signals or software instructions can generate 8088 interrupts. You can program each processor to interrupt the other.

The Rainbow computer's operating system always treats one of the processors as a "master" and the other as a "slave." When the master requires the slave to perform a function, it notifies the slave by generating an interrupt to it. The original slave then becomes a master and the original master becomes a slave.

The Rainbow 100B hardware interrupts can be assigned two sets of interrupt vectors under software control. The Rainbow 100B's firmware provides routines to move the interrupt vectors from one location to another. This is necessary because the Rainbow computer's hardware and MS-DOS operating system both use some of the same interrupts. The firmware relocation function is intended only for use by the MS-DOS operating system. If you try to change vectors from within an application you cannot use MS-DOS. If you access the hardware directly instead of by the firmware the keyboard-related access capabilities are destroyed.

The system module generates a vertical frequency interrupt either 50 or 60 times per second, determined by a Set-Up parameter. The firmware video display refresh routines use this interrupt. These refresh routines include a software interrupt 44H call that the operating systems use. They, in turn, generate an interrupt 64H, as a source of real-time signals for application use.

The MS-DOS operating system moves many interrupt vectors to alternate locations upon being loaded. Applications must always use the switched values. Tables 1-2 and 1-3 show the hardware-generated interrupts and addresses.

Table 1-2: 8088 Interrupt Types

Hardware Generated

Priorit	y		Type No.(Hex) orm/Relocated	Address (Hex) Norm/Relocated
Highest	2 2 6 2 2,6 3 2,6	Parity error Vert Freq Interrupt 7201 (Ext Comm Int 1) Graphics Option DMA (Ext Comm Opt) 7201 (Comm/Printer) 7201 (Ext Comm Int 0) (or Winchester 6 8251A (Keyboard)	22/A2 23/A3 24/A4 25/A5	08 80/280 84/284 88/288 8C/28C 90/290 94/294
Lowest	_	Z80A	27/A7	90/290
		Software Gene	erated	
	52444444444444444444444444444444444444	Firmware Functions 0/S Clock Tic MS-DOS MS-DOS System Function MS-DOS	18 2C/AC 20 21 22 23 24 25 26 27 64	60 80/280 84 88 8C 90 94 98 9C

NOTES

- 1. Relocated interrupt codes and addresses are only applicable to the Rainbow 100+ and 100B computers.
- 2. Initialized by INT 18H Function OCH.
- 3. Reset/disabled by INT 18H Function 16H.
- 4. MS-DOS reserves all interrupts 20H through 3FH for its own use.
- 5. MS-DOS remaps Int 28H (normal Firmware entry) to 18H for a applications use during its startup operation.
- 6. There are signal path names to the Extended Communications Option connector. The Extended Communications Option only uses 23H and 25H, but other devices can use all three types.

Table 1-3: Z80A Interrupt Type

Only 8088 RST 6 30H

1.11 VIDEO MONITOR

The video control logic controls the data displayed on the video monitor. The DCO11 and DCO12 DIGITAL chips are used for the control logic. The system specification explains the operation of the video control logic.

1.12 I/O CONTROL PORTS

The 8088 and Z80A processors each send and receive data and control information to 1/0 devices through ports. Special In and Out instructions of the microprocessor access these ports (Addresses). Figures 1-4 and 1-5 show the addresses (port numbers) at which the several 1/0 devices are accessed. These figures also show bit use. Most of the ports are latches; that is, they hold the data placed in them until it is changed. Input and output ports are independent of one another. Output ports can only be written into, while input ports can only be read.

NOTE

Data sent to an output port is generally NOT readable by an input instruction to the same port address.

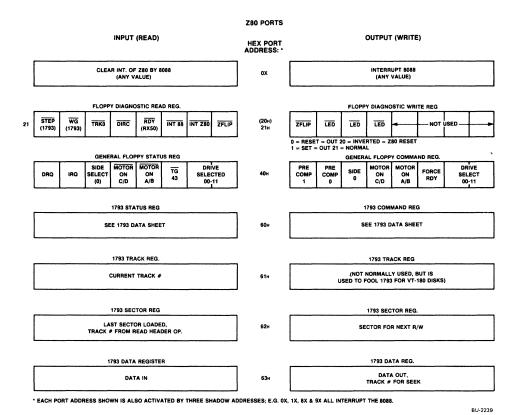


Figure 1-4: Z80 Ports

8088 PORTS

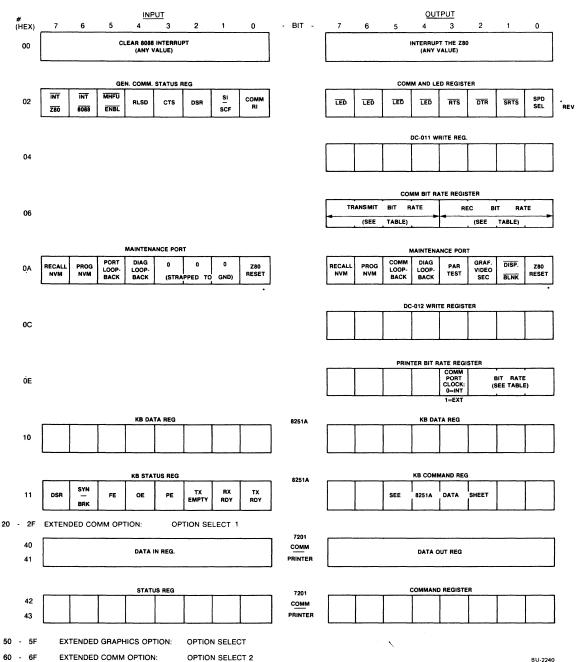


Figure 1-5: 8088 Ports

1.13 COMMUNICATIONS AND PRINTER INTERFACES

The basic Rainbow computer communicates to external devices through two interfaces - the communications interface and the printer interface. These interfaces share a single Intel 8274 (or Nec 7201) Universal Synchronous Asynchronous Receiver Transmitter (USART) Multi-Protocol Serial Controller (MPSC) chip mounted on the system motherboard.

The Intel data sheet and application notes contained in the Rainbow Technical Documentation Kit, (order number QV069-GZ), describe the operation of the 8274 chip. Both interfaces use PB25 25-pin connectors mounted on the rear of the system module. The Communication interface connector is a male (DB-25P), while the printer connector is a female (DB-25S). The communications port provides full modem support (in conjunction with I/O port 2) for both asynchronous and bisynchronous modes. It has an RS-423 compatible interface conforming to CCITT V.21, V.22, and V.23 specifications. This port supports full- and half-duplex modems and break detection. The firmware uses the communications port to attach to a host computer The MS-DOS Version 2.05 operating system when in terminal mode. provides an extensive set of system functions for controlling the communications, printer, and optional communications ports.

The printer port also has an RS-423 interface, which is compatible with both DIGITAL and non-DIGITAL printers. Both the firmware and the MS-DOS operating system use the printer port to send asynchronous data to serial printers such as the DIGITAL LA50, LA100, or LQPO2 printers. Data Terminal Ready (DTR) is supported for this port and can be used by software to control printers or other devices that do not support the XON/XOFF protocol. The Rainbow 100B's terminal mode supports both XON/XOFF and DTR protocols simultaneously. This means that the device attached to the Rainbow 100B's printer port must assert DTR, or no data is sent to the printer, and the system stops.

In console mode under the MS-DOS operating system, two protocols can be selected for the printer port. The Rainbow computer and the MS-DOS operating system default to the XON/XOFF protocol. To enable DTR, the printer port must be specifically programmed by the application or utility.

Figure 1-6 shows the signal paths to the communications and printer ports. Table 1-4 shows baud rates available for both the communications and printer ports. You initially select these ports by the Set-Up parameters, but they can also be selected as shown in Table 1-4 or by using the special serial I/O system functions of the MS-DOS operating system.

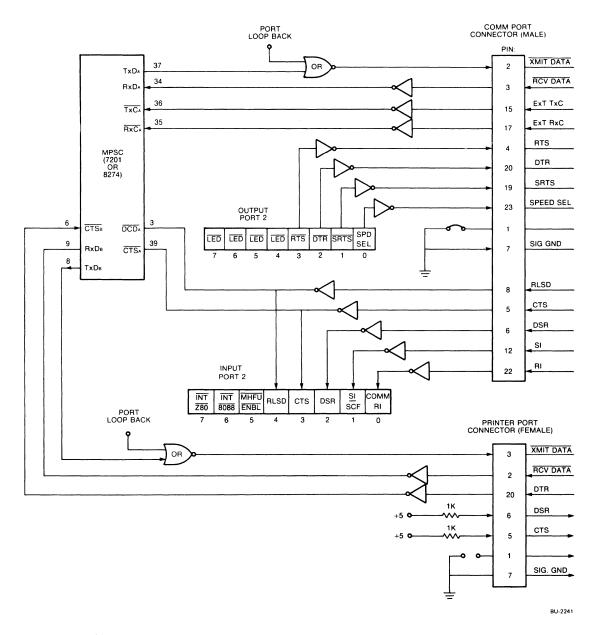


Figure 1-6: Communications and Printer Port Signal Paths

Table 1-4: Baud Rates Available

Rate	Comm	Nibble Value	Printer	Bit 0-2 Value	
50	*	0			
75	*	1	*	0	
110	*	2			
134.5	*	3			
150	*	4	*	1	
200	*	5			
300	*	6	*	2	
600	*	7	*	3	
1200	*	8	*	4	
1800	*	9			
2000	*	A			
2400	*	В	*	5	
3600	*	С		-	
4800	*	D	*	6	
9600	*	E	*	7	
19200	*	F		·	

To set communication port baud rates, transfer a byte with two nibbles from the above table to port 06H. The high nibble sets the receive rate, and the low nibble sets the transmit rate.

To set printer baud rates, transfer a byte with bits 0-2 set to the value from the above table to port OEH. Notice that bit 3 of this port selects the external clock for the communications port, so use care when setting printer baud rates.

1.14 SYSTEM RESET

You automatically clear the 8088 when you turn on the computer. You can also clear the 8088 processor at any time by pressing Ctrl/Set-Up in Set-Up mode. A special watchdog timer can also reset the system in the event it is not refreshed within a period of 100 milliseconds or the interrupts are off. The video display routines in the firmware refresh the circuit. It is important that application programs disable interrupts for no longer than 100 ms. or this circuit can reset the system.

If an application must disable interrupts for a longer period, the interrupts off circuitry can be temporarily disabled. Such a procedure can adversely affect the video display and any real-time dependent system operations.

The correct procedure for temporarily disabling the interrupts off circuitry is as follows:

- 1. Disable interrupts using the CLI instruction
- 2. Disable the interrupts off detection circuitry by transferring a 00H to 8088 I/O port 10CH.
- 3. Enable interrupts with the STI instruction when ready. The interrupts off detection circuit is automatically enabled.

1.15 AUTO-START

When you reset the Rainbow system, the system automatically tests several components. When the tests are completed, a main menu is displayed that offers a choice of disk drives from which to start the operating system. (The Rainbow 100B contains a new Set-Up feature that automatically starts the operating system without requiring you to press any keys.)

1.16 OPTIONAL I/O DEVICES

Three optional I/O devices are available for the Rainbow series computers:

- A color graphics option
- An extended communications option
- A hard disk option

1.16.1 Color Graphics Option

This option consists of a printed circuit board containing a special bit-mapped color video graphics controller chip and related logic that you mount on the motherboard.

The Rainbow computer can operate in text mode or text and graphics mode. In text mode (VT100 text mode), the graphics option video is disabled and the standard video control logic (DC011 and DC012) drives the monitor. In text and graphics mode, the option's video control logic drives the monitor. (You need a color monitor to display color graphics). Monochrome graphics can also be obtained using the VR201.

The <u>Rainbow Computer Color/Graphics Programmers Reference Guide</u> is included in the <u>Rainbow 100+/100B Technical Documentation Kit</u>, (order number QV069-GZ).

1.16.2 Extended Communications Option

An extended communications option provides additional communications capabilities. This option consists of a printed circuit card that mounts in two 40-pin connectors on the system module. Its communication outputs are available on D-subminiature connectors at the left rear of the system module.

The option provides the Rainbow computer with a separate high-speed communications port that can support clustering and local area networking. Asynchronous bit- and byte-synchronous capabilities are provided. This option uses a 8274 Multi-Protocol Serial Controller (MPSC) and a 8273 Direct Memory Access (DMA) controller.

The 8274 MPSC used in the extended communications option is the same as the MPSC used by the standard communications/printer ports. The BIOS of the MS-DOS Version 2.05 operating system includes a full set of routines for controlling the standard communications/printer ports and the ports of this option for asynchronous operations. The Rainbow 100 Extended Communications Option Programmer's Reference Guide, (order number AA-V172A-TV), describes the option. You cannot use this option if you have a hard disk because both options mount (and use) the same motherboard connectors.

1.16.3 Hard Disk Option

Two 5-1/4 inch diameter Winchester hard disks are optionally available for the Rainbow 100 and 100B computers. They differ only in the amount of storage they provide. The RD50 hard disk stores up to 5M bytes and the RD51 up to 10M bytes of data in fixed-length blocks on 5 1/4 inch (130mm) diameter rigid magnetic disks. They use what is called Winchester technology, and are contained in a sealed, non-removable enclosure. The Rainbow 100+ computer comes with a 10M byte RD51.

The controller logic is contained in a printed circuit board that occupies the extended communications option slot on the system module motherboard. It can control one ST506-interface compatible Winchester drive. (The ST506 interface is an industry standard interface.)

The MS-DOS Version 2.05 operating system contains BIOS drivers for the Winchester hard disk option, and the distribution diskette includes a backup and restore utility for use with the Winchester. Each Winchester drive comes with utilities for initializing and partitioning the disk according to your requirements. Partitioning assigns logical drive names (for example, E:, F:) to all or part of the drive, and initializes them for the operating system(s) to be used.

RAINBOW HARDWARE

The ROM of the Rainbow 100+ and 100B computers contains code so you can start the system from the Winchester instead of from a diskette. An upgrade kit is available that permits a Winchester disk to be field installed in the Rainbow computer. You cannot start the operating system from the Winchester disk if you upgraded your Rainbow 100A computer to include this hard disk. This is because its ROM does not contain the necessary code. The Rainbow can configure the Winchester disk for either the CP/M-86/80 operating system, the MS-DOS operating system, or both.

The MS-DOS Version 2.05 operating system contains drivers for programming the hard disk, however, so that files and data can be written and read using the hard disk system functions. Sophisticated users can us the primitive disk operations contained in the BIOS. Chapter 3 discusses these operations.

You cannot use this option if you have an extended communications option because both options mount (and use) the same motherboard connectors.

CHAPTER 2

FIRMWARE

2.1 INTRODUCTION

This chapter describes the basic functionality of the "console" as seen by an application program. It also describes the utility functions you can use with application programs. To use this chapter, you should be familiar with:

- The Rainbow system specification for your particular Rainbow computer
- The video controller chips

2.2 GENERAL

The programs in ROM (firmware) include:

- 1. Diagnostic routines
- 2. Terminal and console emulation routines
- 3. Set-Up routines,
- 4. 8088 and Z80A routines for loading the operating system

The manuals that come with your Rainbow computer describe diagnostic, Set-Up and startup routines.

The Rainbow 100+/100B Terminal Emulation Manual (contained in the Rainbow 100+/100B Technical Documentation Kit, (order number QV069-GZ)), which describes terminal mode.

The Rainbow 100+ and 100B computer's firmware includes an auto-start capability. This lets you start the operating system from one of the possible disk drives without selecting the drive from the main menu.

The following list details the functions that are available in the Rainbow 100+ and 100B ROMs which differ in the Rainbow 100 ROMs:

- The BELL character does not cause a hesitation the first time it "beeps," but hesitates only for subsequent BELL characters that occur too close together.
- A new Set-Up parameter permits a choice of either Caps-Lock or Shift-Lock mode for the "Lock" key.
- Every ROM set supports all keyboard variations. At startup, the firmware asks you to select a keyboard if the stored Set-Up parameters indicate that none had previously been selected. The NVM then stores the choice.
- A new Set-Up parameter allows you to invoke a National-Replacement-Character set for country-specific use.
- The "Compose" algorithm is implemented. Details are provided in the manuals that come with your Rainbow computer.
- The printer port's DTR line is monitored when in terminal mode. If it is not asserted, printing does not occur.
- Pressing Ctrl/2 through Ctrl/8 keys generates the proper control codes.
- Ctrl/<symbol> keys are no longer shift-dependent.
- The Escape sequences for "Erase-in-line" and "Display" now accept selective parameters instead of ignoring all after the first parameter.
- All Set-Up text and all error messages are displayed in languages appropriate to the keyboard type you selected and the language-cluster supported by the installed ROM set.
- Single "Shift-2" and "Shift-3" escape sequences apply only to the next following graphic character, and are not canceled by an intervening escape sequence or other non-graphic character.
- The "Print Screen" key generates an ESC [1 2 ~ in console mode.
- Ctrl/xx keys auto-repeat.
- Set-Up automatically determines the amount of installed RAM and displays it.

Details of the implementation of language-clustering, national-replacement characters, and so forth, are describes in the Rainbow 100+/100B System Specification (contained in the Rainbow 100+/100B Technical Documentation Kit, (order number QV069-GZ.))

2.3 UTILITY FUNCTIONS

The console mode of VT102 emulation acts like a VT102 console (although without modem control or local echo). As such, it must accept characters for display and control, and it must supply characters entered at the keyboard. These, plus several other utility functions, are provided for use by programs as follows:

- Send a character to the "console" for display or control (Console-Out)
- Obtain a character from the keyboard (Level-2 Console-In)
- Determine if a keyboard key has been depressed (Console-In Status)
- Level 1 (16 bit) Console-In
- Enable and disable the cursor
- Initialize interrupt vectors
- Return the clock rate
- Set and clear LEDs on the keyboard
- Send a string of data to the screen
- Initialize the Comm/Printer 8274 chip to Set-Up parameters
- Return "Raw" keyboard data

In addition to the above, the Rainbow 100+ and 100B computers have the following functions:

- Return ROM version number
- Relocate interrupt vectors
- Ring keyboard bell
- Get/Set character set usage as per NVM

For more information, refer to the system specification for your particular Rainbow computer.

You invoke all functions by setting up the desired function number and other parameters in 8088 registers then executing a software interrupt 18H. Place the desired function code in the DI register and parameter data in other registers according to the needs of the function. The application program should save all registers it needs before issuing the INT 18H, because the firmware routines save only CS:, SS:, and DS:.

2.4 FUNCTION OOH CONSOLE OUT

This function sends the character in the AL register to the "console" where it is displayed. It accepts and processes ASCII and 8-bit multi-national characters as a VT102, that is, escape sequences and control characters are executed

ENTRY

AL = Character to be displayed

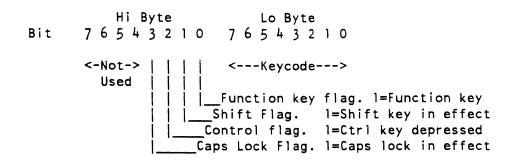
EXIT

None

2.5 CONSOLE IN, CONSOLE IN STATUS, AND LEVEL-1 CONSOLE IN

Pressing a keyboard key generates an Interrupt 26H in the 8088 processor. The keyboard's UART passes a one-byte character code identifying the particular key to the firmware's keyboard interrupt handler. This code is analyzed to determine whether an action must be performed (such as entering Set-Up, Lock mode), or whether the key represents a character to be passed to the application.

If a character is passed to an application, the keycode is stored as the low-byte of a two byte entry in a 30-byte first-in-first-out raw key buffer. The high-byte contains three flags that are set according to whether the Shift, Ctrl, and/or Lock keys were depressed or in effect. When the character is removed from the raw key buffer, the keycode is translated to it ASCII value, or, if a function key, to a function key number. If it is a function key, a fourth function key flag is also set. Figure 2-1 illustrates this.



NOTE

The function-key flag is added only when a Console-In function causes the character to be extracted from the raw key buffer.

Figure 2-1: Level-1 Character Format

Firmware functions can obtain input in either of two levels. Each 16-bit character code stored in the raw key buffer remains there until:

- A Level-1 Console-In
- A Level-1 raw key function call is made
- one of the Level-2 routines requires a character

The Level-2 Console-In routine also contains a buffer. Whenever you invoke the Level-2 Console-In function, the next character in its buffer, if any, is returned to the application. Usually, no character is present in the buffer, so the routine performs the equivalent of a Level-1 Console-In call to obtain a character from the raw key buffer.

If a character is available in the raw key buffer, it is removed. If it is an ASCII character, it is placed into the Level-2 buffer.

If the character represents a function key code, an escape sequence for the key is placed in the Level-2 buffer. That is why the Level-2 buffer is required.

DON'T MIX LEVEL-1 AND LEVEL-2 FUNCTIONS.

Notice that the Level-2 Console-In Status function also causes a character to be removed from the raw key buffer in the event the Level-2 buffer is empty and the raw key buffer is not empty. This can cause confusion, if you mix Level-1 and Level-2 Console-In and Console-In Status calls. If there were a character available in the Raw Key buffer when you performed a Level-2 Console Status function, you would be informed that a character was available. But if you then attempted to obtain that character using the Level-1 Console-In function, you would not get it, because it had been removed from the raw key buffer by the Level-2 Console Status function and transferred into the Level-2 buffer.

Programs should insure that the Level-2 buffer is empty (by repeatedly testing its status and obtaining and discarding any remaining characters) before using the Level-1 Console-In function.

The console emulation code can recognize and respond to escape sequences requesting status, cursor position, or other information. The responses are ASCII escape sequences which are placed directly into the Level-2 buffer.

2.6 FUNCTION O2H CONSOLE IN (LEVEL-2)

This function fetches a character from the keyboard buffer.

ENTRY

None

EXIT

AL = Character from the keyboard buffer (if one was available)

CL = 00H, No character available

= FFH, A character was returned in AL

NOTE

This function does NOT wait for a key to be depressed, if no character is available.

2.7 FUNCTION 04H CONSOLE IN STATUS (LEVEL-2)

This function determine whether a character is available in the Level-2 buffer.

ENTRY

None

EXIT

CL = 00H. No character available

= FFH. A character is available in the Level-2 buffer

NOTE

The character, if any, remains in the Level-2 buffer, but the function may cause the raw key buffer to become empty.

2.8 FUNCTION O6H CONSOLE IN (LEVEL-1)

This function fetches a 16-bit character from the Raw Key buffer.

ENTRY

None

EXIT

LAII

AX = 16-bit character from Raw Key buffer

CL = 00H, if no character is available

- = 01H, if any characters remain in the Level-2 buffer from a previous Level-2 call, whether or not characters are in the Raw Key buffer
- = FFH, if a character is available (returned in AX)

NOTE

OlH is returned to remove some of the complexity surrounding the mixing of Level-1 and Level-2 functions.

The two bytes representing the character in the raw key buffer are placed in the AX register after being analyzed and translated (see Figure 2-1 above).

For non-function keys, the function key flag is zero, and the low-byte contains the character code, either ASCII or 8-bit multinational, according to the keyboard type being used. The effect of Shift, Ctrl and Caps-Lock has already been taken into account for the character. For function keys, the Function key flag is a 1, and the low-byte contains a number that identifies which function key it is. Your Rainbow system specifications list these numbers.

2.9 FUNCTION OBH DISABLE CURSOR

2.10 FUNCTION OAH ENABLE CURSOR

These functions make the cursor visible (Enable), and invisible (Disable). They insure that the cursor control logic is not disturbed by Function 14H. They must be used in pairs, preceding and following the use of function 14H. They are "nested" functions; it takes as many enables as disables to redisplay the cursor. If enabled when already visible, a "ghost" can appear when the cursor is moved.

2.11 FUNCTION OCH INITIALIZE INTERRUPT VECTORS

Function OCH initializes the following interrupt vectors to point to their default firmware routines:

Type Number	Use
(Hex)	
02	NMI for RAM parity error
20	Vertical frequency refresh
22	Graphics controller option
23	Ext Comm Option DMA controller
25	Ext Comm Option 8274
26	Keyboard 8251A
2C	Time Tic from Vert Refresh

In addition, the extended communications option and the graphics option are reset to their disabled states. This function should not be used by application programs because it destroys the MS-DOS interrupt structure.

2.12 FUNCTION OFH RETURN CLOCK RATE

This function determines the current Set-Up clock rate.

ENTRY
---None

EXIT
---AL = 00H, to indicate 60Hz clock
= 01H, to indicate 50Hz clock

2.13 FUNCTION 10H SET KEYBOARD LEDS

2.14 FUNCTION 12H CLEAR KEYBOARD LEDS

These functions Set and Clear the keyboard light emitting diodes (LEDs). They do not cause any action that can be implied by the label of the LED being affected. Firmware normally maintains all but the "compose" LED in the proper state. (On Rainbow 100+ and 100B computers, the compose LED is also handled by the firmware.)

Register AL contains a bit pattern to set (Function 10H), or clear (Function 14H), the LEDs. A lin a bit indicates the LED to be set (by function 10H) or cleared (by function 12H). LEDs whose corresponding bits are zero remain unchanged. Figure 2-2 shows the keyboard LEDs:

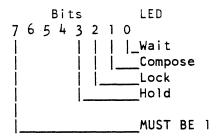


Figure 2-2: Keyboard LEDs

2.15 FUNCTION 14H SEND DATA TO SCREEN

This function sends more than one character at a time to the screen. Entire strings, up to the length of one line, can be sent, along with their attributes, in a single function call. Your Rainbow system specification describes the meaning of characters and attributes.

ENTRY

AX = 0000H, Characters and attributes

= 0001H, Attributes only

= 0002H, Characters only

BH = Column number (1 thru 80, or 1 thru 132)

BL = Line Number (1 thru 24)

The maximum column number is a function of the screen width (80/132) and line width $(\sin gle/double)$. Exceeding the line length destroys the screen image.

CX = Number of characters or attributes to transfer

You are responsible for ensuring that the number of characters does not exceed the end-of-line.

- DX = Offset to the start of Attributes relative to the user's DS
 register
- SI = Offset to the start of Characters relative to the user's DS register
- BP = Copy of user's DS register

Both DX and SI must be relative to the same value of DS.

2.16 FUNCTION 16H INITIALIZE COMMUNICATIONS/PRINTER 8274 TO SET-UP PARAMETERS

This function:

- Initializes the Communications/Printer 8274 to the Set-Up parameters
- Performs a channel reset for both channels A and B
- Sets the baud rates
- Set the number of bits per character
- Sets receive and transmit clock rates
- Enables receive and transmit for the Communication and Printer ports

All this is done according to the Set-Up parameters.

NOTE

When data/parity is 7M or 7S, the 8274 is actually set for 8N.

2.17 FUNCTION 18H RAW KEYBOARD DATA

This function is provided for diagnostic purposes.

Figure 2-1 (above) indicates that the Shift, Lock and Ctrl keys can only be read in conjunction with another key by looking at the flag bits in AH. The Set-Up key is not detectable by a program, but you can look at the screen to determine if you are in Set-Up mode. The Hold Screen key is not detectable by a program, but the Hold Screen LED is lit.

An application program can determine whether or not the Rainbow is in Set-Up mode.

ENTRY

None

EXIT

AX = Flag bits and keycode, as contained in the Raw Key buffer

CL = 0, no key available

= 1, key available

NOTE

The character is removed from the buffer

NOTE

The following four functions are not available on the Rainbow 100.

2.18 FUNCTION 1AH RETURN ROM VERSION NUMBER

This function returns an ASCII text string, identifying the version number of the ROMs, to the user's buffer. The format is:

MM.mm L

MM identifies the major version, mm the minor revision, both expressed in ASCII numerals. In general, minor revisions affect only one ROM, while the major version affects both ROMs. L is the language variation. mm and L are each followed by a Null (OOH) byte for delimiting purposes.

Programs or operating systems can use this function to determine the version and/or language of the ROMs. If you use this function on a Rainbow 100 computer, the buffer remains empty, identifying the unit as a Rainbow 100 computer. The Rainbow system specification contains details of version numbers and language IDs.

ENTRY

DI = 1AH

DX = Offset Address of 8 byte buffer

BP = Segment address of 8 byte buffer

EXIT

None

2.19 FUNCTION ICH RELOCATE INTERRUPT VECTORS

The Rainbow 100+ and 100B computers provide for changing interrupt codes 20H through 27H that are associated with the hardware generated interrupts to A0H through A7H under software control. When this is done it is also usually necessary to move the interrupt vectors associated with each interrupt to its new vector address. Function 1AH provides a convenient way to do this.

ENTRY

- AH = Interrupt code into whose vector the first source vector is relocated. Must be either AOH, OOH (defaults to 20H), or 20H.
- AL = Interrupt code whose vector is the first of group to be relocated. Must be either OOH (defaults to 20H), 20H, or AOH.

(If AX = 00H, default vectors are simply initialized)

CX = Number of vectors to be relocated. If zero, 16 is relocated
 (the default).

DI = 1CH Function Number

EXIT

CX = 0, to indicate a successful move

NOTE

This function is provided for use by the MS-DOS operating system and other operating systems, if required. Any use by application programs destroys the operating system's interrupt structure. The format of the calling process is general, but the only source and target addresses supported in the Version 05 ROM are 20H to/from AOH.

2.20 FUNCTION 1EH RING THE KEYBOARD BELL

This function rings the keyboard bell. It is a convenience for programs that use these firmware functions frequently.

2.21 FUNCTION 20H GET/SET DIGITAL 8/7 BIT CHARACTER CODE USAGE IN NVM

This function provides a method of reading or changing the NVM bit(s) that control character code usage. These can also be set or reset in Set-Up.

ENTRY

AH = 0, for SET

= 1, for GET

AL = 0, for DIGITAL-8 bit character set

= 1, for 7-bit national replacement character codes

EXIT (GET)

AL = 0, for DIGITAL-8

= 1, for 7-bit national replacement character codes

2.22 STACK CONSIDERATIONS

Both hardware and software interrupts place three words of addresses and flags onto whatever stack is currently being used, either the operating system's, your program's, or the firmware's.

In addition, the firmware's hardware interrupt handlers push their registers onto the current stack, and can be interrupted in turn by other hardware devices whose handlers push even more on the current stack. Therefore all application programs should provide a stack with at least 62 bytes more than are required by the application itself, which is the worst case possible.

Another stack-related precaution is that application handlers for hardware interrupts should NOT use either registers or the stack for passing parameters, because these might get changed by an intervening interrupt process. Nor should they use a private stack of their own. The firmware interrupt handlers test to determine whether their own stack is in place. If not, one is set up. This can cause problems. Consider the following situation. A user's hardware interrupt handler is in place for, say, the Communications port, and the handler sets up its own stack. If a character were to be received during execution of the latter part of the video refresh routine, (when interrupts are enabled), and the user's interrupt handler had set up its stack, then a keyboard interrupt was generated. The Firmware's keyboard routine would find a non-firmware stack in place, so would again set up the firmware's stack. In doing so it destroys the original video refresh routine's pushed environment, crashing the system. Software generated interrupt handlers do not have this problem since they can never interrupt a hardware handler.

2.23 SYSTEM PARAMETER DATA

The firmware maintains one 16-bit word of flags in Attribute RAM at location EFFFEH, which describes the system state. These flags are used primarily by the firmware, but can be used by application programs for special purposes. When this word is loaded into a register, its bits have the following meaning:

Table 2-1: System Parameter Data Flags

Bit	Flag	Meaning	y when:
	_	0	1
0	Emulator:	Console Mode	Terminal Mode
1	On/Off Line:	On-Line	Off-Line
2	Set-Up mode:	Normal	Set-Up mode
3	Hold Screen	Normal	Hold Scrn in effect
4	Scroll I/P:	Normal	Sm Scroll in process
5 6	Reserved		
6	Reserved		
* 7	Print Screen Key:	Not Pressed	Pressed
8	Comm Opt Present:	Present	Not present
9	RX50 Ctrl Bd.:	Present	Not present
10	Graphics Bd.:	Present	Not present
**1]	Memory Option:	Present	Not Present
12	Reserved		
13	Reserved		
14	Reserved		
15	Reserved		

^{*} The Print Screen Key flag bit is only in the Rainbow 100+ and 100B computers. The firmware sets this bit whenever you press the Print Screen Key, but does not reset it. If an application tests this bit more than once, it must reset the flag bit after each test.

2.24 LEVEL-1 CONSOLE-IN FUNCTION KEY CODES

The Level-1 (16-bit) Console-In function of the Rainbow computer's firmware returns a one in bit 0 of the most significant byte of the 16-bit value if the depressed key is a function key. In this case, the least significant byte is not the ASCII value of the character, but is an arbitrary code used to identify which key was depressed. The codes generated are shown in Table 2-2.

^{**} The memory option bit is only meaningful for Rainbow 100 computers equipped with a Rainbow 100 expansion RAM board.

Table 2-2: Level-1 Console-In Function Key Codes

Key	Level-1 Key Code (Hex)	Level-2 Esc Seq	Key L	.evel-1 Key Code (Hex)	Level-2 Esc Seq
Help Do Compose Print Scree F4 F6 F7 F8 F9 F10 F14 F17 F18 F19 F20 Find Insert Remove Select Prev Scree	9 3 + 57 9 B D F 11 13 15 17 19 1B 1D 1F 21 23	Esc [2 8 ~ Esc [2 9 ~ Esc [1 0 ~ Esc [1 2 ~ Esc [1 7 ~ Esc [1 7 ~ Esc [2 1 ~ Esc [2 1 ~ Esc [2 1 ~ Esc [2 1 ~ Esc [3 1 ~ Esc [3 3 4 ~ Esc [3 4 ~ Esc [1 3 ~ Esc [1 3 ~ Esc [1 3 ~ Esc [1 3 ~ Esc [2 6 ~ Esc [3 1 ~ Esc [3 2 ~ Esc [1 3 ~ Esc [1 4 ~ Esc [1 3 ~ Esc [1 4 ~ Esc [1 4 ~ Esc [1 5 ~ Esc	Down-Arrow Right-Arrow Left-Arrow Keypad 0 Keypad 1 Keypad 2 Keypad 3 Keypad 4 Keypad 5 Keypad 6 Keypad 7 Keypad 8 Keypad 9 Keypad Dash Keypad Comm Keypad Peri Keypad PF1 Keypad PF2 Keypad PF3	2BD # # # # # # # # # # # # # # # # # # #	Esc [D O 1 2 3 4 5 6 7 8 9 - , . (CR) Esc O P Esc O R Esc O R
Next Scree Up-Arrow	n 25 27 ^	Esc [6 ~ Esc [A	Keypad PF4 Break	62 ~ 65	Esc 0 S not ret.

Notes: * Compose key Esc sequence returned only by Rainbow 100

Sequences shown are for ANSII mode

⁺ Print Screen key Esc sequence returned only by Rainbow 100+ and 100B

[^] These depend on Cursor Key mode and ANSII/VT52 mode

[#] These depend on Keypad mode and ANSII/VT52 mode

[~] These depend on ANSII/VT52 mode

CHAPTER 3

MS-DOS

This chapter contains two parts:

- Part A describes the operation of the MS-DOS operating system (including its internal and external commands)
- Part B describes the principle parts of the MS-DOS operating system

Part A - Operation of MS-DOS

3.1 OVERVIEW

MS-DOS is an operating system for micro-computers using Intel 8086 and 8088 microprocessors. An operating system is a program that controls the overall operation of a computer. It provides an environment within the computer that enables you to easily perform operations such as:

- Starting a program
- Copying files
- Displaying a directory of files
- Simplifying programming

The MS-DOS operating system provides functions for commonly-used operations and I/O operations that are hardware-independent. Thus, you can write an application program to run under the MS-DOS operating system without requiring a detailed knowledge of the computer's hardware. Such a program runs on any computers that can run the MS-DOS operating system, as long as the computer has the appropriate peripherals. The "DOS" in MS-DOS stands for Disk Operating System, which means that MS-DOS provides all the logical operations necessary for writing and reading files to and from disk storage devices.

3.1.1 MS-DOS Operating System Versions

The MS-DOS operating system has evolved through a number of versions. Microsoft's Version 2.0 of the MS-DOS operating system is the basis for DIGITAL's Version 2.01 and Version 2.05.

The MS-DOS Version 2.01 operating system does not contain Winchester hard disk support; the MS-DOS Version 2.05 does. Other features were also added to the BIOS of Version 2.05, and several new utilities were added to its distribution diskette. Among these are:

- MDRIVE, which lets you use excess RAM to be used as a fast, logical disk drive
- RDCPM, which reads files from Rainbow CP/M-86/80 diskettes
- MEDIACHK, which permits the checking of media to be disabled, resulting in faster disk operations
- BACKUP, a utility for making and restoring backup copies of the Winchester hard disk

Both MS-DOS operating system Version 2.01 and Version 2.05 run on the Rainbow 100, 100+, and 100B, provided it has at least 128K bytes of RAM. Only Version 2.05 includes Winchester disk drivers.

Programs written to run under the MS-DOS Version 2.01 operating system run under the MS-DOS Version 2.05 operating system.

3.2 LOADING THE MS-DOS OPERATING SYSTEM

DIGITAL supplies the MS-DOS operating system on a diskette. Such a diskette is called a system diskette. A system diskette contains the system files:

- 10.SYS
- MSDOS.SYS
- COMMAND.COM

These system files contain the MS-DOS operating system code. The outermost tracks of all MS-DOS-formatted diskettes store a loader program, and three data areas (for keeping track of files stored on the diskette). The number of tracks required to store the loader program and data areas varies according to the capacity of a diskette track. Rainbow computer diskettes use about three and 1/5 tracks for this.

Files are stored on the remainder of the diskette. Non-system diskettes differ from system diskettes only in that they do not contain the three system files.

The Rainbow computer contains a small ROM-resident routine that reads the first sector of the outermost track (track 0) of the diskette in the startup (BOOT) drive whenever you turn on or reset the system. This sector contains a small program that reads a loader program from the remainder of the reserved tracks into RAM and starts it executing. The loader program reads two "hidden" system files (MSDOS.SYS, 10.SYS) plus the file COMMAND.COM, from the diskette into RAM, and starts execution. This process is known as starting or BOOTing the system. 10.SYS is loaded just above the 8088 interrupt vector space, and MSDOS.SYS, which contains interrupt handlers, service routines, buffers, control areas and installed device drivers, is loaded immediately above 10.SYS.

The file COMMAND.COM contains:

- A "resident" part
- A "transient" part

The resident part is loaded just above MSDOS.SYS and contains interrupt handlers for those interrupt types used by the MS-DOS operating system plus code to reload the transient part of the file.

The transient part contains code for all of the internal commands and the batch file processor. It is loaded in the highest addressed RAM available in the system. The transient part of COMMAND.COM:

- Issues the prompt
- Reads the command from the keyboard or batch file
- Executes the command itself (internal commands) or builds a command-line
- Loads a program file (external command)
- Starts execution.

The external command is loaded into the lowest available free memory. The MS-DOS operating system maintains a table of available memory, which keeps track of where each program is loaded.

Whenever an application or external command finishes execution and returns control to the MS-DOS operating system, the resident part of COMMAND.COM determines, by a check-sum process, whether the transient part needs to be reloaded. If necessary, it reloads the transient part before issuing its prompt.

Application programs are loaded in the area of memory between the two parts of COMMAND.COM.

Figure 3-1 shows an MS-DOS memory map:

	Top of Available RAM	
	COMMAND.COM (Transient part - May be overlaid by External Commands)	Displays prompt Command Interpreter Gets Cmd from Kbd/.BAT Builds command line Loads and Executes External Commands Executes Internal Cmds Processes Batch Files
	256 Byte User Stack for .COM programs	
	External Command(s) .COM or .EXE files	
	COMMAND.COM (Initialization part)	Contains Auto-Exec code Inits lowest Segment Address
	Overlaid by Ext Cmds)	
:	 COMMAND.COM (Resident part) 	Reload Transient part Int. 23H (Ctrl/C exit) Int. 24H (Fat. Err exit) Error handlers
	 	Buffers Control Areas Installed Device Drivers
	MSDOS.SYS	Interrupt Handlers Service Routines (Int 21H Functions)
	 	 MS-DOS Interface to Hardware
3ffH	 Interrupt Vectors	
000H		,

Figure 3-1: MS-DOS Memory Map

3.3 DISTRIBUTION DISKETTE CONTENTS

Table 3-1 lists the MS-DOS operating system files stored on the system diskettes.

Table 3-1: MS-DOS Operating System Files on System Diskette

File name	Function
COMMAND.COM	MS-DOS Command Processor
<pre>% MSDOS.SYS</pre>	MS-DOS Operating System
10.SYS	Hardware-Operating System Interface
EDLIN.COM	Line Editor
DEBUG.COM	Debugger
LINK.EXE	Linker
CHKDSK.COM	Checks diskettes
FORMAT.COM	Formats diskettes
SYS.COM	Transfers System
DISKCOPY.COM	Copies entire diskettes
RECOVER.COM	Recovers diskettes
PRINT.COM	Print Spooler
MORE.COM	Reviews Text
SORT.EXE	Sorts Text
FIND.EXE	Finds a string in a file(s) or std input
EXE2BIN.EXE	Converts .EXE files to .COM
FC.EXE	Compares files
	•
Non-Standard MS-DOS	Files
MACH EVE	Manna Assambles

MASM.EXE	Macro	Assembler			
CREF.EXE	Cross	Reference	Utility	for	MASM

Files only on Rainbow computer MS-DOS V2.05 Diskette

CONFIG.SYS MDRIVE.SYS	System configuration file (Req'd by MDRIVE) Virtual Memory diskette
MDRIVE.COM	II II II
BACKUP.EXE	Hard disk Backup & Restore Utility
MEDIACHK.EXE	Enables/Disables Media Checking function
RDCPM.EXE	Reads CP/M Formatted diskette files
README.HLP	Addenda and Errata to Rainbow computer documentation

^{*} Denotes "hidden" files

[%] Different levels of functionality in the two Rainbow computer MS-DOS versions

3.4 COMMANDS

The MS-DOS operating system uses the term "command" as both a verb and a noun. When MS-DOS issues a prompt such as "A>", it waits for you to type a command. This is the verb use. The code that performs the operation is called a command. It is the command processor part of the MS-DOS operating system that issues the prompt and invokes the program with the command name you typed.

NOTE

Any program stored as a diskette file is called an external command. This is because such a program is not internal to the operating system. It is a command, because its name must be given in response to the command prompt.

3.4.1 Rainbow MS-DOS Version 2.05 Special Command Caveats

The following section describes some caveats you should keep in mind as you use the MS-DOS operating system.

FORMAT

There are two formatting processes used with diskettes. The first, and most general, is physical formatting. This is the process whereby synchronizing data is placed on a newly manufactured diskette. This synchronizing data is required in order for the diskette controller circuits to read and write data to the diskette. The MS-DOS operating system, however, uses the term "formatting" to mean an entirely different process. This process initializes a blank, but physically-formatted diskette, with an area for storing the diskette directory and a table called the File Allocation Table (FAT).

Both types of formatting must be done for MS-DOS diskettes. DIGITAL's RX50 diskettes are already physically formatted, but diskettes that have been damaged by exposure to magnetic fields may have to be physically reformatted.

The MS-DOS Version 2.01 formatting program only initializes a blank diskette and creates the file allocation table (FAT). The MS-DOS Version 2.05 formatting program physically formats the diskette and then initializes it. (Use the FORMAT command's /I switch to format both ways).

The syntax for the FORMAT command is:

FORMAT d: [/S] [/I] [/V]

Also, the MS-DOS Version 2.05 FORMAT command does NOT format a diskette in the default drive. See the section entitled "External Commands" later in this chapter for further information.

The MS-DOS initialized Winchester disk partitions can also be MS-DOS formatted but not physically formatted.

If you specify a volume label when the FORMAT program asks for one, you cannot later make this diskette into a system diskette using the SYS command. If you do this, the operating system displays an error message to the effect that there is no space on the diskette for system files. To make a system diskette, either format the diskette with the "/S" command option (with or without a volume label) or format the diskette without a volume label, then use SYS.

The MS-DOS Version 2.05 operating system's FORMAT command also performs MS-DOS soft formatting on Winchester partitions previously created by the partitioning formatting utility distributed with the Winchester hardware option. However, the "/I" switch for physically formatting the Winchester disk is inoperative, and does nothing if invoked. Hard formatting a Winchester disk can only be done with the above mentioned partitioning formatting utility.

DISKCOPY

Diskettes receiving the files copied by the DISKCOPY command must have been previously "physically" formatted, not MS-DOS formatted. Diskettes that are specially formatted, such as the Lotus 1-2-3 system diskettes, should NOT be copied using the DISKCOPY command, because their special features can be corrupted.

Use a backslash character when using path names.

Pressing Ctrl/C stops a program if the running program notices the key sequence or if it uses system functions to test for the key sequence. When BREAK is on, all system functions include this test.

The command processor can read commands from the disk file as well as accepting them from the keyboard. This file must have an extension of .BAT. The file contains several MS-DOS command lines, each of which are executed by the command processor once you type the file name at the prompt.

The command processor has also been arranged to automatically search the BOOT disk for a batch file with the name "AUTOEXEC.BAT" immediately after the BOOT operation. If one is found, the file is executed as if you had entered the name AUTOEXEC at the prompt. However, the normal prompts for TIME and DATE are bypassed. If your system expects to use TIME or DATE during the session, the AUTOEXEC command file should include entries for the TIME and DATE commands.

You can use the following commands in command files:

- ECHO
- FOR
- GOTO
- IF
- PAUSE
- REM
- SHIFT

3.4.2 Generic Commands

This sections describes the external MS-DOS commands unique to the Rainbow computer.

BREAK

Most application programs that expect to use Ctr1/C use the system function provided for this purpose. It is not necessary, in this case, for the operator to first invoke the BREAK OFF command.

CHKDSK

Do NOT use the CHKDSK command with the /F option for an IBM diskette under Rainbow computer's MS-DOS Version 2.05 operating system. If the IBM diskette is double-sided, it can be destroyed.

COPY

You can use character I/O device names (such as PRN, AUX) as file names for the COPY command.

When copying from a serial I/O device (for example, from another computer by means of the AUX device (communications port) or the PRN device (printer port)), the operation does not work as expected. Instead, carriage return characters are removed from the data stream, characters are not echoed to the input device, and either a single Ctrl/Z or two successive carriage-return characters ends the COPY operation.

CTTY

The CTTY command changes the device used for interactive entry by the operator, but does not change any device names. For example, the command CTTY AUX causes all operator commands to be expected from the AUX port. If another terminal, such as a VT100, is connected to the communications port (AUX), it can be used for controlling the Rainbow computer. However, if, while doing this, you enter the command COPY file name CON, the file is displayed on the Rainbow computer's screen, not on your VT100. Similarly the command COPY CON file name places whatever is typed on the Rainbow computer's keyboard into the file, not the VT100's keyboard.

ECHO OFF

The ECHO OFF command is echoed.

EXE2BIN

Use this external command to convert .EXE files into .COM files. The

GOTO

A Batch-File label is defined by a leading colon (":"). The characters following the colon define a label. The characters following a GOTO must BE a label. They do not define the label.

PRINT

The file using the /C switch, and all file entries following it in the print queue, are removed from the print queue until you type a /P switch.

The first time you use the PRINT command after starting the MS-DOS operating system, it prompts you for the name of the list device, the default being PRN (also shown in the prompt). Just press the <Return> key, unless you wish to specify some other port.

RECOVER

RECOVER attempts to read and process data from a bad diskette. The data may be bad and can cause unpredictable, or incorrect, results. DIGITAL recommends, however, that you use this command only as a last resort to recover files from a bad diskette.

SORT

Use the SORT command to sort any text file. Be sure that each line has the same fixed-length fields. SORT can be pipelined for multiple-field sorts, such as for DATE and TIME. When performing multiple-field sorts, the secondary, or least significant sorts, must be performed first, and the primary, or most significant sorts, last.

Figure 3-2 shows a diskette DIRectory sorted by both date and time. The two lines giving the number of files and the volume label are included in the sort process. If the last sort had been from column 24 instead of 22, the line containing 40448 would have been somewhere within the sorted list. The command was invoked from the B drive because SORT creates temporary files on the default diskette and the diskette in the A drive was write protected. The SORT command was on the diskette in the A drive.

Directory	y of i	A:\		
LINK	EXE	42368	1-06-83	4:36p
FIND	EXE	5796	1-14-83	6:35r
MORE	COM	4364	1-14-83	6:42r
RECOVER	COM	2277	2-01-83	2:22r
EXE2BIN	EXE	1649	2-01-83	9:19a
FC	EXE	2553	2-01-83	9:36a
DEBUG	COM	11764	2-01-83	10:13a
DISKCOPY	COM	1419	2-14-83	4:39r
EDLIN	COM	4489	5-17-83	4:31p
SORT	EXE	1360	5-17-83	4:34r
CONFIG	SYS	20	9-17-83	6:27r
PRINT	COM	3335	9-18-83	11:58p
CHKDSK	COM	6330		
SYS	COM	850	9-26-83	5:04p
COMMAND	COM	15925	9-26-83	11:21a
MDRIVE	COM	873	9-27-83	10:38p
MDRIVE	SYS	953	9-27-83	11:46p
FORMAT	COM	19405	10-07-83	12:27p
MEDIACHK	EXE	1396	10-10-83	9:25a
MASM	EXE	77440	10-12-83	3:36p
CREF	EXE	13824	10-12-83	
BACKUP	EXE	72534	10-13-83	
RDCPM	EXE	9194	10-14-83	
README	HLP	18238	10-17-83	
	File(s)		448 bytes	
Volume i	n drive	e Ais M	S-DOS V2.0	5

Figure 3-2: Sorted Diskette Directory

3.4.3 MS-DOS Version 2.05 Additional Commands

RDCPM

The README.HLP file, stored on the MS-DOS Version 2.05 distribution diskette, describes the RDCPM command.

RDCPM DIR B: [filename.typ]

This command displays the directory of files on the CP/M-86/80 diskette in drive B: matching the file specification. No default is allowed for the drive. The default file specification is *.*.

RDCPM READ B:[filename.typ] [A:][path]

This command copies all files on the CP/M diskette in drive B: matching the file specification to the directory described by "path" on MS-DOS drive A:. No default is allowed for B:. The default file specification is *.*. The default drive is the current MS-DOS default drive. The default path is the current directory. If the path is specified, it must exist.

RDCPM TYPE B:[filename.typ]

This command types the contents of files on the CP/M diskette in drive B: matching the file specification. No default is allowed for B:. The default file specification is *.*.

MEDIACHK

The MS-DOS Version 2.05 operating system BIOS contains code that checks the type of disk media in a disk drive on every read and write operation to that drive. This checking takes time to execute, and on large files can slow down operation. MEDIACHK is a program that disables, or enables, this media checking function. When disabled, COPY operations proceed faster than when they are enabled.

MEDIACHK

This command displays the current setting

MEDIACHK ON

This command turns MEDIACHK on

MEDIACHK OFF

This command turns MEDIACHK off

3.5 DEVICE NAMES

File names beginning with CON, AUX, LST, PRN, and NUL are illegal because they are used by the MS-DOS operating system to identify the following character I/O devices:

AUX - Communications Port

AUX2 - Extended Communications Port

CON - Console Device (Keyboard/Video Monitor)

PRN - Printer Port

When you use these device names, you need only type the name, although the MS-DOS operating system will also accept them followed by a colon.

3.6 EDITING AND FUNCTION KEYS

The MS-DOS operating system lets you easily edit command lines you have entered at the keyboard. EDLIN, the MS-DOS line editor, provides these same editing functions. EDLIN is described in the users manuals that came with your MS-DOS operating system kit. This section describes some additional information on EDLIN.

When editing an existing file, EDLIN changes the extension of the original copy to .BAK. If two files have the same name, it is difficult to determine which file was the original of the .BAK file. Therefore, avoid giving files the same file name.

To insert an "ESC" character, use the <Interrupt> key, rather than the <ESC> key.

The syntax for (C) opy includes a comma between the line number and the "C". This is optional.

When in (1) nsert mode, pressing Ctrl/C returns to the EDLIN command prompt. A Ctrl/Z sometimes also exits the (1) nsert mode. The latter, however, usually must be followed by pressing the Return key. If the Ctrl/Z is immediately followed by other characters before the Return, the Ctrl/Z code is entered in the text, and (1) nsert is not stopped.

Part B - Principle Parts

3.7 MS-DOS PROGRAMS AND ROUTINES

The programs and routines that make up the MS-DOS operating system are divided into three major functional areas:

1. COMMAND.COM - Command Processor

The file COMMAND.COM is the command processor. It is the part of the MS-DOS operating system that accepts, interprets, and acts upon the commands you enter on the console keyboard. It recognizes and executes the "internal" commands, and loads "external commands" (programs) and starts execution. It is also called an MS-DOS "shell."

2. DOS - Disk Operating System

DOS, contained in the file MSDOS.SYS, performs logical I/O and disk operations, either in response to requests by the command processor's internal commands, or in response to your external commands. DOS also contains a number of useful "system calls," or functions. These functions provide disk and other I/O operations to programs through a standardized access method, making programs transportable from one system to another, and greatly simplifying their preparation.

3. BIOS - Basic Input - Output System

The Basic Input - Output System (BIOS) contains routines that control the hardware of the computer system. These are custom written for each system and are contained in the file IO.SYS. Included are routines that:

- Display a character on the logical console device
- Send a character to the logical printing device
- Read a record from a disk
- Write a record to a disk

The DOS routines and system functions access these BIOS routines to perform all 1/0

Generally, the command processor and DOS portions of every MS-DOS operating system are byte-for-byte, bit-for-bit identical, as provided by Microsoft, while the BIOS is customized for the system's particular hardware.

3.7.1 Command Processor

After you start the MS-DOS operating system, a prompt is displayed on your screen, such as "A>". This tells you that the MS-DOS operating system is ready to receive a command, and that default disk operations are to be performed on the A: drive. The command processor displays the prompt, and either executes, or loads and starts, the command.

3.7.2 Command Execution

When you type a command name at the keyboard, the command processor evaluates it. First, it determines whether it is an internal command. If it is an internal command, it is immediately executed. If it is not, it attempts to find a file having the specified name on the specified drive, or if no drive was specified, on the default drive. The command processor expects that the file type associated with the file name is to be either .COM, .EXE, or .BAT, and searches for them in that order. If a command file, with the name specified in the command line, cannot be found, the operating system displays the message "Invalid Command," and issues another prompt.

When the command processor finds the specified .COM or .EXE file, it reads the file into RAM. Files of both types are usually loaded into the lowest available memory.

3.7.3 Program Segment Prefix

After loading an external command file, the command processor initializes the first 256 bytes of the program segment with several values for use by the loaded program. These first 256 bytes are called the Program Segment Prefix (PSP). Therefore, all application programs must begin at offset 100H. One of the DOS System functions (EXEC) can also be invoked by an application program to load another program file, and it, too, has a PSP.

After initializing the Program Segment Prefix, the command processor starts program execution. Once the external command is started, the command processor is not used again until the program terminates.

The <u>Microsoft Operating System Programmer's Reference Manual</u> provides a detailed description of the PSP and its contents. (This manual is contained in this kit.)

3.8 DOS

3.8.1 General

The DOS (Disk Operating System) constitutes the heart of the MS-DOS operating system. It performs all logical disk I/O operations, calling on the BIOS routines to read and write. It also provides a large number of useful system functions, which application programs can call to do disk and other I/O operations.

3.8.2 Disk Drives

The MS-DOS operating system can access several disk drives. Each is identified by a single letter, "A," "B," "C,". Disk drives can be either actual physical drives, or "virtual" or "logical" drives, such as portions of one large capacity hard disk.

Hard disks can often store more data than can be accommodated by one MS-DOS drive name, so they are subdivided into several partitions.

Each partition can store a portion of the total disk space. Each partition is then given a "logical" drive name.

A logical drive can even be a part of RAM. The MS-DOS Version 2.05 operating system includes a utility called MDRIVE that sets up an area of RAM as a fast access memory disk. Files can be COPYed in and out of it, as with any other logical drive. MDRIVE is useful when running programs such as editors. Of course, it is necessary to COPY files into it before editing and to COPY any that have been modified back to a physical disk before turning off the power.

3.8.3 Disk Files

Central to the MS-DOS operating system is its method for storing and accessing data files on disks. The MS-DOS operating system was originally written to work with diskettes, and it still does. However, it also works with hard disks.

3.8.4 Disk Organization

The MS-DOS operating system reserves one or more of the first tracks of a disk for storing a loader program. These first track are called the "system tracks." You us all remaining tracks for storing files.

System diskettes contain the MS-DOS operating system in three files:

- 10.SYS,
- MS-DOS.SYS
- COMMAND.COM

The two .SYS files are hidden. They are not displayed by the DIR command.

The MS-DOS operating system writes data to disk files in "logical" records, which can be any length, but are usually 128 bytes each. Data files are written wherever there are empty locations, and there can be many locations containing portions of a file. Entries are made in a Directory and a File Allocation Table for each file on a disk that tell the MS-DOS operating system where the file is stored on the disk.

3.8.5 Clusters

Because even a small diskette can store a great many 128 byte records, it is impossible to have the directory and File Allocation Table keep track of each one. Instead, MS-DOS divides a disk's capacity into larger groups, called "clusters," that contain several logical records each.

The data storage area of each disk is addressed in terms of these clusters. They are numbered with zero at the beginning of the data portion of the disk and "spiral" inward with increasing cluster numbers through the remainder of the disk. It is the number of clusters containing a file's data that are stored in the disk's directory and File Allocation Table.

The first few clusters on each disk are reserved to store its directory, the number depending upon the capacity of the disk and cluster size. Disks can have capacities ranging from less than 100K bytes to many megabytes. Cluster sizes can be 512, 1K, 2K, 4K, or 8K bytes. The Rainbow computer's MS-DOS RX50 diskettes use a cluster size of 512 bytes, while the Winchester drives use a cluster size of either 2K or 4K bytes, depending on the size of the partition. The first two clusters on the Rainbow computer's RX50 diskettes are reserved for the Directory.

Each MS-DOS disk contains an ID byte to identify the disk type. MS-DOS Version 2.05 determines, through this byte, the characteristics of the disks to be read.

3.8.6 Directory

Figure 3-3 shows the contents of the first eight directory entries for MS-DOS Version 2.05 system diskette. These are stored in the first logical record of the first cluster. The left side of the figure shows the contents in hex notation, while the right side displays the same data in ASCII (non-displayable bytes are shown as dots).

Byte (Hex)	Contents	(Hex)	Contents (ASCII)
	494F202020202020 00000000000091BA	5359532700000000 4807020000300000	10 SYS'
	4D53444F53202020 000000000000864C	5359532700000000 3C071A00E6420000	MSDOS SYS'
	434F4D4D414E4420 000000000000AF5A	434F4D2000000000 3A073C00353E0000	COMMAND COM
	4D532D444F532056 0000000000006074	3230350800000000 4E070000000000000	MS-DOS V 205 `t N
	434F4E4649472020 000000000000000000000000000000000	5359532000000000 31075C0014000000	CONFIG SYS
00A0 00B0	4D44524956452020 000000000000D9BD	5359532000000000 3B075D00B9030000	MDRIVE SYS
	4445425547202020 0000000000000A551	434F4D2000000000 41065F00F42D0000	DEBUG COM Q A
	43484B44534B2020 0000000000001300	434F4D2000000000 33077600BA180000	CHKDSK COM

Figure 3-3: MS-DOS Directory Entries

3.8.7 Directory Fields

Every file on a disk, whether a hard disk or diskette, has an entry in either the root directory or a subordinate directory. Subordinate directories are actually files that have a directory entry in their "parent's" directory. Each directory entry contains thirty-two bytes.

As illustrated in Figure 3-4, the first byte tells MS-DOS if it is active or not, that is, whether it contains information about a file. If not, this byte is either OOH or E5H.

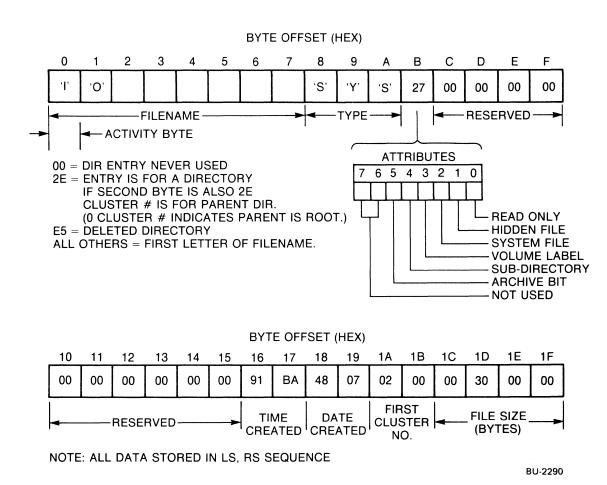


Figure 3-4: Format of a Directory Entry

When a disk or diskette is newly formatted, all directory entries have their first byte set to zero to indicate that they have never been used. When the operating system writes files to the disk or diskette, the first empty directory entries are always assigned first. When the operating system deletes a file, the first byte of its directory entry is changed to an E5H. This arrangement speeds up directory search operations.

If a directory entry is encountered whose first byte is zero, the remainder of the disk or diskette must be empty. This first, or activity byte, is 2EH if the file is a sub-directory. Further, if its first two bytes are 2EH, the first cluster field of the directory entry contains the cluster number where the "Parent's" Directory is stored. This value is zero if the parent is the root directory.

If a directory entry is active, the first byte contains the first letter of the file name.

For active directory entries:

Byte(s)	Contents
00-0A 0B	File name and file type Attribute flags
OC-15	Reserved by MS-DOS
16-17	Time that the file last written
18-19	Date that the file last written
1A-1B	First cluster where file is stored (contained in least three significant nibbles)
1C-1F	Number of bytes in the file

When a file is DELeted, all but the first byte of its directory entry remains on the disk. All file allocation table entries containing cluster numbers allocated to the deleted file are reset to zero. This releases the disk space for use by other files. This also generally makes it impossible to reclaim a deleted file, unless the file was small enough to be contained in the single cluster whose address is in the directory entry. Small files can sometimes be reclaimed because the number of the first cluster assigned to the file is still in the deleted directory entry. This is only possible if the directory entry was not reused for a new file by an intervening COPY or other file-creating activity.

3.8.8 File Attributes

You can assign MS-DOS files one or more of six possible attributes, such as "Read-Only" or "Hidden." Assign these attributes by setting the appropriate bits of the attribute byte in offset OBH of the directory entry, using System Function 43H. The meaning for each bit of OBH follows:

- Bit Meaning When Set
 - 7 Not used.
 - 6 Not used.
 - 5 Archive bit. Set "On" each time file is written or closed.
 - 4 File is a Sub-Directory.
 - 3 Directory Entry is Volume Label, not file. If this bit is set, all other bits are ignored.
 - 2 File is "System," does not display in Directory
 - 1 File is "Hidden," does not display in Directory
 - O File is Read-Only.

The archive bit is useful for programs that make backup copies of files. All file write and close operations set this bit to On (1). A backup, or "archiving" program, should reset it to Off (0) after successfully copying the file. In this way, the backup program can determine which of the files have been changed since the last backup copy was made, and limit its copying to these.

The BACKUP utility on the MS-DOS system diskette has an option that copies only files with the archive bit set.

3.8.9 File Allocation Table (FAT)

The MS-DOS operating system maintains a special File Allocation Table (FAT) on each disk. The FAT stores the cluster-numbers allocated to files on the disk. Generally, disks are arranged with at least two copies of this table as insurance against accidental loss. If this table cannot be read, the files stored on the disk cannot be located, even though their data may be intact.

The FAT stores each cluster number in three half-byte "nibbles." This allows a maximum of 4096 clusters per disk file, because twelve bits can uniquely identify only 4096 locations. The first byte of the FAT usually contains a disk identifier.

Rainbow Disk Identifiers

	V2.01	V2.05
Diskette	FC	FAH
Winchester Hard		F8H

Rainbow computer diskettes have been assigned FAH by DIGITAL. Microsoft has assigned bytes FC,FDH,FEH, and FFH for systems disk configurations.

The second and third bytes of the FAT are always FFH. They assist in identifying the FAT, and also insure that the first FAT cluster entry begins on a byte boundary. Remember that it takes three bytes to store two FAT entries.

The first two clusters of a Rainbow computer's RX50 diskette are always assigned to the directory itself, so the first cluster number assigned to a file is number 02, because cluster numbers begin with zero.

Figure 3-5 shows the first 128 bytes of the FAT from the same MS-DOS Version 2.05 system diskette whose directory entries are shown in Figure 3-3. Notice that the three-nibble cluster entries appear mixed up. This is the way they appear on the diskette when the FAT is examined using DEBUG. When the FAT is read by the MS-DOS operating system, however, they are interpreted as illustrated in Figure 3-6. When a diskette is initialized by the FORMAT program, all FAT entries are set to zero to indicate that the cluster location is not assigned to a file, and that it is suitable for storing data. Any clusters spanning diskette defects have their FAT entries set to FF7H. The MS-DOS operating system skips over such clusters automatically, allowing the remainder of the diskette to be used.

Byte (Hex)	Contents	(Hex)
0000 FAF	FFF0340000560	0007800009A0000B
0010 COC	000DE0000F0001	1120011340011560
0020 011	7800119F0FF1B	C0011DE0011F0002
0030 212	0022340022560	0227800229A0022B
0040 COC	22DE0022F0003	3120033340033560
0050 033	37800339A0033B	F0FF3DE0033F0004
0060 412	20044340044560	0447800449A0044B
0070 COC	044DE0044F0005	5120055340055560

Figure 3-5: Beginning of File Allocation Table (FAT)

Byte Contents (Hex)
(Hex)

0000 FAFFFF0340000560 0007800009A0000B As stored on diskette 0000 FAFFFF0030040050 0600700800900A00 As interpreted

0010 C0000DE0000F0001 1120011340011560 As stored on diskette

0010 B00C00D00E00F01001101201301401501 As interpreted

Figure 3-6: Interpreted File Allocation Table Entries

The first cluster number assigned to a file is contained in the file's directory entry. When this cluster becomes filled with data, the MS-DOS operating system scans the FAT table from the top until it finds a table entry of zero, indicating an empty cluster. It then places the number of this newly assigned cluster into the FAT location of the cluster pointed to by the directory entry. Similarly, whenever a new cluster is assigned to a file, its number is placed into the FAT location for the previous cluster. In this way a file is linked from cluster to cluster. The FAT entry for the last cluster of a file is set to any value from FF8H through FFFH.

The <u>Microsoft Operating System Programmer's Reference Manual</u> describes these file and directory operations in detail. (This manual is contained in this kit.)

3.8.10 Sector Translation

Some computer systems store data on a disk using an interlace, that is, sectors are written in a non-contiguous sequence around a track. This is because time is required for computing and getting ready for the next sector's read or write operation. If this next sector's data were to be written to the disk's next physical sector, the latter might have already passed under the read head, thereby requiring a full revolution of the disk to access it. To avoid this, the record is written at a physical sector, separated by one or more intervening sectors. This interlacing can be accomplished in either of two ways:

- One method is to format the disk with interlaced physical sector numbers, such as in the sequence 1,3,5,7,9,2,4,6,8,10.
- The other way is to format the physical sectors sequentially and do a logical translation of the desired sector number before performing the physical read or write operation.

- When this method is used, the logical sector number specified by the MS-DOS operating system to the disk-driver is translated to obtain the correct physical sector. Files are written on Rainbow computer RX50 diskettes using this latter scheme, that is, in the sequence of physical sectors 1,3,5,7,9,2,4,6,8,10.
- For the hard disk drive, the sequence of physical sectors is 1,8,15,6,13,4,11,2,9,0,7,14,5,12,3,10.

3.8.11 File Control Blocks

When a program needs to read or write data to a file, certain information about the file must be passed to the MS-DOS operating system so that it can "open" the file, (locate its directory, and access its data records.) Such information can be passed to the MS-DOS operating system using a File Control Block (FCB).

A FCB consists of a 36 or 43-byte area in memory. It can be located anywhere the program finds convenient. The command processor sets up one or two default "unopened" FCBs in the Program Segment Prefix when a file is loaded, for the convenience of the programmer.

FCBs are either "unopened" or "opened." An "unopened" FCB must be set up before an "open" system function is executed. It need only contain file ID information (drive, file name, and file type). All other bytes are usually set to zero. The offset relative to DS of a file's FCB is placed by the program into the DX register prior to invoking the desired MS-DOS disk function. The "open" system function locates the file's directory and copies the remaining FCB fields from it for use by subsequent file operations. The FCB thereby becomes an "opened" FCB.

After all file operations are complete, the application program should "CLOSE" the file. The CLOSE operation copies the newly updated data from the FCB back onto the disk's directory and FAT. Closing is not necessary if only Read operations were performed, because the directory and FAT were not changed. If the close operation were omitted after data had been written to the file, however, the file-size field of the directory is not updated and would not agree with the FAT.

3.8.12 Extended File Control Blocks

An extended FCB is required when creating or searching for directory entries of files having special attributes. The extension consists of a seven byte prefix appended to the FCB.

Byte(s) Contents

7 FFH
2-6 00H
1 Attributes (See above)

3.8.13 Wildcards

When the MS-DOS operating system searches the directory of a disk for a matching file name, it accepts as a match any character in those positions of the FCB's file name and file type that contain question marks ("?"). Asterisks are expanded to question marks throughout the remainder of the file name and/or file type fields by the command processor and by system function 29H. This "Wild Card" feature makes multiple file operations possible.

3.9 BIOS

The BIOS is that part of the MS-DOS operating system that directly controls the hardware of the system. Each I/O device is controlled by a "driver." The standard drivers are contained in the file IO.SYS, one of the two system files found in all MS-DOS implementations. Certain features and functions are required by the MS-DOS operating system, since the command processor and the MS-DOS operating system use the I/O drivers provided in IO.SYS for all I/O, and they must be able to properly interface to it.

3.9.1 Serial 1/0

3.9.1.1 Logical and Physical Device Assignments - The generic MS-DOS operating system communicates with three logical, serial I/O devices:

- Console (CON:)
- Auxiliary (AUX:)
- Print (PRN:)

The names in parentheses are the names the MS-DOS operating system recognizes as logical devices.

The Rainbow computer's MS-DOS implementations assign the keyboard/video monitor as the CON device, the communications port as the AUX device, and the printer port as the PRN device. In addition, they provide an additional device driver called AUX2 to the optional extended communications port.

The generic MS-DOS operating system also includes a CLOCK as one of its standard devices, for which there is a driver in IO.SYS.

The MS-DOS operating system lets you install additional, or alternative, device drivers. All that is necessary is for the desired driver to be on the system diskette as a file, and its name contained in a batch text file named CONFIG.SYS. When you start the operating system, it looks to see if the CONFIG.SYS file exists on the diskette, and if so, loads all drivers named. If these have the same name as a default driver, they preempt the latter. In this way special drivers can be easily loaded for new I/O operations.

The <u>Microsoft Operating System Programmer's Reference Manual</u> describes the rules for writing new device drivers and the operations available through the CONFIG.SYS file. (This manual is contained in this kit.)

3.9.1.2 Cross CPU Communications - The Z80A handles all diskette 1/0 in the Rainbow computer. Whenever the disk driver of MS-D0S is called, it calls the Z80A to perform the actual operation. The routines for these operations are contained in the Z80A's private RAM, having been placed there during the loading operation.

3.9.1.3 IBM Diskettes - The I/O drivers of MS-DOS Version 2.01 and Version 2.05 operating systems automatically identify and read RX50, IBM-8 and IBM-9 sectored single sided diskettes, and VT180 diskettes. MS-DOS files from any of the four types of single-sided diskette can be both read and written. However, data written to IBM diskettes is usually unreadable by the IBM PC, because the narrow head of the RX50 cannot fully erase the wide data tracks of these diskettes. Therefore, writing to IBM diskettes is not recommended. If it is absolutely necessary to write to an IBM diskette, try formatting a new diskette on the IBM drive, and COPY the Rainbow computer file onto it. Once you are able to read such a file on the IBM PC, COPY it to another IBM diskette for safekeeping.

3.9.1.4 MDRIVE - The Rainbow computer's MS-DOS Version 2.05 operating system includes a new device driver that can utilize some of available RAM as a logical disk drive. The system diskette contains a program called "MDRIVE.COM" You can use MDRIVE to set up this logical disk drive. Memory is assigned in groups of 64K bytes. MS-DOS can read and write files to this "disk" the same as it does to any real disk. The advantage is that files can be accessed quickly, because they are already in RAM.

You need at least 196K bytes of RAM to use MDRIVE, in which case you have one 64K block of RAM used as a logical drive. Files to be accessed must be COPYed into and out of the MDRIVE before and after use. MDRIVE stores the number of 64K blocks in MDRIVE.SYS, which is referenced in CONFIG.SYS through the DEVICE statement. (You must maintain CONFIG.SYS.) The drive name is the next available drive letter (E - I). For example, if you have 1 hard disk partition (drive E:), then MDRIVE is drive F:.

NOTE

You must always configure MDRIVE from the drive that you are going to start the operating system from.

3.10 SYSTEM FUNCTIONS

In addition to reading and writing disk files, the MS-DOS operating system provides some 80 different system functions for performing I/O and other tasks. Application programs can use and invoke these system functions. Generally, the system functions are invoked by loading parameters into registers and invoking interrupt 21H. Chapter 1 of the Microsoft MS-DOS O.S. Programmer's Reference Manual describes these system functions, and how to invoke them. (This manual is contained in this kit.)

NOTE

The MS-DOS operating system does not generally undertake to save any CPU registers when performing the system functions. Therefore, application programs must save the contents of those registers that need preserving BEFORE calling the function, and then restore them afterwards.

3.10.1 I/O Programming

The MS-DOS Version 2.05 operating system includes several additional system functions that:

- Provide enhanced serial 1/0 control
- Permit application programs to control the ports without the need for programming at the hardware level

You need to know how the MPSC is programmed as a prerequisite to understanding the serial 1/0 functions, as they all relate to MPSC programming. This information is described in:

- The Rainbow <u>CP/M-86/80 V1.0 Tech Doc Kit</u> (order number QV043-GZ). (This kit contains information about the Rainbow 100 computer.)
- The Rainbow MS-DOS V2.01 Technical Doc Kit (order number QV025-GZ). (This kit contains information about the Rainbow 100 computer.)
- The <u>Rainbow 100+/100B Technical Doc Kit</u> (order number QV069-GZ).
- The <u>Rainbow Extended Communication Option Programmer's</u>
 Reference Guide (order number AA-V172A-TV).

Chapter 5 provides details of these additional System Functions.

3.10.2 Interrupt Types

The 8088 interrupt types used by the MS-DOS operating system include several that are also used by the Rainbow computer's hardware and firmware. To resolve this conflict, hardware interrupts 20H through 27H are reassigned to other types. MS-DOS Version 2.01 includes a special routine in the interrupt handlers of those interrupts common to both the hardware and the MS-DOS operating system. This routine determines whether the interrupt was generated by the hardware or by software. If it determines that hardware generated the interrupt, it issues a software interrupt to the alternate interrupt type. (See Table 3-2.)

Table 3-2: Rainbow Computer MS-DOS Interrupt Vector Assignments

Function		Interru (He:			
	V2.01		V2.05		
	100	100+ 100B	100	100+ 100B	
8088 Processor					
Divide by zero	0	0	0	0	
Single step	1	1	1 .	1	
NMI used for Memory Parity error or 8087 error	2	2	2	2	
Break point instruction	3	3	3	3	
Overflow	4	4	4	4	
Hardware Interrupts					
Video refresh	40	40	40	AO	
Graphics option	42	42	42	A 2	
Ext. Comm. DMA	43	43	43	A3	
Comm/printer 7201	44	44	44	Α4	
Ext. Comm. int O (or Winchester)	45	45	45	A 5	
Keyboard	46	46	46	A6	
Int from Z80A	47	47	47	A7	
Software Interrupts					
50/60 hz timer	64	64	64	64	
Direct diskette I/O	65	65	65	65	
Firmware functions	18	18	18	18	
MS-DOS Interrupts					
program terminate	20	20	20	20	
function call	21	21	21	21	
terminate address	22/FO		22/F0		
Control-Break exit address	23/F1	23/F1	23/F1	23/F1	
fatal error address		24/F2			
absolute disk read	25	25	25	25	
absolute disk write	26	26	26	26	
terminate, stay resident	27	27	27	27	
Reserved by MS-DOS	28	28	28	28	
	3F	3F	3F	3F	

The system module used in the Rainbow 100+ and 100B computers includes circuitry that permits the hardware interrupt types to be set to either of two values, 20H through 27H, or A0H through A7H.

The firmware of the Rainbow 100+ and 100B computers includes a user callable function that moves several interrupt vectors from one location to another. The MS-DOS Version 2.05 operating system selects the alternative hardware interrupts and moves their vectors so that no conflict between hardware and software generated interrupts exists.

During system reset, the Rainbow computer's firmware initializes the interrupt vector for interrupt 28H (40 decimal) with the address of its built-in user callable functions. Interrupt 28H is also used by the operating system, however, so both MS-DOS Version 2.01 and Version 2.05 move the vector for the firmware functions to interrupt 18H.

Programs that invoke the firmware routines must use software interrupt 18H.

3.10.3 Modifying Interrupt Vectors

Interrupt 21H, and MS-DOS system functions 25H and 35H, which set and get interrupt vectors for MS-DOS interrupts 20H through 27H, do not work for all of the interrupts. Applications which need to change these interrupt vectors should do so as follows:

 MS-DOS Version 2.05 operating system running on Rainbow .100+ or 100B computers

Vectors for types 20H through 27H may be set and obtained by interrupt 21H, as well as by functions 25H and 35H.

 MS-DOS Version 2.05 operating system running on the Rainbow 100

Vectors for types 22H, 23H, and 24H may be set or obtained by interrupt 21H, as well as by functions 25H and 35H.

Vectors for the remaining types must be changed by placing the new address into specific memory locations. These must be in the usual double-word format. The memory locations are:

Type # (Hex)	Memory Location (Hex)
20	40:0294
21	40:0298
25	40:02A0
26	40:02A4
27	40:02A8

 MS-DOS Version 2.01 operating system running on any Rainbow Series Computer

Vectors for types 22H, 23H, and 24H may be set or obtained by interrupt 21H, as well as by functions 25H and 35H.

Vectors for the remaining types must be changed by placing the new address into specific memory locations. These must be in the usual double-word format. The memory locations are:

Type # (Hex)	Memory Location (Hex)
20	40:022B
21	40:022F
25	40:0233
26	40:0237
27	40:023B

CHAPTER 4

MS-DOS VERSION 2.05 EXTENDED DOS FUNCTIONS

4.1 INTRODUCTION

This chapter describes special functions provided by the MS-DOS Version 2.05 BIOS.

DOS System Function 44H - IOCTL calls most of these special features to permit device control operations.

Chapter 2 describes some additional functions of the firmware.

4.2 DEVICE DRIVERS

The MS-DOS operating system requires a BIOS routine for each device on a system. These are called device drivers. (The Microsoft MS-DOS Operating System Programmer's Reference Manual describes how these drivers routines are constructed, and the functions they provide.)

There are two types of device drivers:

- Serial I/O device drivers,
- Disk drivers.

The MS-DOS operating system calls serial devices "Character" devices and disk drivers "Block" drivers. The names reflect the amount of data handled during one call to the driver. One device driver can accommodate more than one actual device, as long as the devices are identical.

When the MS-DOS operating system reads or writes data to a disk file, or sends a character to a port, it calls on the driver to perform the actual read or write operation. The file IO.SYS contains drivers. Each driver provides the following functions:

- Initialization routines when you start the operating system
- Media checking function (disk drivers only)
- BIOS parameter block building (disk drivers only)
- Optional IOCTL I/O control routines
- Input and output routines
- Status routines
- Empty or "flush" routines

The optional IOCTL function is usually provided because some device characteristics may need to be changed by an application.

The MS-DOS operating system accesses these IOCTL functions through Function 44H. The MS-DOS Version 2.05 operating system has implemented several IOCTL functions for its serial I/O ports, diskettes, and Winchester hard disk. Some of the diskette and Winchester hard disk functions that would normally be accessed through Function 44H (IOCTL), have been implemented through software interrupt 65H.

Various functions provided for each device type in Version 2.05 are described below.

4.3 SERIAL I/O DEVICES

Functions Invoked by Function 44H - IOCTL

DOS Function 44H invokes all 25 additional serial I/O functions.

Most of these provide enhanced serial I/O port control and permit application programs to control these ports completely without the need for programming at the hardware level.

To understand these Serial I/O functions, you should be familiar with the operation of the Multi-Protocol Serial Controller chip (Nec 7201 or Intel 8274 MPSC), because many of the terms and functions relate to it.

The Serial I/O functions operate with three ports:

- Communication port (PORT A of the MPSC = AUX:)
- Printer port (PORT B of the MPSC = PRN:)
- Extended communications option port (PORT B of the option = AUX2)

Notice that the ports are not identical, nor are all the functions appropriate for all ports. Certain baud rates and modem signals are not available for printer PORT A of the Extended Communications option.

4.4 FUNCTIONS NOT PROVIDED

The following functions are not provided:

Full modem-control protocols
Synchronous protocols
Extended communication option's PORT A (RS 422)

4.4.1 PROTOCOLS

Asynchronous Support

Primitive routines are provided to support only asynchronous protocols.

Limited Modem Control

Limited Modem Control is provided so that application programs do not need to directly control the hardware. It also gives application programs a way to determine the state of the signals corresponding to each incoming and outgoing character. Characters received are assumed valid only when Receive Line Signal Detect (RLSD) is asserted, and the receive character is enabled. Characters are transmitted only when Clear To Send (CTS) is on.

Data Leads Only

Characters are transmitted and received regardless of the state of Data Terminal Ready (DTR) and Request To Send (RTS).

4.4.2 RECEIVE AUTO XON/XOFF

Auto XON/XOFF is supported at the driver level to handle receipt of XON/XOFF. If this feature is not selected, received XON/XOFF characters are stored as data in the circular buffer.

4.4.3 TRANSMIT AUTO XON/XOFF

This option monitors the receive character buffer, and automatically transmits XON and XOFF characters when the number of characters in the buffer exceeds certain limits. For this option to be effective, the application program must supply a receive character buffer of adequate size.

4.4.4 ALTERNATE CONTROL CHARACTERS FOR XON/XOFF PROTOCOL

The application program can specify control characters, other than DC1(11H) and DC3(13H), to be used for the XON/XOFF protocol.

4.4.5 MODEM SIGNAL CONTROL

The application program can set or clear the modem control signals without accessing the hardware.

4.4.6 BREAK TRANSMISSION AND DETECTION

Breaks can be sent for any duration, as determined by the application program. When a break is detected on a receiving device, a flag is set, which the application program can use as required.

4.4.7 PROGRAMMING OF PORTS

Ports can easily be programmed without directly accessing the hardware. The management of the interdependencies of ports is also provided, thereby relieving the application program of this difficult task. Application programs pass control and other information to the routines by a Communication Control Block (CCB), as described below.

4.4.8 RECEIVE CHARACTER AND TRANSMIT BUFFER EMPTY INTERRUPTS

Functions are provided that install and pass control to an application's interrupt handler upon:

- Receipt of a character
- Detecting that the transmitter buffer is empty

You can program the MPSC to generate interrupts under these two conditions. Any such interrupt handlers must also be disabled by the application program before termination. Functions are also provided to do this.

4.4.9 APPLICATION SUPPLIED BUFFER SIZE AND SPACE

An application can specify its own buffer size and location to be used for received characters. Each received character occupies two bytes in the buffer. One byte contains the character, the other byte contains information about the status of the port at the time the character was received (buffer overrun, parity error, break detection).

Applications should specify a buffer of at least 512 bytes if they use the auto transmit XON/XOFF feature. Such applications must be careful to restore the regular buffer before termination.

4.4.10 COMMUNICATION CONTROL BLOCK

MPSC control and setup information is passed between application programs and the functions through a Communication Control Block (CCB). Each cell of the CCB is a byte, except for the address and length of the optional user-specified buffer.

Notice that not all possible values are appropriate for all three ports, nor are all combinations of supported features allowed (for example, only Mark and Space are provided for 7 bits characters, and some values are included only for future support (for example, 7200 baud rate).

It is necessary to create or modify a CCB whenever a different configuration is required for a port. Each field of the CCB must be filled with the appropriate code for the desired parameter. Only those parameters that need to be changed must be filled. Those to remain unchanged must contain zero. When a passed value is not appropriate, the high order bit of the byte or word parameter is set to indicate an error.

NOTE

Though some validity checking on passed parameters is performed, it is not guaranteed. The application program must insure that the rules and interfaces specified below are carefully followed.

Table 4-1: Communications Control Block Format

Offset (Hex)	Offset Contents	Possible values
0	Device number	l=communications, 2=printer,
1	Modem Control	3=extended comm l=data only, 2=limited modem control
2	Stop bits	l=one, 2=one and one half, 3=two
3	Data bits **	5, 6, 7, 8, 7S, 7M
4	Parity **	Even, Odd, None
5	Receive baud	<pre>17 values (no 7200 baud) [printer cannot split baud rates]</pre>
6	Transmit baud	17 values (no 7200 baud)
7	XON character	If alt XON character to be used
8	XOFF character	If alt XOFF character to be used
9	RCV XON/XOFF	On or Off
A	XMT XON/XOFF	On or Off
В С	Alternate buffer size	If auto XMT XON/XOFF selected, user receive buffer is strongly suggested. An error is returned if the user buffer size is less than default.
D E F 	start address of buffer in offset,segment format.	This field must have entry if user buffer size is non-zero.

^{**} Data bits and parity should be specified together. In all fields of the CCB, zero means no change is requested

If an alternate receive buffer is selected, a non-zero value greater than 50H must be entered. Otherwise the start address for the buffer is ignored and the default buffer is used.

Table 4-2: Communications Control Block Parameter Values

Parameter Values:	(A	1 UX)		3 (AUX2)	
Modem Control: 0 = No Change 1 = Data Leads 2 = Limited Modem Control		1	1	1	
	CV:		2	2	
Stop Bits: 0 = No Change 1 = One 2 = one and one-half 3 = two (stop bits)		1	1	1	
Data Bits: 0 = No Change Data bits and Parity 1 = 5 should be specified 2 = 6 together (see note). 3 = 7 4 = 8 5 = 7+Space 6 = 7+Mark		5	4	5	
Parity: 0 = No change Data bits and Parity 1 = even should be specified 2 = odd together (see note). 3 = none		3	3	3	
Note: If parity is specified without da for a new configuration, an parity specified is odd or even a effect were 7M or 7S.	erro	or mi	ght b	e returne	d if the
XON character:	1	1H	11H	11H	
XOFF character:	1	3H	13H	1 3H	
Baud Rate - RCV (See table for values) - XMT		I ОН	OEH OEH	10H 10H	
Buffer Size: (16-bit words)	5	50Н	50H	50H	

Table 4-3: Baud Rates

<hex< th=""><th>Value</th><th>placed</th><th>in (</th><th>CCB></th></hex<>	Value	placed	in (CCB>
---	-------	--------	------	------

	Comm.	Ext. Comm.	Printer (*)
50	1	1	-
75	2	2	2
110	3	3	_
134.5	4	4	-
150	5	5	5
200	6	6	-
300	7	7	7
600	8	8	8
1200	9	9	9
1800	Α	Α	-
2000	В	В	-
2400	С	С	С
3600	D	D	-
4800	E	E	E D
7200	**	**	**
9600	10 D	10 D	10
19200	11	11	_

D Default Setting

4.5 CALLING PROCESS

Application programs access functions by means of MS-DOS system function 44H (IOCTL). Entry conditions follow:

NOTE

If the port number specified is invalid, the function might be ignored or an error condition might be returned. Application programs must determine whether a device is installed. This can be done by invoking the "Read Device Setup" function

^{*} Transmit and Receive Baud rates for the printer port must always be the same.

^{** 7200} Baud is not available for any port.

⁻ Not available

FUNCTION 44H - I/O CONTROL FOR DEVICES

ENTRY

AH = 44H

AL = Function code

Both 02H and 03H work for either READ or WRITE

BX = Handle number

O3H = AUX

04H = PRN

(AUX2 must be "Opened" via MS-DOS to get handle number)

DS:DX = IOCTL Packet Address

EXIT

Returned Data in IOCTL packet

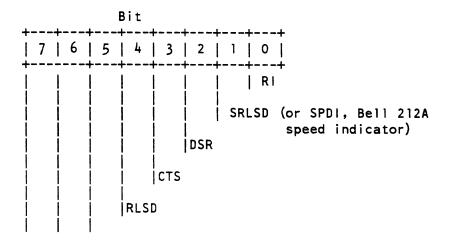
The format of the IOCTL packet is:

Offset	Label	Description
0	Function	Sub-function Number
1	Func_retC	Function return code:
	_	FFH = successful, 0 = unsuccessful
2	Character	Character Input or Output
3	Char_Stat	Character Status
4 to n	Buffer	Block Buffer

The application program must supply the device number in the applicable buffer field. Set-up parameters must be passed each time a different set-up is required. The entry to each parameter either contains zero, if the parameter is to remain unchanged, or a value from 1 to the largest applicable number for that parameter. On return to the caller, the high order bit of the appropriate byte is set, if the particular option requested is not supported. However, this is not guaranteed if a value is outside of the range specified.

MODEM SIGNALS - Modem signals that are available are listed below. Notice that unused bits are undefined. See Functions 13 and 14 below for full description of their use.

Incoming signals.

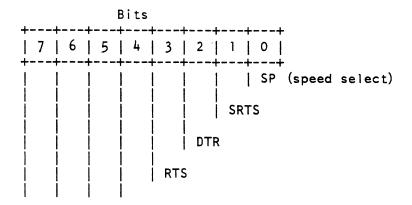


NOTE

SRLSD and SPDI signals are assigned to the same physical line. It is up to the application program to determine which one is connected. The remainder of the specification refers to this physical line as the signal SRLSD.

Also, control signals sometimes use connector pins other than those shown so that they can be monitored. The signals named in this document refer to the corresponding pin-outs and not the specific signals.

Outgoing signals that can be asserted.



4.6 LOGICAL ASSIGNMENT OF DEVICES, AND DEFAULT SETTINGS

The two ports on the motherboard are assigned as follows:

PORT A to AUX

PORT B to PRN

PORT B on the extended communications option is assigned to AUX2

If the port (device) number is invalid, the function might be ignored or return an error condition.

It is up to the application program to determine if the device is installed. Do this by invoking the "Read Device Setup Values" function.

FUNCTION O - PROGRAM DEVICE

This function reprograms the indicated device to the values specified in the CCB. It has no effect on the state of receiver enable.

ENTRY

(FUNCTION) = 0 (BUFFER) = CCB

EXIT

(FUNC RETC) = FFH, all functions programmed successfully

= 0 not all functions programmed successfully. The fields that cannot be programmed in the control block are marked with high order bit set.

NOTE

RCV interrupts MUST be disabled before invoking this function, if the start address of the User's Buffer is changed by this function. Use Function 5 to do this.

FUNCTION 1 - PROGRAM DEVICE TO DEFAULT NVM SETTINGS

This function reprograms the device to the default values specified in NVM. The optional communications port is set to the same values as the comm port. The receiver is enabled.

ENTRY
---(FUNCTION) = 1

EXIT
--None

NOTE

RCV interrupts MUST be disabled before invoking this function, if the start address of the User's Buffer is changed by this function. Use Function 5 to do this.

FUNCTION 2 - SET DEVICE TO USE DEFAULT BUFFER

This function reprograms the device to use the default buffer. This must be done before program termination. The default buffer is reinitialized before it is used again.

ENTRY
---(FUNCTION) = 2

EXIT
--None

NOTE

Interrupts MUST be disabled before invoking this function, if the start address of the User's Buffer is changed by this function.

FUNCTION 3 - READ DEVICE SET-UP VALUES

This function returns, in the CCB, information on the state of the device, that is, how it is programmed. The application must fill in the device number prior to the call to indicate which device is being asked for. All other fields must be set to zero.

FUNCTION 4 - ENABLE RECEIVER INTERRUPTS

This function enables the MPSC to receive characters. Characters received are placed in a circular buffer. In case of overruns, IAH is stored in the buffer. Each character is stored along with its status. Receiver-interrupts are normally enabled.

```
ENTRY
----
(FUNCTION) = 4

EXIT
---
None
```

NOTE

The function is ignored if the device number is invalid.

FUNCTION 5 - DISABLE RECEIVER INTERRUPTS

This function ignores received characters.

ENTRY
---(FUNCTION) = 5

EXIT

None

FUNCTION 6 - READ INPUT DEVICE STATUS

This function reads the status of the device's receive buffer.

FUNCTION 7 - READ INPUT CHARACTER, RETURN IF NONE AVAILABLE

This function reads the next character from the circular buffer. If none are available, return to the caller.

ENTRY
---(FUNCTION) = 7

EXIT
--(FUNC_RETC) = FF, character available = 0, char not available

(CHARACTER) = character, if available.

(CHAR_STAT) = character status

Bit 7 = 1 Break character
Bit 6 = 1 Framing Error
Bit 5 = 1 Overrun error
Bit 4 = 1 Parity error

FUNCTION 8 - GET CHARACTER, RETURN WHEN AVAILABLE

This function reads the next character from the circular buffer. If none are available, wait till one is available.

ENTRY
---(FUNCTION) = 8

EXIT
--(CHARACTER) = character

(CHAR_STAT) = status of character

Bit 7 = 1 Break character

Bit 6 = 1 Framing error

Bit 5 = 1 Overrun error

Bit 4 = 1 Parity error

NOTE

The function is ignored if the device number is invalid.

FUNCTION 9 - READ OUTPUT DEVICE STATUS

This function reads the status of the transmitter.

```
ENTRY
----
(FUNCTION) = 9

EXIT
---
(FUNC_RETC) = FF transmitter ready
```

= 0 transmitter not ready or device number invalid

NOTE

If auto XON/XOFF is active, the state of the output status can change between 2 function calls.

FUNCTION 10 - WRITE CHARACTER, RETURN IF UNABLE TO

This function puts the character in the transmitter. If it is unsuccessful, return to caller with appropriate error.

ENTRY

(FUNCTION) = 10

(CHARACTER) = character

EXIT

NOTE

If auto XON/XOFF is enabled, the drivers monitor and handle XON and XOFF characters.

FUNCTION 11 - PUT CHARACTER, RETURN WHEN SUCCESSFUL

This function puts a character into the transmit buffer, and does not return until the character is accepted.

ENTRY

(FUNCTION) = 11

(CHARACTER) = character

EXIT

None

NOTE

This function is ignored if the device number is invalid.

If auto XON/XOFF is enabled, the drivers monitor and handle XON and XOFF characters.

FUNCTION 12 - TRANSMIT CHARACTER IMMEDIATELY

This function puts a character into the transmit buffer immediately, ahead of other characters in the queue, if any. It ignores the state of the modem signals, that is, if limited modem control is used, it is ineffective for this character, and the characters ahead of it in the buffer. USE THIS FUNCTION ONLY IN EMERGENCY SITUATIONS.

```
ENTRY
----
(FUNCTION) = 12
(CHARACTER) = character
EXIT
----
None
```

NOTE

This function is ignored if the device number is invalid.

If auto XON/XOFF is enabled, the drivers monitor and handle XON and XOFF characters.

FUNCTION 13 - READ MODEM SIGNALS

This function fetches the incoming modem signals and the state of the transmit/receive flags. (Not applicable to the Printer port.)

```
ENTRY
----

(FUNCTION) = 13

EXIT
----

(CHARACTER) =

Bit 7 = 1, modem signals cannot be read
Bit(s) 5-6, Undefined
Bit 4 = 1, RLSD (comm port pin 8)
Bit 3 = 1, CTS (comm port pin 5)
Bit 2 = 1, DSR (comm port pin 6)
Bit 1 = 1, SRLSD (comm port pin 12)

or

SPDI, bell 212A speed indicator
Bit 0 = 1, RI on (comm port pin 22)
```

```
CHAR_STAT) =

Bit 7 = 1, XOFF transmitted
Bit 6 = 1, XOFF received
Bit 5 = 1, Receiver disabled
Bit (s) 1-4, Undefined
Bit 0 = 1, Transmitter empty
```

FUNCTION 14 - SET/RESET MODEM OUTPUT SIGNALS

This function changes the state of the outgoing modem signals to the desired mask value. Notice that this also clears unmasked signals. The printer port's MODEM signals cannot be set. Modem signals are positive when asserted.

```
ENTRY
----
(FUNCTION) = 14

(CHARACTER) = Modem signal mask

    Bit (s) 4-7, Ignored
    Bit 3 = 1, RTS set (connector pin 4)
    Bit 2 = 1, DTR set (connector pin 20)
    Bit 1 = 1, SRTS set (connector pin 19)
    Bit 0 = 1, SP set (connector pin 23)

EXIT
----
(FUNC_RETC) = 0 modem signals cannot be set
    = FF modem signals set
```

FUNCTION 15 - TRANSMIT BREAK

This function latches the transmit-data line to a space condition until "cease transmission of break" is requested. Invoke a CEASE TRANSMISSION OF BREAK function after the desired BREAK time has elapsed.

```
ENTRY
----
(FUNCTION) = 15

EXIT
---
None
```

NOTE

It is up to the application program to insure (if appropriate) the transmitter is empty.

Notice also that the duration of the space condition is completely up to the application program.

FUNCTION 16 - CEASE TRANSMISSION OF BREAK

This function returns the transmit-data line to a marking condition to terminate a previous "transmit break" command.

ENTRY
---(FUNCTION) = 16

EXIT
--None

FUNCTION 17 - SET RECEIVE CHARACTER INTERRUPT

This function permits a user's subroutine to be called upon each received-character interrupt. The call is made with a far call, and the user's routine must exit with a far return. The routine is called after the interrupt is serviced and has been reenabled. If a second interrupt occurs before this routine has been completed, it is not called the second time.

When called, the routine has the following conditions:

- 1. Interrupts off, and should remain off.
- 2. Stack is already changed to communications stack.
- 3. All register should be preserved by the called routine.
 Allow enough stack space for this.

The routine should not call any DOS functions.

FUNCTION 18 - CANCEL RECEIVE CHARACTER INTERRUPT

This function cancels any previous function 17 calls.

```
ENTRY
----
(FUNCTION) = 18

EXIT
---
None
```

FUNCTION 19 - TRANSMIT BUFFER BECOMING EMPTY INTERRUPT

This function permits a user's subroutine to be called upon each transmit-buffer-empty interrupt. The call is made with a far call, and the user's routine must exit with a far return. The routine is called after the interrupt is serviced and reenabled. If a second interrupt should occur before the routine has finished, it is not called the second time.

All registers should be preserved by the called routine, so allow enough stack space for this.

The routine should not call any DOS functions.

FUNCTION 20 - CANCEL TRANSMIT BUFFER BECOMING EMPTY INTERRUPT

This function cancels any previous Function 19 calls.

```
ENTRY
----
(FUNCTION) = 20

EXIT
---
None
```

FUNCTION 21 - SET USER DEFINED INTERRUPT SERVICE ROUTINE

This function redefines the interrupt service routine for a given device. When an interrupt occurs for the device, the application service routine is called instead of the normally defined interrupt service routine. Use this function only under unusual circumstances, such as for asynchronous protocols, and then only if you are familiar with the operation and programming requirements of the MPSC.

Register AX on entry to the call contains twice the value of MPSC register 2B. The latter contains one of eight vector values according to the source of the interrupt.

All registers should be preserved by the called routine. Allow enough stack space for this.

The routine should not call any DOS functions.

Also, the installed driver sends an EOI instruction to the MPSC so the called routine need not send this instruction.

```
ENTRY
----

BX = (Contents of MPSC Register 2B) * 2
(FUNCTION) = 21

(BUFFER) = 5-byte buffer:
    Byte 0 = Device number
    Bytes 1-4 = Offset and segment of routine to be called

EXIT
----
None
```

FUNCTION 22 - RESET DEVICE INTERRUPT VECTORS

This function resets the interrupt service routine back to the system-defined service routine.

```
ENTRY
----
(FUNCTION) = 22

EXIT
---
None
```

FUNCTION 23 - SET EXTERNAL STATUS CHANGE INTERRUPT

This function permits a user's subroutine to be called upon each external-status-change interrupt. The call is made with a far call, and the user's routine must exit with a far return.

FUNCTION 24 - CANCEL EXTERNAL STATUS CHANGE INTERRUPT

This function cancels any previous function 23 call.

```
ENTRY
----
(FUNCTION) = 24

EXIT
---
None
```

FUNCTION 25 - NON-DESTRUCTIVE CHARACTER READ, NO WAIT

This function returns the next available character in the device's ring-buffer, if any, but does not remove the character from the ring-buffer. This function allows the application to look ahead one input character.

4.7 DISK CONTROL FUNCTIONS

All diskette IOCTL-type functions are invoked using INT 65H. This is instead of the usual MS-DOS IOCTL function 44H with INT 21H, because using function 44H would cause drive motor problems. The functions are called differently under MS-DOS Version 2.01 and Version 2.05. Both calling processes are described below to emphasize their differences.

The Winchester disk IOCTL functions are called by the MS-DOS IOCTL function 44H using INT 21H. They can be invoked only with Version 2.05, since Version 2.01 does not support Winchester drives. Since Winchester media are not removable, the media check function need not be enabled or disabled, so functions 6-8 are not defined for the Winchester.

4.8 VERSION 2.01 DISKETTE FUNCTIONS

```
ENTRY
----
DS:BX = Pointer to control block:

Control Block Format

Offset

O CTLBLK DB Function code:
```

```
0 Write
                     1 Read
                     2 Media check
                  Drive code (0=A, 1=B, 2=C, 3=D)
1
            DB
2
            DB
                  Track number (first is 0)
   TRACK
3
            DB
                  Sector number (first is 1)
4
                  Sector count (must be 1)
            DB
5
    CURTRK DB
                  Current track (OFFH initially)
                  Format (must be 0, RX50 media only)
            DB
7-B
            DB
                  0, 0, 0, 0, 0
C-D
                  Offset of user buffer for one full sector
            DW
                  (512 bytes)
E-F
            DW -
                  Segment of user buffer (Buffer must all be in
                  shared memory ( 00800H to OFFFFH.)
```

EXIT

```
Carry NOT SET:
```

AL = 0 Function successful

Carry SET:

- AL = 0 Write protected
 - 2 Not ready, or busy
 - 4 CRC error
 - 6 Seek error
 - 7 Robin media
 - 10 Write fault
 - 11 Lost data

EXAMPLE

MOV BX, offset CTLBLK

INT 65H ;saves only DS and ES

MOV AH, OFFH ; in case of error,

:track is unknown

JC SETCURT

MOV AH, TRACK ; otherwise, track is as requested

SETCURT: MOV CURTRK, AH; set correctly before next call

4.9 VERSION 2.05 DISKETTE FUNCTIONS

```
ENTRY
____
DS:BX = Pointer to control block DIFFERENT from 2.01:
     Control Block Format
Offset.
 O FCTLBL DB Function code:
                 0 Read
                 1 Write
                 2 Write with verify
                 3 Format track
                 4
                    Media check
                 5 Verify disk
                 6 Enable media check function
                 7 Disable media check function
                 8 Status of media check function
  1
      DB Drive code (0=A, 1=B, 2=C, 3=D,
                     OFFH=physical unit)
       DB Sector # (one-based, skewed if track > 1)
       DB Physical unit (only if drive code = OFFH)
  3
            High nibble = unit number
               (0 to 3 for diskettes A to D)
            Low nibble = head number
               (normally 0, for first side)
 4-5 DW Track number (first is 0)
 6-7 DW Sector count (always 1, ignored)
 8-9 DW Offset of user buffer (512 bytes)
A-B
      DW Segment of user buffer (Buffer may be anywhere in
         memory, except "format track" must be in absolute
         OO8OOH-OFFFFH.)
EXIT
Carry NOT SET:
   For functions 0 - 5:
      AL = 0 Function successful
   For functions 6 - 8:
```

AL = 0 Media check function enabled

1 Media check function disabled

```
Carry SET (functions 0 - 5 only):
          AL = 0 Write protected
                2 Not ready, or busy
               4
                  CRC error
               6 Seek error
               7 Robin media
               10 Write fault
               11 Lost data
     EXAMPLE
     MOV
          BX, offset FCTLBL
     INT
                   ;Functions 0 - 5 save BX, DS, ES
                   ;Functions 6 - 8 save all except AX
4.10 VERSION 2.05 WINCHESTER
     ENTRY
     ____
     AH = 44H
     AL = Function code (must be 4 or 5)
     BL = Drive (0=default, 5=E, 6=F, 7=G, 8=H)
     DS:DX = Pointer to control block DIFFERENT from diskette
          Control Block Format
     Offset
       O DB Function code:
              0 Read
               1 Write
               2 Write with verify
               3 Format track
               4 Media check
               5 Verify disk (simply returns AL=5)
       1 DB Drive code
               (4=E, 5=F, 6=G, 7=H, 0FFH=physical unit)
                Notice difference from drive value in BL
       2 DB
             Sector number
               (first is 1, skewed if track > 1)
       3 DB Physical unit ( only if drive code = OFFH)
                 High nibble = unit
                    (0, has only one physical unit)
                 Low nibble = head or surface
                    (0 to 3, unit has 4 heads)
                 Notice that track = cylinder and is not offset, and
                 that sector is not skewed, when performing physical
```

unit 1/0.

Offset

```
4-5 DW Track number
            (first is 0, offset from partition start)
6-7 DW
       Count of sectors (functions 0-2)
           or tracks (function 3)
8-9 DW
        Offset of user buffer for either:
           sector data (512 bytes each for read or write), or
            track data (for formatting, 32 bytes per track, 2
           each for 16 sectors, first is 00H for normal or 80H
            for bad, second is logical sector number to write)
A-B DW
        Segment of user buffer (may be anywhere in memory)
 C DB Returned status from driver
  D DB Error type bits (= AH, valid if status is 1, 3, or ODH)
EXIT
AL = Returned status:
    00 Function successful
    Ol Error (during read, write, or format)
     03 Write fault at drive (or 8-second timeout)
     05 Invalid drive in BL or in WCTLBL
    ODH Write verify error (during reread after write)
    OFH Soft read error (retries were done and worked)
   OFFH Driver not called (only way to tell)
AH = Error type bits (for functions 0-3 only):
     Olh Data Address Mark not found (during read)
     02H Track 0 error (cannot restore to track 0)
     04H Aborted (new command issued while busy)
     10H ID not found (or track number too large)
     40H CRC error in data field (after 8 retries)
     80H Bad block detect (sector is marked as bad)
EXAMPLE
MOV
      AX,4404H ; IOCTL function for disk
      BL,5 ;for drive E, first Winchester partition
MOV
MOV
      DX, offset WCTLBL
INT
      21H
               ;saves DS, ES, SI, DI, BP
```

CHAPTER 5

MS-DOS PROGRAMMING NOTES

- 5.1 DIRECT ROM CALLS
- 5.1.1 IBM ROM Calls (INT 10)
 - Set mode
 - Set cursor type
 - Set cursor position
 - Read cursor position
 - Select active display page
 - Scroll active page up
 - Scroll active page down
 - Read attribute and character at current cursor position
 - Write attribute and character at current cursor position
 - Write character at current position
- 5.1.2 Rainbow ROM Calls (INT 18H under the MS-DOS operating system)
 - Write character at current position
 - Disable/enable cursor o Send data to screen

5.1.3 Set Mode

IBM computer

40x25 black _white 80x25 black _white

RAINBOW computer

ANSI Escape Sequences

ESC#5 - 80 x 24 ESC#6 - 40 x 24

5.1.4 Set/Read Cursor position

RAINBOW computer

ANSI Escape Sequences

ESC[#;#H Set Cursor position ESC[#;#R Read Cursor position

The # refers to a numeric value.

5.1.5 Paging

RAINBOW computer

The Rainbow computer does not support a paging scheme for the video. If paging is required, you must write your own support routine(s).

5.1.6 Read Attribute and Character

RAINBOW computer

The Rainbow computer does not support reading attribute and character. You can write a routine to read the cursor position, calculate the memory location, then return the character and attribute set.

5.1.7 Write Character at the Current Cursor Position

	IBM Computer	Rainbow Computer	
Function	AH = 10	DI = 0	
Character	AL	AL	
Page	ВН	N/A	
# Repeat Char	CX	N/A	

5.1.8 Write Character and Attribute

RAINBOW computer

A single system call can write multiple characters/attributes (see below).

Rainbow Fast Access to Video Memory by ROM (INT 18H)

DI = 14

AX = Transfer type

0 = Character _Attribute

l = Attribute only

2 = Character only

BL = Line number (1-24)

BH = Column number (1-132)

CX = Number of Char/Attr to transfer

DX = Start address of Attributes

SI = Start address of Characters

BP = Segment code

Table 5-1: Memory Map for Video

IBM computer

B0000 to B3FFF 16K Character/Attribute RAM

Even bytes are Character RAM Odd bytes are Attribute RAM

RAINBOW computer

EE000 to EEFFF 4K Character RAM EF000 to EFFFF 4K Attribute RAM

Rainbow Video Memory Map - RAM Addresses

Direct video memory access routines start by issuing escape sequence ESC[?31 with a generic INT 21H call. After receiving this escape sequence, the video memory appears as follows for an 80 column display:

(The addresses are for the first line of the display.)

EE00:12 - EE00:61 (80 characters) :62 Termination Char (FF) :63-64 Address of next line

Subsequent line addresses are listed below:

LINE 99H LINE 3 120H LINE 4 1A7H LINE 5 22EH LINE 6 2B5H LINE 7 33CH LINE 8 3C3H LINE 9 44AH LINE 10 4D1H LINE 11 558H 12 5DFH LINE LINE 13 666H LINE 14 6EDH LINE 15 774H

```
LINE 16 7FBH
LINE 17 882H
LINE 18 909H
LINE 19 990H
LINE 20 A17H
LINE 21 A9EH
LINE 22 B25H
LINE 23 BACH
LINE 24 C33H
LINE 25 CBAH (Used when Scrolling)
```

Table 5-2: Character Attributes

	IBM Computer	RAINBOW Computer
REVERSE VIDEO	YES	YES
BLINK	YES	YES
UNDERSCORE	YES	YES
BOLD	YES	YES

Table 5-3: IBM Attribute Byte Bit Mask

	7	6	5	4		3	2 1 0		
В١	ink	r	g	b	1	ntensity	r g b		
Foreground							Background		
Ва	Background Foreground				reg	round	Function		
			-						
r	g	b		r	g	b			
-	_	_		-	-	-			
0	0	0		0	0	0	Non display		
0	0	0		0	0	1	Underline		
0	0	0		1	1	1	White character		
1	1	1		0	0	0	Reverse video		

Table 5-4: Rainbow Attribute Byte Bit Mask

Bit 3 = Underscore

0 = Underscore

1 = No underscore

Bit 2 = Blink

0 = Blink

1 = No blink

Bit 1 = Bold

0 = Bold

1 = No bold

Bit 0 = Reverse Video

0 = Normal

1 = Reverse video

5.2 KEYBOARD INTERFACE (IBM COMPUTER)

5.2.1 Generic MS-DOS calls (INT 21H)

- Function 1 (Read Keyboard and Echo)
- Function 6 (Direct Keyboard 1/0)
- Function 7 (Direct Keyboard Input)
- Function 8 (Read Keyboard)
- Function 10 (Buffer Keyboard Input)
- Function 11 (Check Keyboard Input)
- Function 12 (Flush Buffer, Read Keyboard)

NOTE

These generic calls work the same in both IBM and Rainbow systems

5.3 DIRECT ROM CALLS

5.3.1 IBM ROM Calls (INT 16)

- Keyboard Input (AH=0, character returned in AL)
- Keyboard Status (AH=1, Z-flag set = no character, character in AX) (Notice that this allows non-destructive read.)
- Return the current shift status

5.3.2 Rainbow ROM Calls (INT 40)

- Keyboard Input (DI=2, character returned in AL)
- Keyboard Status (DI=4, CL=0 no char / CL=FFH, AL=char)
- Level 1 Keyboard Input ("raw" key-code)

5.4 IBM SHIFT KEY STATUS

The Shift status is the second charter of a keyboard. Function keys and other keys generate two-byte inputs.

The Rainbow computer generates an escape sequence when you type special keys (for example, keypad) and function keys. These escape sequences must then be parsed by the application.

5.5 DISK INTERFACE

5.5.1 Generic MS-DOS Calls

All generic disk calls are equivalent in the two systems.

5.6 DIRECT HARDWARE/FIRMWARE/BIOS CALLS

5.6.1 IBM ROM Calls (INT 13)

- Reset disk system
- Read status
- Read disk sector
- Write disk sector
- Verify disk sector
- Format disk

5.6.2 Rainbow Floppy Diskette Calls (INT 65) and Winchester Calls (IOCTL 44)

See Chapter Four for details.

- Read sector
- Write sector
- Write and verify
- Format
- Media check
- Verify media (bad block check)

5.7 SAMPLE PROGRAM

The following sample program shows the use of MS-DOS system function 4BH (EXEC). "Shelling" (a term used by the UNIX and XENIX operating systems) means to invoke the operating system's command processor to load and start a program. The MS-DOS system function 4B is called "EXEC". The sample program is written for assembly by Microsoft's MASM.

TITLE: Exec call test

PAGE: 60,132

Notice that no stack segment is defined. This is because the program is linked as a .COM file that, when loaded, has all segment registers set to the same value. Notice that the program is ORGed at 100H. This is necessary for

```
SEGMENT 'codesg'
code
       ASSUME CS:code, DS:code, ES:code
       ORG
               0100H
                                   ; Program entry point
exectest:
               SP,OFFSET stack ; Set up local stack
       MOV
       MOV
               AH.09H
                                  ; Print "Before shell"
       MOV
               DX,OFFSET mess1
       INT
               21H
                                   ;
       MOV
               BX,OFFSET lastloc+15; BX := program size in
       MOV
               CX.4
                      ; paragraphs
               BX,CL
       SHR
       VOM
               AX,4AOOH
                                 ; Deallocate unused memory
               21H
       INT
                                 ; Get environment address
       MOV
               SI,2CH
               AX, CS: [S1]
       MOV
                                 ; from PSP+2CH
               WORD PTR parmblk, AX;
       MOV
       MOV
               AX.CS
                                  ; Set segment registers
               WORD PTR parm4, AX ; in parameter block
       MOV
       MOV
               WORD PTR parm8,AX
       MOV
               WORD PTR parmC, AX
       MOV
               DX,OFFSET filenam ; Set up exec call
       MOV
               BX,OFFSET parmblk
       MOV
               AX,4BOOH
               DS
       PUSH
                                  ; Save machine state
               ES
       PUSH
       MOV
               CS:savess,SS
       MOV
               CS:savesp,SP
       INT
               21H
                                 ; Shell to DOS
               SP,CS:savesp
       MOV
                                 ; Restore machine state
       MOV
              SS,CS:savess
       POP
               ES
                                   ;
       POP
               DS
```

MOV AH,09H ; Print "After shell" MOV DX,OFFSET mess2 INT 21H INT 20H ; Terminate program savess DW ; Holders for SS:SP DW ? savesp 'Before shell', ODH, OAH, '\$' DB messl mess2 DB 'After shell',ODH,OAH,'\$' filenam DB 'A:\COMMAND.COM',O ; Assume COMMAND.COM on A: ; Parameter block parmblk DW 00 OFFSET comline DW parm4 DW 00 DW 5CH parm8 DW 00 DW 6сн parmC DW 00 09H, '/C dir A:', ODH ; Command line comline DB PAGE DB 128 DUP (?); Stack stack LABEL BYTE ; lastloc LABEL BYTE ; End of program code **ENDS** END exectest

5.8 PROGRAMMING HINTS

Keep the following programming hints in mind when using the MS-DOS Version 2.05 operating system:

- Serial I/O Function 14 "Set/Clear Modem Signals" does not work.
- 2. Serial I/O Function 21 "Program Device Interrupt Vector" does not work.
- 3. Data received from the communications port and printer port when the character format has been set to 7M (7 bits, 8th bit always Mark) always have the eighth bit set. The MPSC does not strip off this bit, so the application must strip it.

- 4. The BIOS reads IBM single-sided, 8- or 9-sectored diskettes. However, it does NOT check to determine if a diskette in the specified drive is double sided. As a result, the top side (which contains the directory and FAT) is read if you try to read a double-sided IBM diskette. This falsely indicates that all of the data can be accessed. Copy operations do not work, however, because files are stored on both sides of the double-sided diskettes, and the data on the lower side cannot be read.
- 5. The MS-DOS Version 2.05 system diskette contains Z80A code used for starting (B00Ting) the operating system. The computer stops if you try to hard format a write-protected diskette using the /I switch.
- 6. The FORMAT program on the MS-DOS Version 2.05 distribution diskette contains an error that fails to place an error code in the FAT if the last sector on the last track of a diskette contained a hard error. This is of minimal significance, since diskettes having errors in sector 10 of track 79 are probably extremely rare.

APPENDIX A

RAINBOW ON DISK STRUCTURES

This appendix defines the layout of all software related disk structures. It also defines the contents of all fixed data blocks necessary for supporting various system configurations and applications.

A.1 INTRODUCTION

The on-disk structure accommodates disks having larger capacitties, and removable media.

A.2 GOALS

- 1. Provide a disk structure suitable for supporting multiple operating systems, as well as multiple logical disks.
- 2. Be able to support removable (mountable) media.
- 3. Support media interchange between Rainbow and non-Rainbow computers.

A.3 CONVENTIONS

A.3.1 Disk Address Space

Internal pointers to disk addresses are in the form:

```
Disk Addresses
+---+--+
! track !sect! (3-bytes) ! length ! (opt. 2-bytes)
+---+---+
```

The intersection of a cylinder and surface is a track. Each track has a unique number assigned to it. The numbers are assigned sequentially starting with track 0 located at cylinder 0 - surface 0 and proceeding downward within the cylinder until the last surface is reached. The next track is the top surface of the next cylinder. The formula for computing track number from cylinder and surface numbers is:

```
track = surface + cylinder x (# of surfaces on disk)
```

For example, the track located on surface 2 of cylinder 3 on a 4-surface disk would be track 14.

Disk addresses can be viewed as monotonically increasing block numbers starting at 1 (Cylinder 0, Surface 0, Sector 1) and continuing until the end of the last usable cylinder-surface-sector. All sectors on a track are used sequentially until the last sector of that track. The next block is then the first sector of the next track. For example (for RD51):

```
Block 1 -- Track O (Cylinder O, Surface O), Sector 1
Block 16 -- Track O (Cylinder O, Surface O), Sector 16
Block 17 -- Track 1 (Cylinder O, Surface 1), Sector 1
Block 64 -- Track 3 (Cylinder O, Surface 3), Sector 16
Block 65 -- Track 4 (Cylinder 1, Surface O), Sector 1
```

A.3.2 Checksums

Each reserved area of the on-disk structure contains a 16-bit checksum value in bytes 4 and 5. This value is computed by zeroing the checksum bytes, then performing a cumulative modulo-16 addition of the reserved area data. The data is treated as 16-bit quantities starting with bytes 0 and 1. The resulting sum is complemented and a value of 1 is added to it. This number becomes the checksum. When a reserved area is read, the checksum is verified by performing the same modulo-16 addition, including the checksum quantity. The resulting sum should be zero.

A.4 RESERVED AREAS

Tracks 0 and 1 contain information used to configure and maintain system areas on the hard disk. This data is accessed by various utilities that need to know the extent of disk partitions and bad regions. This data is duplicated on tracks 3 and 4. This area is not usable by any operating system for their respective file structures. The information stored on these tracks is:

- PRE-BOOT This block is to be read by new firmware. It contains a small program that reads and starts the primary boot program.
- HOM This block contains the volume ID of the disk, a description of the physical disk layout, and pointers to the other disk system areas.
- BAT (Bad Address Table) This area contains a bit map identifying all bad sectors on the disk.
- AST (Alternate Sector Table) This area contains the addresses of bad sectors on the disk along with an alternate sector address for each.
- DPD (Disk Partition Data) This area contains a description of each disk partition, its logical assignment, and operating system code.
- OSN (Operating System Name Table) This area contains the name strings of each known operation system type code found in the DPD.
- BOOT This area is reserved for the Winchester boot program.

Other reserved areas are:

- PAS (Partition Alternate Sector Table) This area contains the addresses of all bad sectors in the partition along with an alternate sector address for each. It occupies the first sector in the first track of each partition (CP/M and Concurrent CP/M only).
- 2. Alternate Sector Area This area is reserved for use as alternate sectors in place of known bad sectors.
- 3. Maintenance Cylinder This is the next to the last cylinder and is reserved for use by the hard disk diagnostic.
- 4. Manufacturing Cylinder This is the last cylinder and is reserved for bad spot information written during the manufacturing process.

A.4.1 Pre-Boot

This block contains the code necessary to read in the HOM block and locate the Disk Boot Program. It then reads in this program from consecutive sectors as specified by the HOM block and starts it at a fixed address (to be determined at a later time). When a new HOM block is written to the disk, this block should be initialized with a small program that displays the message: "There is no bootable system on this disk."

A.4.2 Home Block (HOM)

This block contains the data necessary to locate all other reserved areas on the disk. It contains the Volume ID (for removable media) as well as the physical disk parameters. The home block data must fit into 128 bytes in order to be compatible with disks that have a sector size of 128 bytes. If the disk sector size is greater than 128 bytes, then the remainder of the home block must be filled with zeros to maintain a proper checksum.

Byte Contents

- 0-2 HOM Block Identification (ASCII characters).
- Flag: \$00 if partitioned; else \$FF
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-13 8-character VOL ID (Default := "RAINBOW")
- 14-17 System ID or serial number (Default := 0)
- 18-22 Disk Addresses/length of BAT or 0 if none.
- 23-27 Disk Addresses/length of DPD or 0 if none.
- 28-32 Disk Addresses/length of OSN or O if none.
- 33-37 Disk Addresses/length of BOOT or O if none.
- 38-42 Disk Addresses/length of AST or 0 if none.
- 43-44 Starting track number for alternate sector area.
- Number of tracks reserved for alternate sector area.

Byte Contents

46-63 Auto-boot parameters:

```
46 Auto-boot Flag: $FF if no auto-boot
$nn if auto-boot: nn is index into
DPD entries section of DPD block
```

47-48 Partition Boot Track

64-126 Physical Disk Parameters:

```
64-65
        # of Cylinders
66
        Sectors/Track
67-68
        Sector size (bytes)
69
       # of Surfaces
70-71
       Maint. Cylinder #
       Mfg. Data Cylinder #
72-73
74-75
       Write Pre-comp value
76
        Step Rate
        Disk Type Code, 10 = RD51; 12 = RD50
77
```

Physical block # of this block (for example, Ol for HOM)

128-511 Must be zero (MBZ)

A.4.3 Bad Address Table (BAT)

This table contains a bit map identifying known bad sectors on the disk. The map is created by the hard disk utility program using the factory bad spot data and the diagnostic mapping data. The table is treated as an array of 16-bit words. The number of words required depends on the size of the disk. (For the RD51, 1224 words are needed.) A bit is accessed first by locating the word containing it and then by locating the bit position within the word. The bit locations are a function of the sector disk address. The word offset and bit position are computed as follows:

```
table value = (trk number X sectors/track) + (sector number - 1)
word offset = bits 4-23 of table value
bit position = bits 0-3 of table value
```

Because the entire table cannot fit in a single sector, multiple BAT blocks are required. The header of each block identifies the range of sector addresses whose bits are represented in that block's portion of the table.

Byte Contents

- 0-2 BAT Block Identification (ASCII Characters)
- 3 Logical Block Number (LBN) Relative block number within BAT table. (That is, the first BAT block is 0, the second is 1, and so forth.)
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-8 Disk Address of the sector corresponding to the first bit entry in the block
- 9-11 Disk Address of the sector corresponding to the last bit entry in the block
- 12-511 BAT entries, one bit each:

Bit value Meaning

- O Corresponding sector is good
- 1 Corresponding sector is bad

A.4.4 Alternate Sector Table (AST)

This block contains an ordered list of known bad sectors on the disk along with an alternate good sector for each. The disk utility programs assign alternate good sectors to each bad sector specified in the BAT blocks. The bad sector/alternate sector information relevant to each partition is duplicated in the PAS block located at the beginning of the partition.

Byte Contents

- 0-2 "AST" Block Identification (ASCII Characters)
- 3 Logical Block Number (LBN) Relative block number within AST table. (That is, the first AST block is 0, the second is 1, and so forth.)
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-7 Maximum_Entry_Count The maximum number of AST entries allowed in this block. (Max := 100 for 512 byte sectors.)

- Byte Contents (Cont.)
- 8-9 Entry_Count The number of entries in this block, counting bad sectors with or without alternate sectors and unassigned alternate sectors.
- 10-11 Reserved
- 12-511 AST entries, 5-bytes each:

Offset Contents

- 0-2 Disk Address of bad sector: 0 means associated alternate sector is unassigned.
- 3 Track offset of alternate good sector (from first alternate track)
- 4 Sector number of alternate good sector

A.4.5 Disk Partition Data (DPD)

This area contains the data for partitioning the disk into logical, assignable areas. Areas are categorized by name and OS type. This data is constructed at sub-system installation time or whenever the user wishes to repartition the disk.

- Byte Contents
- 0-2 "DPD" Block Identification (ASCII Characters)
- 3 Logical Block Number (LBN) Relative block number within DPD table. (That is, the first DPD block is 0, the second is 1, and so forth.)
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-7 Maximum_Entry_Count The maximum number of DPD entries allowed in this block. (Max := 15 for 512 byte sectors.)
- 8-9 Entry_Count The number of DPD entries in this block.
- 10-31 Reserved

Byte Contents (Cont.)

32-511 DPD entries, 32-bytes each:

```
Offset Contents
0
        Flag: $FF := Non-existent; $0F := not initialized;
              $FO := initialized.
1
        Logical unit: $00 := Unassigned; 1-63 := Assignment
                      within OS (For CP/M: 1 = A, 2 = B, 3 =
                       C, and so forth.)
2-9
        Partition name (ASCII Alphanumeric characters)
        [Default := "DISKnn"; nn := 01, 02, etc.]
10
        Partition occurrence number (i.e. 0,1,2,...
                                                           for
        partitions of the same name).
        OS type code: Index into OS Name table (OSN)
11
12-13
      First track number
14-15
       Last track number
16
        Number of PAS blocks at the beginning of the partition
        (CP/M and Concurrent CP/M partitions only)
17
        if Concurrent CP/M partition, directory size code:
             0 = default (256 entries for <5MB partition,
                    512 for >5MB partition)
             1 = 256 entries
             2 = 512 entries
             3 = 1024 entries
             4 = 2048 entries
18
        if MS-DOS partition, cluster size code:
             0 = 2KB cluster
             1 = 4KB cluster
             2 = 8KB cluster
```

- 3 = 16KB cluster
- if MS-DOS partition, number of FAT sectors (only if Version 3.0 or later of Hard Disk Utility is used)
- 20-31 Reserved for Backup/Restore system.

A.4.6 Operating System Name Table (OSN)

This area contains the operating system name string table representing the operating system type codes found in the DPD. This table may be sparsely populated, therefore, it is not necessary to allocate consecutive entries. Each entry is a zero filled 16-byte text string.

Byte Contents

- 0-2 "OSN" Block Identification (ASCII Characters)
- 3 Logical Block Number (LBN) Relative block number within OSN table. (That is, the first OSN block is 0, the second is 1, and so forth.)
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-7 Maximum_Entry_Count The maximum number of OSN entries allowed in this block. (Max := 31 for 512 byte sectors.)
- 8-15 Reserved
- 16-511 OSN entries (16-bytes). Operating system name strings created by the partition utility. Name strings may consist of 1 to 16 ASCII characters excluding CONTROL, SPACE and DEL characters.

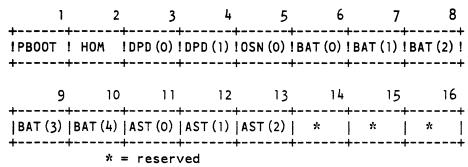
A.4.7 System Boot Program Area

This area contains a secondary bootstrap routine. This routine asks you what operating system and partition should be used for booting. It optionally uses the "Auto-boot" data if present in the HOM block. Whenever this area is created, the PRE-BOOT block (0) should also be written.

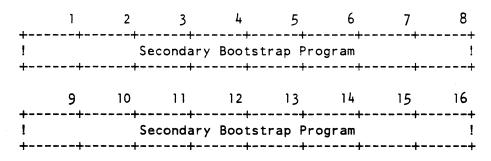
A.4.8 Track Layout

For a 10M byte RD51 disk with 16 sectors of 512 bytes each per track.

Track 0:



Track 1:



Track 2:

same as track O

Track 3: same as track 1

Tracks 4-19: Alternate sectors

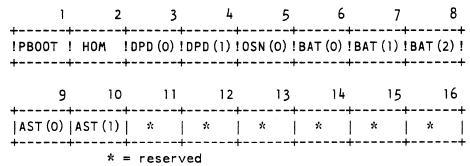
Tracks 20-1215: Partitions

Tracks 1216-1219: Maintenance tracks

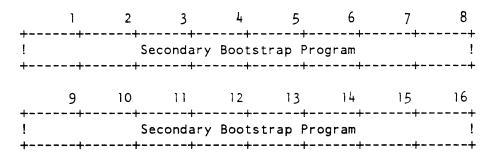
Tracks 1220-1223: Manufacturing tracks

For a 5M byte RD50 disk with 16 sectors of 512 bytes each per track.

Track 0:



Track 1:



Track 2: same as track 0

Track 3: same as track 1

Tracks 4-12: Alternate sectors

Tracks 20-603: Partitions

Tracks 604-607: Maintenance tracks

Tracks 608-611: Manufacturing tracks

A.4.9 Partition Alternate Sector Table (PAS)

This block contains an ordered list of known bad sectors within the partition along with an alternate good sector for each. This information is a subset of that which is contained in the AST block. It is provided for use by the operating system to prevent use of known bad sectors. It is located in the first sector on the first track of a CP/M or Concurrent CP/M partition. There is no PAS block for MS-DOS partitions.

Byte Contents

- 0-2 "PAS" Block Identification (ASCII Characters)
- 3 Logical Block Number (LBN) Relative block number within a possible sequence of PAS blocks (That is, the first PAS block is 0, the second is 1, and so forth.)
- 4-5 Checksum [Block Checksum := 0 (mod 16-bits)]
- 6-7 Maximum_Entry_Count The maximum number of PAS entries allowed in this block. (Max := 100 for 512 byte sectors.)
- 8-9 Entry_Count The number of PAS entries in this block.
- 10-11 Reserved
- 12-511 PAS entries, 5-bytes each:

Offset Contents

- 0-2 Disk Addresses of bad sector
- 3 Track offset of alternate good sector (from first alternate track)
- 4 Sector number of alternate good sector

A.4.10 Operating System Bootstraps

Individual operating system bootstraps reside on reserved areas within their respective partitions, starting at the first sector after the PAS block in the partition.

APPENDIX B

MICROSOFT ARTICLES FOR PROGRAMMERS AND OEMS

This appendix includes articles written by Microsoft Corporation. Generally, they cover the same information covered in the MS-DOS "Programmers Reference Manual." They are included since they provide additional, useful information.

B.1 CONFIGURATION FILES IN MS-DOS VERSION 2.0

The following are a list of commands for the configuration file CONFIG.SYS:

BUFFERS = <number>

This is the number of additional sector buffers to add to the system list. The effect of several BUFFERS commands is to allocate a series of buffers.

FILES = <number>

This is the number of open files that the XENIX system calls can access.

DEVICE = <filename>

This installs the device driver in <filename> into the system list.

BREAK = < ON or OFF>

If ON is specified (the default is OFF), a check for ^C at the console input is made every time the system is called. ON improves the ability to abort programs over previous versions of the DOS.

MICROSOFT ARTICLES FOR PROGRAMMERS AND DEMS

SWITCHAR = <char>

Causes the DOS to return <char> as the current switch designator character when the DOS call to return the switch character is made. Default is '/'.

AVAILDEV = <true or false>

The default is TRUE, which means both \dev\<dev> and <dev> reference the device <dev>. If FALSE is selected, only \dev\<dev> refers to device <dev>, <dev> by itself means a file in the current directory with the same name as one of the devices.

SHELL = <filename>

This begins execution of the shell (top-level command processor) from <filename>. Used when COMMAND.COM is not in the current directory.

A typical configuration file might look like this:

BUFFERS = 10
FILES = 10
DEVICE = \bin\network.sys
BREAK = ON
SWITCHAR = SHELL = a:\bin\COMMAND.COM a:\bin /p

The default value for BUFFERS is OEM specific in that the OEM can specify the number in the BIOS. A typical value is 2, the minimal value is one. (The Rainbow's default is 4). The default value for FILES is usually 8 (as above, it may be set by OEM BIOS), so "FILES = 10" actually allocates only 2 new file channels. If a number less than, or equal to, five is specified, the command is ignored. (The Rainbow's default is 8). BREAK defaults to OFF, SWITCHAR to /, and AVAILDEV to TRUE. Notice that the setting of SWITCHAR may effect characters used on the SHELL line (this is true of COMMAND.COM).

B.2 VERSION INCOMPATIBILITIES

Areas where Version 2.0 is not compatible with $\ensuremath{\mathsf{previous}}$ versions of the DOS.

• Direct calls to the BIOS

Any program which jumped directly to the BIOS by way of the jump table at 40:0 no longer works.

• FAT pointer calls

Programs that used system calls 27 and 28 to get a pointer to the FAT no longer work. Because the FAT is now cached with other disk resources, there is no fixed location in memory to pass the address to. The calls still exist, however, and have the same format. THEY CAN ONLY BE USED TO GET THE FAT ID BYTE. On return ES:BX points to a FAT ID BYTE for the Drive. Doing anything except READing this ONE byte will probably crash the system. In order to get at the FAT, programs first call DSKRESET (call 13) to flush out any dirty buffers, and then make a GETDPB call (call 31 or 50) to find out which sector on the disk the FAT starts at, how big it is, and how many copies of it there are. Then INT 25H and INT 26H can be used to transfer the FAT in and out of the programs memory space.

• INT 25H and INT 26H

In order for the above to work, and in order to maintain some order in the world of multi-surface disks, it is required that INT 25H and 26H use the MS-DOS sector mapping rather than some rather arbitrary head-cylinder-sector mapping.

The following subroutine reads the FAT into the area of memory specified by DS:BX. DL contains the drive number, DL=0 means read the FAT from the default drive, DL=1 indicates read from drive A:, and so on.

```
getfat:
       push
               bх
                              ; save pointer to FAT area
       push
               ds
       mov
               ah,50
                             ; request the apb
        int
               21h
               cx,[bx+15]
       mov
                             ; get FAT sector count
               dx,[bx+6]
                               ; first sector of FAT
       mov
                               ; restore FAT area pointer
        pop
               ds
        pop
               bx
               al,dl
                               ; is it the default drive?
       mov
       or
               al.al
               driveok
                              ; if not, load FAT
        inz
       mov
                ah,19h
                              ; ask for default drive
        int
                21h
                               ; get the default drive
        inc
                a l
                               ; map a=0 to a=1
driveok:
        dec
                al
                               ; map a=1 to a=0
        int
                25h
                               ; read the FAT into DS:BX
                               ; clean up the stack
        gog
                ax
        ret
```

MICROSOFT ARTICLES FOR PROGRAMMERS AND DEMS

B.3 DIFFERENCES AND ADDITIONS TO INT 24H HARD ERROR HANDLER(S)

For MS-DOS 2.0

Additional Constraints:

Under previous versions it was not explicitly stated that an INT 24H handler must preserve the ES register. It is now required that INT 24H handlers preserve ES.

When it is desired to ignore an error, the same registers must be preserved as when it is desired to retry the operation (SS,SP,DS,BX,CX,DX).

It was not clearly stated in the past, but it was true that only system calls 1-12 can be made by an INT 24H handler. Making any other calls destroys the DOS stack and its ability to retry or ignore an error.

INT 24H handlers should always return to the DOS on a retry, ignore, or abort. Failure to return to the DOS leaves the DOS in an unstable state until a non 1-12 function call is made.

Additional features:

Character device errors are now handled by the INT 24H mechanism. Previously only disk I/O errors were handled by the INT 24H handler. Additional information is now passed to the INT 24H handler in the BP and SI registers (which need not be preserved).

BP:SI is a DWORD pointer to the Device Header of the device causing the error. Information can be gotten from this header to determine whether or not the device is a block or character device; if the device is a character device, the name of the device can also be obtained. The DEVICE-DRIVERS document for 2.0 contains the definition of this header format.

NOTE

AL (drive number for Disk errors) is indeterminate on character device errors. Bit 7 of AH is always 1 for character device errors. Previously bit 7 was 1 only in the case of a bad memory image of the FAT.

LIST OF INT 24H ERROR CODES PASSED IN DI

- 0 Write protect violation
- * 1 Unknown unit
 - 2 Drive not ready
- * 3 Unknown command
 - 4 CRC error
- * 5 Bad drive request structure length
- * 6 Seek error
- * 7 Unknown media
 - 8 Sector not found
- * 9 Printer out of paper
 - A Write fault
- * B Read fault
 - C General failure
- * Denotes New Function

As mentioned above BP:SI points to the device header:

BP:SI-> DWORD Pointer to next device (-1 if last device) | WORD Attributes Bit 15 = 1 if char device, 0 if blk if bit 15 is 1 Bit 0 = 1 if Current sti device Bit 1 = 1 if Current sto output Bit 2 = 1 if Current NUL device Bit 3 = 1 if Current CLOCK dev Bit 14 is the IOCTL bit (see below) Bit 13 is the NON IBM FORMAT bit . ________ WORD Pointer to Device strategy entry point WORD Pointer to Device interrupt entry point +-----| 8-BYTE character device name field Character devices set a device name I For block devices the first byte is | The number of units

To tell if the error occurred on a block or character device you must look at bit 15 in the attribute field (WORD at BP:SI+4).

If the name of the character device is desired, look at the eight bytes starting at BP:SI+10.

B.4 MS-DOS 2.0 UTILITY EXTENSIONS

The following notation is used below:

BATCH COMMAND invocation

COMMAND [[<drive>:]<path>] [<CTTYDEV>] [/D] [/P] [/C <string>]

/P If present, COMMAND is permanent, otherwise this is a transient command.

/D If present, COMMAND does not prompt for DATE and TIME when it comes up.

drive: Specifies device where command looks for COMMAND.COM current default drive if absent.

<path> Specifies a directory on device drive: root directory if
absent.

<CTTYDEV> Name of the CTTY device. /DEV/CON if absent and command is permanent. The /DEV/ may be left off if AVAILDEV is TRUE (see AVAILDEV above).

/C <string> If present, /C must be the last switch. This causes COMMAND to try to execute the string as if the user had typed it at the standard input. COMMAND executes this single command string then exits. If the /P switch is present it is ignored (can't have a single command, permanent COMMAND).

NOTE

ALL of the text on the command line after the /C is just passed on. It is not processed for more arguments, this is why /C must be last.

Redirection of standard input/standard output.

Programs that read from the keyboard and write to the screen are said to be doing 1/0 to the standard input and standard output. Using any of the following results in 1/0 to these standard devices:

Writing to default handles 1 / read from default handle 0.

Doing byte I/O using system calls 1, 2, 6-12.

These standard devices may be redirected to/from files by the following in command line arguments:

> <filename>

causes <filename> to be created (or truncated to zero length) and then assigns standard output to that file. All output from the command is placed in the file.

< <filename>

causes standard input to be assigned to <filename>. All input to the command comes from this file. If end-of-file is reached, then system calls 1, 2, 6-12 returns ^Z, while reading from handle 0 returns zero characters.

>> <filename>

causes <filename> to be opened (created if necessary) and positions the write pointer at the end of the file so that all output is appended to the file.

Notice that the above does not appear in the command line that the program being invoked sees.

Examples:

DIR *.ASM >FOO.LST

Sends the output of the dir command to the file FOO.LST.

FOR %O IN (*.ASM) DO MASM %O; >>ERRS.LST

Sends all error output from assembling every .ASM file into the file ERRS.LST.

Piping of standard 1/0

It is often useful for the output of one program to be sent as input to another program. A typical case is a program that produces columnar output that must later be sorted.

The pipe feature allows this to occur naturally as the programs do all of their 1/0 to the standard devices.

For example, if we had a program SORT that read all of it's standard input, sorted it and then wrote it to the standard output, we could get a sorted directory listing as follows:

DIR | SORT

The | would cause all standard output generated by the left-hand command to be sent to the standard input of the right-hand command.

If we wanted the sorted directory to be sent to a file, we type:

DIR | SORT >FILE

and away it goes.

The piping feature is implemented as sequential execution of the procedures with redirection to and from temporary files. In the example above, the following would be an exact equivalent:

DIR >\tmp\stdl

SORT >\tmp\stdl >FILE

The pipe is not a real pipe but rather a quasi-pipe that uses temporary files to hold the input and output as it sequentially executes the elements of the pipe. These files are created in the current directory, of the current drive and have the form %PIPEx%.\$\$\$, where x is 1 or 2. This means that any program that runs in the pipe must be sure to restore the current directory and drive, if it has changed them, or the pipe files are lost.

COMMAND extensions

BREAK [{ON | OFF}]

"BREAK ON" turns on the Control C check in the DOS function dispatcher. "BREAK OFF" turns it off. If no argument is given the setting of BREAK is printed to the standard output in the form:

BREAK is xxx

Where xxx is "on" or "off".

CHDIR [{<drive>: | <path>}]

Change directory, or print current directory. If no argument is given, the current directory on the default drive is printed. If drive: alone is given, the current directory of drive is printed. Otherwise the current directory is set to path.

"CD" is accepted as an abbreviation.

CLS

Clear screen causes the ANSI escape sequence ESC[2J to be sent to standard output.

COMMAND internal commands take path arguments.

DIR <path>

COPY <path> <path>

DEL (ERASE) <path>

If the path is a dir, all files in that dir are deleted.

NOTE

The "Are you sure (Y/N)" prompt for DEL and ERASE now uses buffered standard input, so users must type a return after their answer. This gives them the chance to correct if they type 'y' by mistake.

TYPE <path> (must specify a file)

CTTY \DEV\dev - Change console TTY. For instance:

CTTY \DEV\AUX

Would move all command I/O to the AUX port.

CTTY \DEV\CON

Would move it back to the normal device. The \dev\ prefix may be left off if AVAILDEV is TRUE (see configuration-file doc).

ECHO [{ON | OFF | <message>}]

Normally, commands in a BATCH file are echoed onto the standard output as they are seen by COMMAND. ECHO OFF turns off this feature. ECHO ON turns echoing back on. If ON or OFF is not specified and there is text following the command, that text (a message) is echoed to standard output. If there are no arguments at all, the current setting of echo (on or off) is echoed to the standard output in the form:

ECHO is xxx

Where xxx is "on" or "off".

EXIT

For COMMANDs run without the P switch, this causes COMMAND to return. For a normal COMMAND it causes a return to itself.

GOTO <label>

Causes commands to be taken from the batch file beginning with the line after the <label> definition. If no label has been defined, the current batch file terminates.

Example:

:foo

REM looping...

GOTO foo

produces an infinite sequence of messages:

REM looping...'

NOTE

Labels are case insensitive, :F00 == :foo == :Foo

IF <condition> <command>

where <condition> is one of the following:

ERRORLEVEL <number>

true if and only if the previous program EXECed by COMMAND had an exit code of <number> or higher.

<stringl> == <string2>

true if and only if <stringl> and <string2> are identical after parameter substitution. Strings may not have embedded delimiters.

EXIST <filename>

true if and only if <filename> exists.

NOT <condition>

true if and only if <condition> is false.

The IF statement allows conditional execution of commands. When the <condition> is true, then the <command> is executed otherwise, the <command> is skipped.

Examples:

IF not exist \tmp\foo ECHO Can't find file \tmp\foo

IF \$1x == x ECHO Need at least one parameter

IF NOT ERRORLEVEL 3 LINK \$1,,;

FOR %%<c> IN <set> DO <command>

<c> can be any character but 0,1,2,3,...,9 (so there is no confusion with the %0 - %9 batch parameters).

<set> is (<item> *)

The %%<c> variable is sequentially set to each member of <set> and then <command> is evaluated. If a member of <set> is an expression involving * and/or?, then the variable is set to each matching pattern from disk. In this case only one such <item> may be in the set, any <item>s after the first are ignored.

Example:

FOR %%f IN (*.ASM) DO MASM %%f;

for %%f in (FOO BAR BLECH) do REM %%f to you

NOTE

The '%%' is needed so that after Batch parameter (%0 - %9) processing is done, there is one '%' left. If only '%f' were there, the batch parameter processor would see the '%' then look at 'f', decide that '%f' was an error (bad parameter reference) and throw out the '%f' so that FOR would never see it. If the FOR is NOT in a batch file, then only ONE '%' should be used.

:<label>

This is essentially a no-op. It defines a label in the batch file for a subsequent GOTO. It may also be used to put comment lines in batch files since all lines that start with ':' are ignored.

MICROSOFT ARTICLES FOR PROGRAMMERS AND DEMS

MKDIR <path> - Make a directory.

"MD" is accepted as an abbreviation.

PATH [<path>{;<path>} *]

Set command search paths. This allows users to set directories that should be searched for external commands after a search of the current directory is made. The default value is NO PATH. In addition there are two special cases: PATH with no arguments prints the current path. Path with the single argument ';' (i.e., "PATH;") sets the NUL path (no directories other than the current one searched). If no argument is given, the current value of PATH is printed to the standard output in the form:

PATH=text of path or No path

PROMPT [cprompt-text>]

Set the system prompt. MS-DOS prompts are now user settable, all of the text on the command line is taken to be the new prompt. If no text is present the prompt is set to the default prompt. There are meta strings for various special prompts. These are of the form '\$c' where c is one of the following:

\$ - The '\$' character.

t - The time.

d - The date.

p - The current directory of the default drive.

v - The version number.

n - The default drive.

g - The '>' character.

1 - The '<' character.</pre>

b - The '|' character.

s - The ' ' character.

e - The ESC character.

_ - A CR LF sequence.

EXAMPLE:

PROMPT \$n>

Would set the normal MS-DOS prompt.

PROMPT Time = \$t\$ Date = \$d

Would set a two line prompt which printed

Time = (current time)

Date = (current date)

For '\$c' sequences, lower case = upper case, and any character not on the above list is mapped to nothing.

RMDIR <path> - Remove a directory.

"RD" is accepted as an abbreviation.

The directory must be empty except for '.' and '..'.

<path> - A standard XEN!X style path with the optional addition
of a drive spec:

A:\F00\BAR Full path

\F00\BAR Full path, current drive

FOO\BAR Current dir relative

A:F00\BAR " " "

SET (ENVNAME) = (ENVTEXT)

Set environment strings.

This command inserts strings in COMMAND's environment. For instance:

SET PROMPT=\$n>

Duplicates the function of the PROMPT command.

SET PATH=p1;p2

Duplicates the function of the PATH command.

SET foo=bar

Puts the string F00=bar into the environment (notice the case mapping of (ENVNAME)).

NOTE

Environments are very flexible, and almost anything can be put into the environment with the SET command; the only requirement is that a single '=' be present in the string.

SHIFT

Currently, command files are limited to handling 10 parameters: %0 through %9. To allow access to more than these, the command SHIFT performs a 'pop' of the command line parameters:

if %0 = "foo

%1 = "bar"

%2 = "blech"

%3...%9 are empty

then a SHIFT results in the following:

%0 = "bar"

%1 = "blech"

%2...%9 are empty

If there are more than 10 parameters given on a command line, those that appear after the 10th (%9) are shifted one at a time into %9 by successive shifts.

VER

Prints DOS version number.

VERIFY [{ON | OFF}]

Select/Deselect verify after write mode. This supplements the V switch to the COPY command. Once turned ON, it stays on until some program changes it (via the set verify system call) or the VERIFY OFF command is given. If no argument is given, the current setting of VERIFY is printed to the standard output in the form:

VERIFY is xxx

Where xxx is "on" or "off".

MICROSOFT ARTICLES FOR PROGRAMMERS AND OEMS

VOL [<drive>:]

Prints the volume ID of the disk in drive: No drive: it does the default drive.

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MS™-DOS V2.05 BIOS Listings

digital equipment corporation

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Printed in U.S.A.

```
Warning: No STACK segment

Start Stop Length Name Class

OOOOOH O25SCH 255DH CDDE CODE

O2560H O2053H 07F4H SYSINITSEG SYSTEM_INIT

Origin Group
OOOO:O CGROUP
```

```
Address Publics by Name

0000:1978 AUX

0000:1981 AUX

0258:0750 BANCOM

0000:1232 BIOSINT

0000:1105 CLK

0
```

```
HDRIVES
HDSKO
HDSKDEV
HD_INIT
HFORMAT
HINIT
HIOCTL
HMCHK
HNFMT
HNFMT
HYPECTOR
HWRITE
ISSPKT
INCITES
INSTAT
INCITES
INTTAB
INTY21
INTY24
INTY25
INTY24
INTY25
INTY27
INTRET
I
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                                                 0000:0011
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0000:040D
0000:1ECA
                                 0000 : 1 CES
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RCVENA
RCVENA
RCVENT
RDNVMCOM
ROSETUP
RONVMCOM
ROSETUP
ROLL
RE IN IT
RE IN IT
RE UPT7201
SECNO DEM
STATUS
STATUS
STATUS
STATUS
STEPRATE(S
SYSINIT
SYSIN
```

0000:0FFS 0000:1FS0 0000:161E 0000:162C 0000:2429 0000:0B40 0000:1F04

0000:040E 0000:040E 0000:021A

XMTMTY XOPTION XRUPT7201 Z80ISR

```
Address
                                                                                                                                                                                                                                                                                                                                                                                                                                          Publics by Value
                                                                                                                                                                                                                                                                                                                                                                                                               MNPARTS
DOS_POINTER
PART_SIZE
STRATEGY
        0000:0004
                                                                                                                                                                                                                                                                                 Abs
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0000 0075
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                                                                                                                                                                                                                                                                                      Abs
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                                                                                                                                                                                                                                                                                                                                                                                                                   PTRSAVE
DEVLST
DSKDEV
HDSKDEV
HDRIVES
AUX2DEV
                                                                                                                                                                                                                                                                                                                                                                                                          AUX2DEV
DECDISK
280ISR
DOS20VEC
DOS21VEC
DOS24VEC
DOS25VEC
DOS25VEC
                                                                                                                                                                                                                                                                                                                                                                                                               DOS27VE
INT20
INT21
INT24
INT25
INT28
INT27
RE_INIT
R100
XOPTION
INITTBL
DSKTBL
CMDSTR
                                                                                                                                                                                                                                                                                                                                                                                                          CMDSTR
DSKTMR
DSK
MC_FLG
HDSKO
INI_TAB
DSKO
INI_TAB
GETFTHO
GETFTHO
GETFTHO
GETPEA
HIOCTL
MAXTRK
STEPRATE
PRECOMP
HREAD
HWRITTY
                                                                                                                                                                                                                                                                                                                                                                                                          HREAD
HWRITEV
HWRITE
HFORMAT
HMCHK
HINIT
CLTPN
INTHDL
HD_INIT
DSK_INIT
HNFMT
0000:0E7D
0000:0E7E
0000:0F7E
0000:108D
0000:108D
0000:10E9
0000:10F8
0000:110B
0000:110B
0000:110B
0000:110B
0000:110B
                                                                                                                                                                                                                                                                                                                                                                                                               NDRV
RHOME
UP_BAT
TEMP_TRK
TEMP_SEC
AUX2
PRN
                                                                                                                                                                                                                                                                                                                                                                                                                        AUX
                                                                                                                                                                                                                                                                                                                                                                                                          CLK
CON
TICKER
CLK_50_50
CLK_50_50
CLK_10_20
CLK_1SR
INTRET
OUTHEX
PSTR
PCCOM
PCPCTOM

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                                                                                                                                                                                                                                                                                                                                                                                                               XBAUD2
RDNVMCDM
RUPT7201
XRUPT7201
PRG7201
DFLT7201
DFLTBUF
RDSETUP
RCVENA
RCYDIS
INSTAT
                                                                                                                                                                                                                                                                                                                                                                                                                        XBAUD2
                                                                                                                                                                                                                                                                                                                                                                                                          INSTAT
INCHAR
INCHAR
OUTSTAT
OUTCHAR
PUTCHAR
OUTNOB
ROMODEM
SETMODEM
BRKON
BRKON
BRCVINT
0000:1DEC
0000:1E33
0000:1E50
0000:1E78
0000:1E8E
0000:1ECA
0000:1ECA
0000:1F04
0000:1F18
                                                                                                                                                                                                                                                                                                                                                                                                               BRKOFF
RCVINT
RCVCANCEL
XMTMTY
XMTCANCEL
HACK7201
VECT7201
```

```
0000:178D STATCHG
0000:178D STCANCEL
0000:178D STCANCEL
0000:178A SIFT STATCHG
0000:233 BUFFER
0000:2235 BUFFER
0000:2235 BUFFER
0000:2241D TYRACK
0000:241D PACKET_BASE
0000:2421 TYRACK
0000:2421 TYRACK
0000:2428 PACKET_ADR
0000:2428 PACKET_ADR
0000:2428 PACKET_ADR
0000:2428 TYRCCDD
0000:242B TYRCCDD
0000:242B TYRCCDD
0000:242C PACKET
0000:242T DRYND
0000:242T DRYND
0000:242T DRYND
0000:242T DRYND
0000:242T DRYND
0000:242T DRYND
0000:2431 TRACKN
0000:2431 TRACK
```

```
The Microsoft MACRO Assembler
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                                                                              PAGE
                                                                                         1 - 2
28728301233345337839041244454784901
                                                        page
code segment byte public 'code'
assume cs:cgroup,ds:cgroup
             0000
                                                        ,;Initialize ourselves and what's necessary for;SYSINIT, then pass control there.
                                                        init: cli
             0000 FA
0001 E9 0000 E
                                                                              biosinit
             0004 0000
                                                        dos_pointer
                                                                              dw 0
                                                                                                  ;Filled in by BIOSINIT used by FORMAT
                                                        ;Simplistic strategy routine for 2.00.
             0005
                                                        stratg proc far
                                                        strategy:
             0008 2E: 88 1E 0011 R
0008 2E: 8C 08 0013 R
0010 CB
                                                                  mov word ptr cs:ptrsave,bx
mov word ptr cs:ptrsave+2,es
ret
             0011
                                                        stratg endp
                                                        ;Public pointer save.
             0011 7777777
                                                        ptrsave dd
```

```
23455678901234566789012345667890123345667890123345677777789012334567899100012334567899100011006
                                                                                    page
                                                                                   ; 1/0 device table. This contains the five ;minimum devices: CON, AUX, PRN, CLOCK and ;disks. SYSINIT may load others later, but ;that's no concern to us here.
                                                                                    DWORD pointer to next device (-1,-1 if last device)
                                                                                              Device attribute WORD

Bit 15 = 1 for chacter devices.

O for Block devices.
                                                                                                   Charcter devices. (Bit 15:1)
Bit 0 = 1 current sti device.
Bit 1 = 1 current sto device.
Bit 2 = 1 current NUL device.
Bit 3 = 1 current Clock device.
                                                                                                       Bit 14 = 1 IOCTL control bit.
                                                                                                Device strategy pointer.
                                                                                               Device interrupt pointer.
                                                                                               Device name field.
Character devices are any valid name;
left justified, in a space filled;
field.
                                                                                    ; Block devices contain # of units in ; the first byte. ;
                    0015
0015
0019
001B
001D
                                                                                    devist label word
                                0027 R 0040
8003
                                                                                                                   auxdev,biosseg ;ptr to next,
8003h ;char,sti,sto,
strategy
con
'CON
                                                                                                   0 w 0 w 0 w
                                                                                    condev:
                                0005 R
0000 E
43 4F 4E 20 20 20
20 20
                    0027
                                                                                    auxdev :
                                                                                                                   prndev, biosseg
ocooon
strategy
aux
'AUX
                    0027
0028
002B
002F
                                0039 R 0040
                                0039 R 0040
C000
0006 R
0000 E
41 55 58 20 20 20
20 20
                                                                                                    dw
                                                                                                    dw
                    0031
                                                                                                    db
                    0039
0039
003D
                                                                                   prndev:
                                004B R 0040
                                                                                                   dw
                                                                                                                    c 1kdev . biossea
                                C000
0005 R
0000 E
50 52 4E 20 20 20
                                                                                                   dw
dw
                                                                                                                    OCOOON
                                                                                                                    strategy
prn
'PRN '
                    003F
```

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```
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                                                                                       02-20-84
                                                                                                                   PAGE
                                                                                                                                    1-4
107
                              20 20
108
1109
1111
1112
1114
1118
1117
1118
1120
1121
1123
1124
1125
1126
1130
1131
1131
1133
1134
1136
1137
1138
1139
1139
                  004B
                                                                                  c1kdev:
                              005D R 0040
8008
0008 R
0000 E
43 4C 4F 43 4B 20
20 20
                  004B
004F
0051
0053
                                                                                                   dw
dw
dw
                                                                                                                   dskdev,biosseg
8008h
strategy
clk
                                                                                                   dw
                                                                                                                     CLOCK
                  0055
                                                                                                   db
                  005D
005D
0051
                                                                                  dskdev:
                              FFFF FFFF
2000
                                                                                                                   -1,-1
2000h
strategy
dsk
2,0,0,0,0,0,0,0,0
                                                                                                   dw
                                                                                                                                                    ;non-IBM compatable without IOCTL
                                                                                                   dw
                              0005 R
                                                                                                   dw
db
                  0063
                  0065
                               02 00 00 00 00 00
                  008F
008F
0073
0075
0077
                                                                                  hdskdev:
                              FFFF FFFF
8000
0008 R
0000 E
01 00 00 00 00 00
                                                                                                  dw
dw
                                                                                                                   -1,-1
5000h
strategy
dsk_int
1,0,0,0,0,0,0,0
                                                                                                                                                    ; with IOCTL
                                                                                  dw
hdrives db
                  0081
0081
0085
0087
                              FFFF FFFF
C000
0006 R
0000 E
41 55 58 32 20 20
20 20
                                                                                                   DW
DW
                                                                                                                   -1,-1
OCOOOH
                                                                                                                   strategy
aux2
'AUX2
                                                                                                   DW
                   0089
                                                                                                                                                    ; device name
                  0093
                                                                                  code ends
```

```
The Microsoft MACRO Assembler
                                                                                                           02-20-84
                                                                                                                                            PAGE
                                                                                                                                                                Symbols-1
Segments and groups:
                                                                                                    Size
                                                                                                                         align
                                                                                                                                             combine class
GROUP
0093
                                                                                                                                             PUBLIC 'CODE'
Symbols:
                                                                                                     Туре
                                                                                                                         Value
                                                                                                                                             Attr
AUX. . .
AUX2 . .
AUX2DEV.
                                                                                                   L NEAR
L WORD
L WORD
L WEAR
L NEAR
                                                                                                                                                                 External
External
Global
                                                                                                                        0000
0081
0027
0000
0040
0000
                                                                                                                                             CODE
AUXDEV .
BIOSINIT
BIOSSEG.
                                                                                                                                                                 Externa 1
CLKDEV .
                                                                                                                                             CODE
CONDEV

DEVLST

DOS_POINTER

DSK

DSKDEV
                                                                                                                                                                 External
                                                                                                                        0015
0015
0004
0000
005D
                                                                                                                                             CODE
                                                                                                                                             CODE
                                                                                                                                                                 Global
Global
                                                                                                                                                                  External
Global
                                                                                                                                             CODE
                                                                                                                                                                 External
Global
Global
                                                                                                                        0000
                                                                                                                        0000
0079
006F
0000
0000
                                                                                                                                             CODE
CODE
CODE
                                                                                                                                                                  Externa 1
                                                                                                                                             CODE
                                                                                                                                             CODE
                                                                                                                                                                 Global
Global
Length :000B
Warning Severe
Errors Errors
O O
```

```
Symbol Cross Reference
                       (# is definition)
                                    Cref-1
22#
22#
19
88
                        135
131#
94#
21#
                        34
88
                            95
                                102
                                    109
11
23#
102
11
22#
88#
                        28
112
                            23
                        108#
27#
91
                            27
                                139
18
                    18
23#
19
24#
                            115#
18
                        128#
33#
105
101#
43
                             44
                                 51#
97
                                                126
                             80
                                    104
                                        111
                                            118
                                                    134
                    18
40#
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                               02-20-84
                                                                                                                                                 PAGE
                                                                                                                                                                      1 - 1
                                                                                                                            PAGE
TITLE
NAME
                                                                                                                                                 50,132
DEC Rainbow Rx-50/280 disk driver
DECDISK
                                                                                                                            COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
All rights reserved.
0123456789012345678901234567890123444444444555555
                                                                                                        caroup aroup code
                                                                                                        ;Converts the standard BIOS data packet to ;the Rainbow 280 packet and executes it.;Returns normal MSDOS error codes.
                                                                                                       ;
public decdisk,z80isr,physdisk
extrn buffer:word, MC_FLG:BYTE
                                                                                                        ;280 function codes:
                                                                                                       ;
OKRDCOM equ
OKWTCOM equ
OKCMCOM equ
OKWYFCOM equ
OKVFYCOM equ
OKFMTCOM equ
                      = 0013
= 0014
= 0015
= 0016
= 0023
= 0024
                                                                                                                                                 13h
14h
15h
16h
23h
24h
                                                                                                                                                                     disk read,
disk write,
media check,
write with verify,
verify disk command
format track/disk command
                                                                                                        int 280 equ
gscr equ
                       : 0000
: 0002
                                                                                                                                                 0
                                                                                                                                                                      ; z80 interrupt port
                                                                                                                                                                       ; interrupt status port,
                       = 0000
= 0001
= 0002
= 0003
= 0004
= 0006
= 0008
                                                                                                        XCOMMAND EQU
XDRIVE EQU
XSECTOR EQU
                                                                                                                                                                       ;IOCTL PACKET COMMAND OFFSET
;DRIVE/SIDE OFFSET
;SECTOR OFFSET
;PHYSICAL DRIVE CODE
                                                                                                                               EQU
EQU
EQU
EQU
EQU
                                                                                                        XPHDRY
                                                                                                        XTRACK
XCOUNT
XDMAOFF
XDMASEG
                                                                                                                                                                      ;TRACK # TO USE
;# OF SECTORS TO XFER
;OFFSET OF DMA XFER
;SEGMENT OF DMA XFER
                                                                                                                                                  8
OAH
                       = 0000
= 0008
= 0002
                                                                                                                            EQU
EQU
                                                                                                                                                 00h
08h
02h
                                                                                                                                                                      ;return value for RX50 disks.
;media check return value for IBM 8 sector
;mediacheck return value for IBM 9 sector
                       = 0000
= 0001
= 0002
                                                                                                        rx50val equ
ibm8val equ
ibm9val equ
                                                                                                                                                 00h
01h
02h
                                                                                                                                                                      ;MSDDS value for rx50 disks
;IBM 8 sector disks
;IBM 9 sector disks
                                                                                                       include iodef.ash
;Rainbow Interrupt numbers.
;1-Apr-83 sgs added dskio int vector
;19-Mar-83 sgs added profile int vector [user clock]
;the int profile is called by the RTC interrupt service for each tick.
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                          02-20-84
                                                                                                                                         PAGE
                                                                                                                                                             1 - 2
                                                                                            C : The ax and ds register don't need to be saved [done in clkisr]. C :
: 0064
: 002C
: 0085
                                                                                                                                          64h
2ch
65h
                                                                                                                                                             ;100. user interface to clock interrupt
;80Hz int,
;direct disk io for format
                                                                                                   profile equ
                                                                                                    clk_int equ
dskio equ
                                                                                                   ;

;17-Mar-83 sgs changed to include int 20-26

;interrupt 22-24h are duplicated at 42-44h for consistency.

;These are the relocated Rainbow interrupts.

;interrupts, 20h-26h moved to 40h-48h

;ATTENTION Modules IDINIT and REINIT may have to be

;changed if int vectors 40h to 46h are changed
                                                                                           C ;
C vert equ
C spare41 equ
C spare42 equ
C comdma equ
C aux_prn equ
C excom equ
C uart equ
C x80 equ
                        = 0040
= 0041
= 0042
= 0043
= 0044
= 0045
                                                                                                                                          40h
41h
42h
43h
44h
45h
46h
47h
18h
                                                                                                                                                             ;new vert. freq.
                                                                                                                                                             ;DMA ctrl optional comm. board
;7201 comm./printer;
extented comm. option
;new UART vector;
;280 interrupt;
;new RDM access,
                                                                                                   uart
z80
rom
                         . 0018
                                                                                                                       equ
                                                                                                    ;DEC Rainbow IO port stuff.
                                                                                                   ;
kdp
ksp
auxdp
prndp
auxp
prnp
                       = 0010
= 0011
= 0040
= 0041
= 0042
= 0043
                                                                                                                                                             ;8251 data port,
;8251 status,
;7201 data,
                                                                                                                       equ
                                                                                                                                          10h
                                                                                                                      edn
edn
edn
edn
edn
                                                                                                                                          11h
40h
41h
42h
43h
                                                                                                                                                             ;7201 command,
                                                                                                   ; Values in XOPTION
                                                                                                   ;
wprsnt equ 01
xcprsnt equ 02
include disk.ash
                        : 0001
: 0002
                                                                                                                                                             ;Winnie preset
;XCOMM present
                                                                                                 ;Physical disk IO data packet.
                        : 0000
: 0001
: 0002
: 0003
                                                                                                                                                             ;read or write,
;phs. drive,
;track,
;starting sector,
                                                                                                    command equ
                                                                                                   drive equ
track equ
sector equ
                             0003
                                                                                                                                                             ; setting sector
; sector count,
; current track,
; N.A.
; N.A.
; N.A.
                                                                                                 curtrk equidensity equigablen equidtl
                                                                                                    count
                                                                                                     count
curtrk
                            0005
0006
0007
0008
                             0009
A000
                                                                                                                                          10
12
14
16
18
                                                                                                                                                             ; N.A.; sector size,; DMA offset,; DMA segment; sectors/track; head,
                                                                                                    secsiz
dmaoff
                                                                                                                       edn
edn
edn
                             2000
                         = 000E
= 0010
= 0012
                                                                                                   spt
head
                                                                                                                       equ
  The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                          02-20-84
                                                                                                                                          PAGE
                                                                                                                                                              1 - 3
                                                                                                    page
   112
113
114
115
116
117
118
119
120
121
122
123
124
                                                                                                    ;This is what the Z80 thinks memory looks like.
                         0000
                                                                                                    z80seg segment at 0
z80seg ends
                                                                                                     extrn
                                                                                                                       packet_adr:near
tformat:byte ; Table for current formats, one byte per drive
ttrack:byte ; Current tracks, one per drive. As in TFORMAT,
                                                                                                     ;Pointers to the Z80 and 8088 packets.
                                                                                                     extrn xfrpkt:word
extrn i88pkt:word
                                                                                                                                                                                  ;pointer to "packet" ;returned pointer from Z80
   125
126
127
128
129
                                                                                                    ;The packet for the 280.
                                                                                                                                                                                 ; Z80 packet pointers
; Function code
; retnd status,
; drive and side,
; sector #
; track #
; # of sectors to process
; DMA address (abs)
; extended status
                                                                                                                       packet: byte
fnccod: byte
status: byte
drvno: byte
secno: byte
trackn: byte
                                                                                                     extrn
                                                                                                     extrn
extrn
extrn
extrn
    130
131
132
133
134
135
136
137
                                                                                                      extrn
extrn
                                                                                                                        nsect:byte
dmaadr:word
exstat:byte
```

```
The Microsoft MACRO Assembler DEC Rainbow Rx-50/280 disk driver
                                                                                                           02-20-84
                                                                                                                                             PAGE
                                                                                                                                                                   1 - 4
                                                                                                      page
code segment byte public 'code' assume cs:cgroup,ds:cgroup,es:z80seg
                      0000
                                                                                                      ;
;Convert the BIOS packet for the Z80, make the
;request. The absolute address range must be
;1000 - ffff hex, as below that is Z80 private,
;and the Z80 cannot reach above it. A special
;'disk' module is requred.
                                                                                                      ;
decdisk:push
mov
mov
                      0000 08
0001 B8 ---- R
0004 8E CO
                                                                                                                                              ax,seg z80seg
es,ax
                                    32 D2
8A 57 O1
D0 E2
D0 E2
D0 E2
D0 E2
26: 88 16 0000 E
                       0008
                                                                                                                                                                                       ;clear drive byte & assume single side
;make drive
;select bits,
;SHL DL,1
;4 times is
                                                                                                                                               d1,d1
                                                                                                                           xor
                                                                                                                          mov
shi
shi
                                                                                                                                               d1,drive[bx]
d1,1
d1,1
                       000B
                       000B
000F
0011
0013
                                                                                                                           sh1
                                                                                                                                                                                        :much faster
                                                                                                                                               es:drvno.dl
                                                                                                                          mov
                                                                                                      ;
;Only if the media is RX-50, and the track is
;above 1, skew the sector. RX-50 media tracks
;O and 1 are the boot, and non-RX-50 media
;is assumed to be IBM.
                                                                                                                                             al, sector[bx]
byte ptr density[bx], o
zwo ; if RX-50,
byte ptr track[bx], 2
zwo ; and trk 2-,
                      0018
001B
001F
0021
0025
                                   8A 47 03
80 7F 06 00
75 0E
80 7F 02 02
72 08
                                                                                                                          cmp
jne
cmp
jb
                                     84 00
88 F0
8A 84 01EE R
                                                                                                                                               ah,O ;skew it, SI=
si,ax ;index to tbl
al,skewtbl_2[si]
                       0027
                       0029
0028
                                                                                                                          mov
                      002F 26: A2 0000 E
0033 8A 47 02
0036 26: A2 0000 E
003A 8A 47 04
003D 26: A2 0000 E
                                                                                                                                              es:secno,al
al,track[bx]
es:trackn,al
al,count[bx]
es:nsect,al
                                                                                                      ZWO:
                                                                                                                          mov
                                                                                                                                                                                      ; set it
                                                                                                                          mov
                                                                                                      ;
;Make the absolute address in AX. Ignore out of
;bounds addresses; DISK is supposed to process
;them for us.
                      0041
0044
0046
0048
004A
004C
                                    8B 47 OE
D1 E0
D1 E0
D1 E0
                                                                                                                                               ax.dmasecibx]
                                                                                                                                                                                       : seament
                                                                                                                          shi
shi
shi
shi
                                                                                                                                              ax, omaseg[bx]
ax, 1
ax, 1
ax, 1
ax, dmaoff[bx]
es:dmaadr, ax
                                                                                                                                                                                     ; segment; times 2,; times 4,; times 8,; times 16,; plus offset,
                                      D1 E0
03 47 OC
26: A3 0000 E
                                                                                                                           mov
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                     PAGE 1-5
                                                                             02-20-84
                0053 8A 07
0055 98
0056 8B FO
0058 8A 84 0085 R
005C 26: A2 0000 E
                                                                                                    al,command[bx] ; what to do?
; expand to word
; copy command
al,ZdSkfcn[si] ; get actual command
es:fnccod,al ; store Z80 command
195
196
197
198
199
2001
201
202
203
204
205
206
207
208
209
210
                                                                                       mov
                                                                                       mov
mov
                                                                                       mov
                                                                         ; Execute the packet.
                0060 E8 019C R
0063 07
0064 C3
                                                                                       call
                                                                                                     execz80
                                                                                                                                 ; Invoke 280 and report error if any
                                                                                       pop
ret
                                                                        ;translate table for internal command to 280 command
                0065 14 13 15 16
                                                                                                     qkwtcom, qkrdcom, qkcmcom, qkwvfcom
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                               02-20-84
                                                                                                                           PAGE
page
                                                                                        ;
;for use by format, direct disk i/o
;same as decdisk.
                    0069
                                        04 [
                                                                                                         dЬ
                                                                                                                            4 dup (0) ;this space maker needed for exe2bin 1-Apr-83 sgs no joke
                                                                                                         check media function switch
COMMAND = 8 then turn the function ON
COMMAND = 7 then turn the function OFF
COMMAND = 8 then read the function status in AL
                    006D
                                                                                        physdisk label near
physdisk1 proc far
                                8A 07
3C 06
72 1A
1E
0E
1F
                                                                                                                           AL,XCOMMAND[8X]
AL,6
PHYS1
DS
CS
DS
                    00 B D
                                                                                                         MOV
                                                                                                                                                                                ; get command ; check media function?
                                                                                                         CMP
JB
PUSH
PUSH
                     006F
                    0071
0073
0074
0075
                                                                                                                                                                                ; save user's DS
                                                                                                         POP
                                B4 00
74 05
FE C4
3C 07
75 04
88 26 0000 E
33 C0
A0 0000 E
1F
CA 0002
                    0076
0078
007A
007C
007E
                                                                                                         MOV
JZ
INC
CMP
                                                                                                                           AH,O
Phys2
                                                                                                                                                                                ; assume turn function on
                                                                                                                            AH
AL,7
                                                                                                                                                                                ; assume turn function off
; if > 8 then do as 8
                                                                                                                           AL,7
PHYS3
BYTE PTR MC_FLG,AH
AX,AX
AL,BYTE PTR MC_FLG
DS
                                                                                                           JNZ
                                                                                        PHYS2:
PHYS3:
                     0080
                                                                                                          MOV
                    0084
0086
0089
008A
008D
                                                                                                         XOR
MOV
POP
                                                                                                         RET
                                                                                        PHYS1:
                                 53
                                                                                                         push
push
                    008D
                                                                                                                           bx
es
                    008E
                                                                                        ds set to ca
                                                                                                                          ing segment for [bx] param.
                    008F
0092
                                                                                                                           ax,seg z80seg
es,ax
                               88 ---
8E CO
                                                                                                         mov
                                32 D2
8A 57 O1
80 FA FF
75 O6
8A 57 O3
EB 09 90
DO E2
DO E2
DO E2
DO E2
                    0094
0096
0098
009C
                                                                                                         xor
                                                                                                                            d1,d1
                                                                                                                                                              ;make drive
;is this a physical disk oper?
;no,its logical
;get user's pre defined drive code
;else set the drive
;move drive # to high nybble
                                                                                                                           dl,xdrive[bx]
dl,Offh
                                                                                                         mov
                                                                                                         CMD
                                                                                                                           dl,Offh
logdrv
dl,xphdrv[bx]
stdrv
dl,1
dl,1
                                                                                                         jnz
mov
jmp
shl
                    00A1
00A4
00A6
00A8
                                                                                        logdry:
                                                                                                                                                               ;SHL DL,1
;4 times is
;much faster
                                                                                                          sh 1
                                                                                                          sh 1
                                                                                                          sh 1
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                              02-20-84
                                                                                                                           PAGE
                                                                                                                          es:drvno,d1
cx,xtrack[bx]
ax,ax
al,xsector[bx]
cx,02
skwed
si,ax
al,cs:skewtb1_2[si];and get skewed value
es:trackn,c1
es:secno,al
;put drive/side in packet
get track #
;clear high byte
;get sector #
;is track # > 2
;if not, dont skew table
index into skew table
;save track #
;save processed cost--
                              26: 88 16 0000 E
8B 4F 04
33 C0
8A 47 02
83 F9 02
72 07
8B F0
2E: 8A 84 01EE R
26: 88 0E 0000 E
26: A2 0000 E
                    OOAC
OOB1
OOB4
OOB6
OOB9
stdrv:
                                                                                                         xor
                                                                                                         mov
                                                                                                          cmp
                                                                                                         jb
mov
mov
                    OOBE
                    OOCS
OOCS
OOCS
                                                                                        skwed:
                                                                                                         mov
                                                                                                         mov
                                                                                                                           es:secno,al ;sa
byte ptr es:nsect,1
                                                                                                                                                               ;save processed sector
                                                                                                         mov
                                32 E4
8A O7
8B FO
3C OO
3C OO
8B 4F OA
8B 47 OA
83 FE
57
56
57
                                                                                                                           ah,ah ;clear high byte
al,xcommand[bx] ;what to do?
si,ax ;index into function table
al,o ;if read, use intermediate buffer and move later
                    00D4
                                                                                                         xor
mov
                    00D4
00D8
00D8
00DC
                                                                                                         mov
                                                                                                         CMP
                                                                                                          jz
mov
                                                                                                                                                              ; get users data buffer ptr
;it is, after all, a dword type
;are we doing a format?
;then use the users buffer
;save the registers we are going to cream
;in the block move
                                                                                                                           cx,xdmaseg[bx]
ax,xdmaoff[bx]
si,03
fmtfnd1
                    00E1
00E4
00E7
00E9
                                                                                                          cmp
                                                                                        movist: push
                                                                                                         push
push
push
c 1d
                    OOEB
                                                                                                                           ds
es
                    OOED
OOEE
OOEF
OOFO
OOF3
OOF5
OOFA
OOFC
OOFD
                                                                                                                                                              push
                                                                                                                           cs
                               0E
07
BF 0000 E
8E D9
8B F0
B9 0100
F3/ A5
07
                                                                                                                           cs
es
di,offset buffer
ds,cx
si,ax
cx,100h
                                                                                                         pop
mov
mov
                                                                                                                                                              ; and the default buffer ; and the source is the users buffer
                                                                                                                                                               ;move full sector
                                                                                                         mov
                                                                                                          rep
                                                                                                                            movsw
                                                                                                         pop
                    OOFE
                                                                                                         pop
                                                                                                                                                              restore registers
                    0100
                                                                                        redfnd1:
                    0100
0102
0105
0105
0107
0109
010B
                                 8C C9
B8 0000 E
                                                                                                                           CX,CS ; use our buffer ax,offset buffer; for read, and move after
                                                                                                         mov
                                 D1 E1
D1 E1
D1 E1
                                                                                                                           cx,1
cx,1
cx,1
cx,1
cx,ax
es:dmaadr,cx
                                                                                                         sh1
                                                                                                          sh 1
                                                                                                         sh1
sh1
add
                                 D 1
                                 03 C8
26: 89 OE 0000 E
                                                                                                                                                              ;make absolute address ;and put it in the packet
                                                                                                         mov
                                 2E: 8A 84 017F R
26: A2 0000 E
                                                                                                                           al,byte ptr c
es:fnccod,al
                                                                                                                                                            xdskfcn[si] ;get function code
;put code in packet
                    0119
                                                                                        ; now set ds to cs for local code
                    011D 1E
                                                                                        pzw1:
                                                                                                         push
                                                                                                                           ds
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                                                                                 02-20-84
                                                                                                                                                                                                                 PAGE
                                                                                                                                                                                                                                               1 - 8
                                011E 8C C8
0120 8E D8
0122 52
0123 58
0124 E8 018C R
0127 5A
0128 9C
0128 9C
012A 83 FA 04
012D 75 2A
012F B1 04
0131 D3 CE
0133 81 E5 000F
0137 25: 8A A4 0000 E
013C 80 FC 00
013F 74 0C
0141 80 FC 08
0144 75 05
0145 84 01
0148 EB 03 90
0148 B4 02
0140 28: 8B 1E 0000 E
0152 28: 88 27
0157 33 C0
0159 9D
                                                                                                                                                                                                              ax,cs
ds,ax ;set ds
dx ;save drive #
si ;get function code
execz80 ;Invoke Z80 and wait for status
dx ;recover function code
si ;recover drive #
;save flags around grand munging we do
dx,04 ;was this a media check?
eggsit ;if not, just bail out
cl,4 ;restore drive value
si,cl ;to its virgin state
si,ofh ;JIC any garbage
ah,es:byte ptr tformat[si];get media type
ah,0 ;is this an RX80?
fmtset ;just put 0 in buffer
an,ibm8 ;Is this an IBM 8 sector
oterfmt ;no,must be an IBM 9
ah,ibm8val ;save MSDDS code
ffmtset ;for users
ah,ibm9val ;assume IBM 9 sector for now
bx,es:dmaadr ;get pointer to xfer buffer
es:[bx],ah ;Save media type in buffer
dx,dx ;do move the buffer, but
ax,ax ;dont return a funny status
mov
mov
push
                                                                                                                                                                                                                  ax,cs
ds,ax
                                                                                                                                                                                   push
call
pop
pop
                                                                                                                                                                                    pushf
                                                                                                                                                                                   cmp
jnz
mov
ror
                                                                                                                                                                                    mov
                                                                                                                                                                                   cmp
jz
cmp
                                                                                                                                                      jmp
oterfmt:mov
fmtset: mov
mov
                                                                                                                                                                                    xor
                                                                                                                                                                                    xor
                                                                                                                                                      eggsit:
                                  popf
                                                                                                                                                                                    POP
                                                                                                                                                                                                                 bx ;restore users request buffer; save flags again byte ptr xcommand[bx],0 ;was this a read? outhere
                                                                                                                                                                                    pop
pop
                                                                                                                                                                                   pushf
cmp
jnz
                                                     1 E
0 8
5 6
                                  0183
0184
0185
0186
0186
0188
0188
0188
0177
0177
0177
0177
                                                                                                                                                                                    push
                                                                                                                                                                                                                  ds
                                                                                                                                                                                                                 as
es
si
di
es,xdmaseg[bx] ;put data where the user expects to see it
di,xdmaoff[bx] ;
                                                                                                                                                                                    push
                                                                                                                                                                                   push
push
mov
mov
push
                                                     55
57
8E 47 OA
8B 7F O8
OE
1F
                                                                                                                                                                                   mov
mov
pop
pop
                                                                                                                                                                                                                  ds
                                                     1F
BE 0000 E
B9 0100
F3/ A5
5F
5E
07
1F
                                                                                                                                                                                                                as
si,offset buffer
cx,100h
movsw
di
si
                                                                                                                                                    outhere:
popf
ret
                                  017B
017B 9D
017C CA 0002
                                                                                                                                                                                                                                               ;return flags
;called with INT (clean stack)
                                                                                                                                                                                                                2
                                  017F 13
                                                                                                                                                      XDSKFCN: db
                                                                                                                                                                                                                  OKRDCOM
```

02-20-84	PAGE 1-
db	OKWTEOM
db	OKWYFCOM
db	OKFMTCOM
db	OKCMCOM
db	OKVFYCOM
physdisk1 endp	
	db db db db

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                   02-20-84
                                                                                                                                  PAGE
                                                                                                                                                     1-10
page
                                                                                             ; Interrupt service from the Z80. The Z80 ; indicates it is done by setting Z80PKT == ; I88PKT, and interrupting us. Once we clear ; our interrupt, it then zeros the packet ; pointer. The reason for all this interlocking ; pointer fiddling is a mystery.
                    0185 0000
                                                                                            zpkt dw
                                                                                                                               ٥
                    0187 50
0188 06
0189 B8 ---- R
018C 8E C0
018E 26: 87 06 0000 E
0193 2E: A3 0185 R
0197 E4 00
0199 07
0198 CF
                                                                                                                                 ax
es
ax,seg z80seg
es,ax
ax,es:xfrpkt
cs:zpkt,ax
a1,intz80
es
ax
                                                                                            z80isr: push
push
mov
mov
                                                                                                                                                                  ; if the ptr is
;O, do nothing,
;see if packet back yet
;if zppkt still O, upper level not back
;clear the int.
                                                                                                              xchg
mov
in
pop
                                                                                                               pop
iret
                                                                                            ; AS in the ISR, we must play strange games with ;packet pointers. Set the i88 packet pointer, ;interrupt the Z80, when it clears it's intgerrupt, zero the packet pointer, then wait ;for an interrupt from the Z80.
                    019C
019C 26: C7 06 0000 E 0000 E
01A3 2E: C7 06 0185 R 0000
01AA 26: C6 05 0000 E 00
01B0 ES 00
                                                                                             execz80:
                                                                                                                                 es:xfrpkt,offset z80seg:packet ;set
cs:zpkt,0 ;set status to gone
es:byte ptr status,0
intz80,a1 ;start z80,
                                                                                                                                                                                                             ;set xfer packet ready
                                                                                                               mov
                                                                                                               mov
                                                                                                               out
                                                                                             ,
;Wait until the Z80 accepts the interrupt,
;then zero the packet pointer.
                    0182 26: 83 3E 0000 E 00
0188 75 F8
                                                                                                                                 word ptr es:xfrpkt,0
xz0
                                                                                             xzo:
                                                                                                               cmp
                                                                                                               jnz
                                                                                             ;Wait for the Z80 to interrupt us.
                    O1BA FA
O1BB 2E: 83 3E O185 R OO
O1C1 75 O4
O1C3 FB
O1C4 F4
                                                                                             x21:
                                                                                                               c 1 i
                                                                                                                                                                      ;lock out Z80,
;see if gone
;done.
                                                                                                                                 cs:zpkt,0
xz2
                                                                                                               cmp
                                                                                                               jne
sti
hlt
                                                                                                                                                                     ;wait for an ;interrupt.
                     01C5 EB F3
                                                                                                               jmp
                    01C7 FB
01C8 26: AO 0000 E
01CC 3C 00
01CE 75 02
01D0 F8
01D1 C3
                                                                                                               sti
                                                                                             x22:
                                                                                                               mov
                                                                                                                                  al,es:status
                                                                                                                                                                      : OK?
                                                                                                               cmp
jnz
clc
ret
                                                                                                                                                                       ;if no error,
;make sure carry bit clear
; and return
                                                                                                                                  x280err
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                  02-20-84
                                                                                                                                 PAGE 1-11
```

```
xz80err:
                                                                           mov si,0
mov al,es:exstat
cmp al,0
jnz x24
mov al,oCh
jmp short x25
                                                                                                                ;ptr to code,
;get raw machine status
;this may be zero if no hardware error
                                                                                                                ;return "general failure"
              01E1 DO CO
01E3 72 03
01E5 46
01E6 EB F9
                                                                           rol
jc
inc
                                                                                       a1,1
x23
                                                              x 2 4 :
                                                                                                                ;shift to Cy,
                                                                           inc si
jmp short x24
                                                                                                                ;next bit ...
              01E8 8A 84 01F9 R
01EC F9
01ED C3
                                                              x z 3 :
                                                                           mov
                                                                                                               ,get return
;code,
                                                                                       al,errcode[si]
```

```
02-20-84
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                                       PAGE
                                                                                                                                         1-12
                                                                                     page
;
;DEC RX-50 sector skew table. . Dnly
;used on tracks 2 to 79. Tracks 0 and 1 are
;unskewed.
                                                                                      ; skew == 2
skewtb1_2 label byte
                   OIEE
                   01EE
01EF
01F0
01F1
01F2
01F3
01F4
                               00
01
03
                                                                                                                                                          ;no sector 0,
                                                                                                       đb
                                                                                                       db
db
db
                                05
07
09
02
04
                                                                                                       đb
                                                                                                       db
                                                                                                       db
db
                                                                                                                         10
                                                                                      ;179x -> MSDOS disk error code translate table.
                   01F9
                                                                                      errcode label byte
                   01F8
01FA
01FB
01FC
01FD
01FE
01FF
                                                                                                                                         ;bit 7: not ready,
;bit 6: write protect,
;bit 5: write fault,
;bit 4: seek error,
;bit 3: CRC error,
;bit 2: lost data,
;bit 1: **Robin media**
;bit 0: busy
                                                                                                                        2
C
10
                                00
0A
                                                                                                       db
                                                                                                       db
db
db
                                06
04
08
07
02
                                                                                                       db
                                                                                                       db
                    0201
                                                                                      code ends
```

```
The Microsoft MACRO Assembler
DEC Rainbow Rx-50/280 disk driver
                                                                                                                                                                                                                                                  02-20-84
                                                                                                                                                                                                                                                                                                                          PAGE
                                                                                                                                                                                                                                                                                                                                                                        Symbols-1
  Segments and groups:
                                                                                           Name
                                                                                                                                                                                                                                 Size
                                                                                                                                                                                                                                                                              align
                                                                                                                                                                                                                                                                                                                          combine class
 GROUP
0201
0000
                                                                                                                                                                                                                                                                              BYTE
AT
                                                                                                                                                                                                                                                                                                                            DABTIC , CODE,
  Symbols:
                                                                                           N a m e
                                                                                                                                                                                                                                                                               Value
                                                                                                                                                                                                                                                                                                                           Attr
                                                                                                                                                                                                                                  Type
AUXDP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...AUXP...A
                                                                                                                                                                                                                                                                              0040
0042
0044
                                                                                                                                                                                                                                   Number
                                                                                                                                                                                                                                     Number
                                                                                                                                                                                                                                   Number
                                                                                                                                                                                                                                          WORD
                                                                                                                                                                                                                                                                               0000
                                                                                                                                                                                                                                                                                                                                                                        External
                                                                                                                                                                                                                                   Number
Number
Number
Number
                                                                                                                                                                                                                                                                               002E
0043
0000
                                                                                                                                                                                                                                                                               0004
                                                                                                                                                                                                                                  Number
Number
L NEAR
Number
V WORD
Number
                                                                                                                                                                                                                                                                               0005
  DECDISK.
DENSITY
DMAADR
DMAOFF
                                                                                                                                                                                                                                                                              0000
0000
0000
                                                                                                                                                                                                                                                                                                                           CODE
                                                                                                                                                                                                                                                                                                                                                                        Global
                                                                                                                                                                                                                                                                                                                                                                          External
   DMASEG
                                                                                                                                                                                                                                    Number
                                                                                                                                                                                                                                                                               OOOE
  DRIVE. .
DRVNO. .
DSKIO. .
                                                                                                                                                                                                                                    Number
                                                                                                                                                                                                                                                                               0001
                                                                                                                                                                                                                                                                             0001
0000
0065
0009
0159
                                                                                                                                                                                                                                    V BYTE
Number
                                                                                                                                                                                                                                                                                                                                                                        External
                                                                                                                                                                                                                                   NUMBER OF THE PROPERTY OF T
   DTL...
EGGSIT .
                                                                                                                                                                                                                                                                                                                          CODE
   ENN. . . . ERRCODE .
                                                                                                                                                                                                                                                                             0008
01F9
0045
019C
0000
0105
014D
0000
0007
                                                                                                                                                                                                                                                                                                                            CODE
   EXSTAT .
FMTFNDL .
FMTSET .
                                                                                                                                                                                                                                                                                                                                                                         External
                                                                                                                                                                                                                                                                                                                            CODE
 FMTSET
FMTSET
FNCCOD
GAPLEN
GSCR
HEAD
ISSPKT
IBMS
IBMS
IBMS
IBMS
IBMS
IBMS
                                                                                                                                                                                                                                                                                                                             CODE
                                                                                                                                                                                                                                                                                                                                                                          External
                                                                                                                                                                                                                                    Number
                                                                                                                                                                                                                                                                               0012
                                                                                                                                                                                                                                    V WORD
Number
Number
Number
                                                                                                                                                                                                                                                                              0000
0008
0001
                                                                                                                                                                                                                                                                                                                                                                        External
                                                                                                                                                                                                                                                                               0002
                                                                                                                                                                                                                                    Number
                                                                                                                                                                                                                                                                               0002
    INTZ80
KDP. .
KSP. .
LOGDRY
                                                                                                                                                                                                                                     Number
                                                                                                                                                                                                                                                                                0000
                                                                                                                                                                                                                                                                              0010
0011
00A4
                                                                                                                                                                                                                                    Number
                                                                                                                                                                                                                                  L NEAR
V BYTE
L NEAR
V BYTE
L NEAR
                                                                                                                                                                                                                                                                                                                            CODE
    MC_FLG .
                                                                                                                                                                                                                                                                               0000
                                                                                                                                                                                                                                                                                                                                                                         External
                                                                                                                                                                                                                                                                               COFS
                                                                                                                                                                                                                                                                                                                            CODE
                                                                                                                                                                                                                                                                               0000
014B
                                                                                                                                                                                                                                                                                                                                                                          External
                                                                                                                                                                                                                                                                                                                             CODE
```

he Microsoft MACRO Assembler C Rainbow Rx-50/280 disk driver	02-20-84	PAGE	Symbols-2
THERE	L NEAR 017B	CODE	
CKET	V BYTE 0000		External
CKET_ADR	L NEAR 0000		External
Y\$1	L NEAR OOSD L NEAR OOSO	CODE	
YS3	L NEAR OOS4	CODE	
YS2	L NEAR COSD	CODE	Global
TSDISKI	F PROC OOSD	CODE	Length =0118
NDP	Number 0041 Number 0043		
DFILE	Number 0043 Number 0064		
DFILE	L NEAR OIID	CODE	
CMCOM	Number 0015		·
FMTCOM	Number 0024 Number 0013		
RDCOM	Number 0013		
NTCOM	Number 0014		
WYFCOM	Number 0016		
OFNOL	L NEAR 0100 Number 0018	CODE	
50	Number 0018 Number 0000		
BOVAL	Number 0000		
CNO	V BYTE 0000		External
CSIZ	Number 000A		
CSIZ	Number 0003 L BYTE 01EE	CODE	
WED	L NEAR OOCS	CODE	
WED	Number 0041		
AREA2	Number 0042		
T	Number 0010 V BYTE 0000		
T	L NEAR COAC	CODE	External
DRMAT	V BYTE OOOO		External
A C V	Number 0002		
ACKN	V BYTE 0000 V BYTE 0000		External
RT	Number 0046		External
RT	Number 0040		
RSNT	Number 0001		
DMMAND	Number 0000		
PRSNT	Number 0006 Number 0002		
PRSNTMADFF	Number 0008		
MASEG	Number 000A		
RIVE	Number 0001 L NEAR 017F		
RPKT	L NEAR 017F V WORD 0000	CODE	External
HDRY	Number 0003		External
ECTOR	Number 0002		
RACK	Number 0004		
1	L NEAR 01B2 L NEAR 01BA	CODE	
2	L NEAR OICT	CODE	
	L NEAR 01E8	CODE	
• • • • • • • • • • • • • • • • • • • •	L NEAR O1E1	CODE	
ne Microsoft MACRO Assembler C Rainbow Rx-50/280 disk driver	02-20-84	PAGE	Symbols-3
S	L NEAR OIEC	CODE	
BOERR	L NEAR 01D2 Number 0047	CODE	
DISR	L NEAR 0187	CODE	Global
KFCN	L BYTE 0065	CODE	
	L NEAR 0197	CODE	
1	L WORD 0185 L NEAR 002F	CODE	
KT		C 3 D E	
α			
(T	<u>-</u> .		
(T			
(T	<u>-</u> .		

.

Symbol Cross Reference		(# is d	efiniti	on)
AUXDP	83#			
AUX_PRN	73#			
BUFFER	18#	284	306	3 8 3
CGROUP	11 59#	142	142	
CODE	11 72#	141#	141	489
COUNT	98# 100# 101#	195		
DECDISK	17	150#		
DENSITY	102# 138#	168	313	342
DMADFF	107#	192	.	
DRIVE	87# 132#	155 160	286	
DTL	80# 105#			
EGGSIT	330 104#	346#		
ERRCODE	451 74#	478#		
EXECZ80	203 137#	325 440	410#	
FMTFNDL	286	307#		
FMTSET	336 130#	340 199	342# 316	
GAPLEN	103#			
HEAD	110#			
	125#			
IBM8	43#	337 339		
IBM9	44#	341		
INTZ80	29# 81#	399	414	
KDP	82#			
LOGDRY	259	262#		
MC_FLG	18# 287#	242	244	
NSECT	135#	181	276	
DEC Rainbow Rx-50/280 disk drive	r		*****	
Symbol Cross Reference OTERFMT	338 353 129# 118#	(# is do 341# 371# 411	efiniti	on)
Symbol Cross Reference OTERFMT	338 353 129#	341# 371#	efiniti	on)
Symbol Cross Reference OTERFMT. OUTHERE	338 353 129# 118# 232 238 241 17 228# 84# 88# 58#	341# 371# 411 247# 242# 243# 227#	ofiniti 379	on)
Symbol Cross Reference OTERPMT. OUTHERE	338 353 129# 118# 232 238 241 17 228# 84# 58# 320 24# 27# 22#	341# 371# 411 247# 242# 243# 227# 381	379 375	on)
Symbol Cross Reference OTERFMT. OUTHERE. PACKET. PACKET PACKET. PHYS1. PHYS2. PHYS3. PHYS01SK PHYSD1SK	338 353 129# 118# 232 238 241 228# 88# 58# 320# 24# 27#	341# 371# 411 247# 242# 243# 227# 381	379	on)
Symbol Cross Reference OTERFMT. OUTHERE	338 353 129# 118# 232 238 241 17 228# 88# 320# 24# 27# 28# 28#	341# 371# 411 247# 242# 243# 227# 381	379 375 376	on)
Symbol Cross Reference DTERFMT. DUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYDISK PHYD	338 353 129# 118# 232 241 17 228# 88# 58# 58# 24# 27# 28# 23# 25#	341# 371# 411 247# 242# 243# 227# 381 209 378 209 380 209 209	379 375 376	en)
Symbol Cross Reference DTERFMT DUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYSDISK PRNDP PROFILE PZW1 OKCMCOM. OKFMTCOM OKKMCOM. OKFMTCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM SECNO.	338 353 129# 118# 238 241 17 228# 84# 588# 580# 320# 27# 22# 23# 25# 25# 25# 25# 25# 25# 25# 25# 25# 25	341# 371# 411 247# 242# 243# 227# 381 209 378 209 380 209 209	379 375 376	on)
Symbol Cross Reference DTERFMT DUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYDISK PHYD	338 353 129# 118# 232 238 241 17 228# 88# 520# 24# 22# 25# 25# 287 277# 48# 133# 108#	341# 371# 411 247# 242# 242# 227# 381 209 378 209 300 209 304#	378 375 376 377	on)
Symbol Cross Reference DTERFMT DUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYDISK PHYD	338 353 129# 118# 232 248 17 228# 858# 320# 277# 28# 25# 25# 287 77# 458# 133# 108# 175 271	341# 371# 411 247# 243# 243# 227# 381 209 378 209 380 209 209	379 375 376 377	on)
Symbol Cross Reference OTERFMT. OUTHERE. PACKET. PACKET. PACKET. PHYS.2. PHYS.3. PHYS.01SK. PROP.	338 353 129# 118# 232 238 241 17 228# 88# 320# 24# 228# 25# 287 28# 48# 138# 195 271 701#	341# 371# 411 247# 242# 243# 227# 381 209 378 209 380 209 209 304#	378 375 376 377	on)
Symbol Cross Reference OTERFMT. OUTHERE	338 353 129# 118# 232 238 241 17 228# 858# 320# 24# 25# 25# 25# 25# 25# 46# 133# 175 271 70# 109# 1109#	341# 371# 411 247# 243# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274#	378 375 376 377	on)
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS1 PHYS3 PHYSDISK PROPORT OKCHCOM OKFOOM OKFOOM OKFOOM OKFOOM OKFOOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM OKWYFCOM SECSIZ	338 353 129# 118# 2238 241 17 228# 88# 320# 24# 25# 25# 25# 25# 46# 133# 108# 175 71# 108# 175 281 281 281	341# 371# 411 247# 243# 243# 227# 381 209 378 209 300 209 209 209 209 209 209 209 209	379 375 376 377 275	on)
Symbol Cross Reference OTERFMT OUTHERE	338 353 129# 118# 232 238 241 17 228# 858# 320# 24# 25# 25# 25# 25# 25# 46# 133# 175 271 70# 109# 1109#	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274# 413 288# 334	379 375 376 377 275 482# 432	on)
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET PACKET_ADR PHYS1 PHYS2 PHYS1 PHYS2 PHYSDISK PROPO PROPO PROPO PROPO PROFILE PZW1 OKCMCOM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKWYFCOM REDFNDL ROM REDFNDL ROM RX50 RX50VAL SECON S	338 353 129# 118# 232 238 241 17 284# 88# 320# 24# 22# 22# 25# 25# 25# 48# 133# 175 271 71# 109# 175 271 71# 109# 131# 281	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 209 209 209 209 209 34#	379 375 376 377 275 462#	on)
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYSDISK PHYDISK PHYSDISK	338 353 129# 118# 232 238 241 17 228# 88# 320# 24# 228# 25# 287 28# 25# 287 32# 48# 133# 195 271 701# 109# 175 109# 1314 134# 134#	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274# 413 288# 334	379 375 376 377 275 482# 432	on)
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PHYDDISK PROPO ROM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKWYFCOM OKWYFCOM REDFNDL ROM RXSO RXSOVAL SECSIZ SECTOR SECSIZ SECSIZ SECTOR SECSIZ SECTOR SECSIZ SECSIZ SECTOR SECSIZ SECSIZ SECTOR SECSIZ SE	338 353 129# 118# 232 238 241 17 228# 88# 320# 24# 228# 25# 25# 287 277# 48# 133# 175 70# 109# 175 109# 1314 1344 1344 1344 1344	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274# 413 288# 334	379 375 376 377 275 482# 432	on)
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS3 PHYSDISK PHYSDISK PRNDP PRNDP PRNP PROFILE PZW1 OKCMCOM OKFMTCOM OKFMTCOM OKFMTCOM OKFMTCOM OKVFYCOM OKWYFCOM REDFNDL ROM REDFNDL ROM REDFNDL ROM RESCOR SECSIZ SECTOR SECSIZ SECTOR SECSIZ SECTOR SECNIZ SECTOR STATUS STORV TFORMAT TRACK UART	338 353 129# 118# 232 238 241 17 28# 88# 320# 24# 22# 22# 22# 22# 25# 25# 25# 210 32# 25# 25# 25# 25# 25# 25# 25# 25# 25# 2	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274# 413 288# 334	379 375 376 377 275 482# 432	on)
Symbol Cross Reference OTERFMT. OUTHERE	338 353 129# 118# 232 238 241 17 284# 88# 320# 24# 25# 25# 25# 25# 25# 25# 25# 25# 25# 25	341# 371# 411 247# 243# 227# 381 209 378 209 380 209 209 304# 177 187 273 274# 413 288# 334	379 375 376 377 275 482# 432	on)
Symbol Cross Reference OTERPMT. OUTHERE	338 353 129# 118# 232 238 241 17 284# 88# 320# 247# 252# 252# 252# 252# 2774 28# 108# 175 2710# 109# 131# 281 320# 320# 320# 320# 320# 320# 320# 320#	341# 371# 411 247# 243# 227# 381 209 378 209 209 209 209 209 209 209 209 209 209	379 375 376 377 275 482# 432 178 274	
Symbol Cross Reference OTERFMT OUTHERE PACKET PACKET_ADR PHYS1 PHYS2 PHYS2 PHYSDISK PHYSDIS	338 353 129# 1138# 238 2417 228# 858# 320# 247# 258# 257 267 277 271 271 271 271 271 271 271 271 27	341# 371# 411 247# 243# 227# 381 209 378 209 209 209 209 304# 177 187 273 274# 413 285# 334 179	378 376 377 275 482# 432 178 274	

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DEC Rainbow Rx-50/280 disk driver
 Symbol Cross Reference
                                                                    (# is definition)
                                                                                                              Cref-3
                                                           35#
34#
36#
419#
424#
                                                                        260
269
267
420
429
431#
451#
446#
452#
XPHDBV
XPHDRV
XSECTOR.
XTRACK.
X20
X21
X22
X23
X23
X24
X25
X24
X25
                                                            426
447
442
444
434
                                                                                    449
XZ80ERR.
Z80. . .
Z801SR .
Z80SEG .
ZDSKFCN.
                                                            76#
17
115#
198
399#
391#
169
              393#
116
209#
                                                                                                151
                                                                                     142
                                                                                                             253
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                                                                                                                                     411
ZII. . . ZPKT . . ZWO. . .
                                                                        398
171
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```

```
The Microsoft MACRO Assembler
Rainbow Interrupt fix routines
                                                                                                                         02-20-84
                                                                                                                                                              PAGE
                                                                                                                                        PAGE
TITLE
NAME
                                                                                                                                                              60,132
Rainbow Interrupt fix routines
INTFIX
23456789111111111122222222233333333334444444445555555
                                                                                                                                        COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
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                                                                                                                  cgroup group code
                                                                                                                  ;
;Placed at the 2x interrupts, dispatch to
;either the proper MSDOS interrupt or
;the hardware vector, determined by looking
;at the last executed instruction.
                                                                                                                  public int20,int21,int24,int25,int26,int27
public dos20vec,dos21vec,dos24vec
public dos25vec,dos26vec,dos27vec
                                                                                                                 code segment byte public 'code'
assume cs:cgroup,ds:nothing
include iodef.ash
;Rainbow Interrupt numbers.
;1-Apr-83 sgs added dskio int vector
;19-Mar-83 sgs added profile int vector [user clock]
;the int profile is called by the RTC interrupt service for each tick.
;The ax and ds register don't need to be saved [done in clkisr].
                          0000
                          = 0064
= 002C
= 0065
                                                                                                                  profile equ
clk_int equ
dskio equ
                                                                                                                                                              64h
2ch
65h
                                                                                                                                                                                    ;100. User interface to clock interrupt
;60Hz int,
;direct disk io for format
                                                                                                                  ;
;17-Mar-83 sgs changed to include int 20-26
;interrupt 22-24h are duplicated at 42-44h for consistency.
;These are the relocated Rainbow interrupts.
;interrupts, 20h-26h moved to 40h-46h
;ATTENTION Modules IDINIT and REINIT may have to be
changed if int vectors 40h to 46h are changed
                                                                                                                 vert equ
spare41 equ
spare42 equ
comdma equ
aux_prn equ
excom equ
uart equ
z80 equ
                           = 0040
= 0041
= 0042
= 0043
= 0044
                                                                                                                                                               40h
41h
42h
43h
                                                                                                                                                                                      ;new vert. freq.
                                                                                                                                                                                      ;DMA ctrl optional comm. board
                                                                                                                                                                                      ;DMA ctrl optional com;
;7201 comm./printer
;extented comm. option
;new UART vector,
;280 interrupt,
;new ROM access,
                                                                                                                                                               44h
45h
46h
47h
                            = 0045
= 0046
= 0047
                            . 0018
                                                                                                                   rom
                                                                                                                                          equ
                                                                                                                                                                18h
                                                                                                                   ;DEC Rainbow IO port stuff.
                            = 0010
= 0011
                                                                                                                  kdp
ksp
                                                                                                                                         equ
                                                                                                                                                                                      ;8251 data port,
;8251 status,
                                                                                                                                         equ
```

```
The Microsoft MACRO Assembler Rainbow Interrupt fix routines
                                                                                                   02-20-84
                                                                                                                                  PAGE
page
intmac macro nn,hwi
                                                                                             ;
;Attempt to figure out where to go. The "soft
;parts" (our ambiguity) are marked with <*>.
                                                                                             flagann db
                                                                                             intann proc far
dec cs:flagann
jnz iannaa
int hwi
iret
                                                                                                                                                                      ;(*) if flag was
;(*) set, must
;be hardware
;service it.
                                                                                             ;
;Dont know what type of interrupt yet. NOTE:
;The STI is done first: any pending interrupt
;will NOT be serviced until the MOV FLAG,1 is
;executed. Absolute minimum ambiguity.
                                                                                                                                 cs:flagann,1 ;(*) enable ints,
;(*) set flag,
;off again,
cs:flagann,0 ;if flag clear,
iannab ;it's hardware
                                                                                             iannaa: sti
                                                                                                               mov
cli
cmp
jnz
                                                                                             ;
;Hmm. Flag cleared. Either a hardware interrupt
;Or a Software interrupt that got interrupted
;in our soft parts. All we can do is assume
;it was a hardware interrupt. Goodbye.
                                                                                             , ;
;We reenabled interrupts, and didn't get inter-
;rupted. Must be software.
                                                                                             ;
i&nn&b: mov
jmp
                                                                                                                                 cs:flag&nn,0
dword ptr dos&nn&vec
                                                                                            int&nn endp
endm
```

```
The Microsoft MACRO Assembler Rainbow Interrupt fix routines
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                                                                                                                                                                               PAGE
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151
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                                                                                                                            page
                                                                                                                              intmac
flag20 db
int20 proc far
dec
                                                                                                                                                                              20, vert
0
                          0018 00
0018
0019 2E:
001E 75
0020 CD
                                          2E: FE OE OO18 R
75 O3
CD 40
CF
FB
2E: C5 O5 OO18 R O1
FA
2E: 80 3E OO18 R OO
75 O1
CF
2E: C8 O5 OO18 R OO
2E: FF 2E OOO0 R
                                                                                                                                                                                                                               ;(*) if flag was
;(*) set, must
;be hardware
;service it.
;(*) enable ints,
;(*) set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                                                                                                               cs:flag20
                                                                                                                                                     dec
jnz
int
iret
sti
mov
cli
                                                                                                                                                                               i20a
vert
                           0020
0022
0023
0024
002A
002B
0031
0033
0034
003A
                                                                                                                                                                               cs:flag20,1
                                                                                                                                                                              cs:flag20,0
i20b
                                                                                                                                                      CMP
                                                                                                                                                      jnz
iret
mov
                                                                                                                     + 120b: mov
+ 120b: mov
+ jmp
+ int20 endp
                                                                                                                                                                               cs:flag20,0
dword ptr dos20vec
```

```
The Microsoft MACRO Assembler
Rainbow Interrupt fix routines
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                                                                                                                155
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                                                                                                                       page
                          003F 00
0040
0040 2E: FE 0E 003F R
0045 75 03
0047 CD 41
0048 EF
0048 2E: C6 08 003F R 01
0051 FA
0052 2E: 80 3E 003F R 00
0058 75 01
005A CF
006B 2E: C6 08 003F R 00
005B 2E: FF 2E 0004 R
                                                                                                                                                                    21,41h
0
                                                                                                                                                                                                                     ;(*) if flag was
;(*) set, must
;be hardware
;service it.
;(*) enable ints,
;(*) set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                                                                                                      cs:flag21
i21a
41h
                                                                                                                                                                       cs:flag21,1
                                                                                                                                                                       cs:flag21,0
i21b
                                                                                                                 + i21b: mov
+ i21b: mov
+ jm
+ int21 endp
                                                                                                                                                                       cs:flag21,0
dword ptr dos21vec
                                                                                                                                                jmp
```

```
The Microsoft MACRO Assembler Rainbow Interrupt fix routines
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                                                                                                                                                                                                                     1 - 7
172
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                                                                                                                                     page
                            0086 00
0087 2E: FE 0E 0086 R
008C 75 03
008E CD 44
0070 CF
0071 FB
0072 2E: C6 06 0086 R 01
0078 FA
0079 2E: 80 3E 0086 R 00
007F 75 01
0081 CF
0082 2E: FF 2E 0008 R
                                                                                                                             intmac
+ flag24 db
+ int24 proc far
dec
+ jnz
+ int
+ iret
+ i24a: sti
+ mov
+ cmp
+ jnz
                                                                                                                                                                                      24,44h
0
                                                                                                                                                                intmac
                                                                                                                                                                                                                                               ;(*) if flag was
;(*) set, must
;be hardware
;service it.
;(*) enable ints,
;(*) set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                                                                                                                           cs:flag24
i24a
44h
                                                                                                                                                                                           cs:flag24,1
                                                                                                                                                                                          cs:flag24,0
i24b
                                                                                                                              + jnz
+ iret
+ i24b: mov
+ jmp
+ int24 endp
                                                                                                                                                                                          cs:flag24,0
dword ptr dos24vec
```

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The Microsoft MACRO Assembler Rainbow Interrupt fix routines
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189
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197
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200
201
202
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204
205
                                                                                                page
                                                                                            page intmac 25,45h 
+ flag25 db 0 
+ int28 proc far 
+ dec cs:flag 
+ jnz i25a 
+ int 45h 
+ int
                     008D 00
                     008E
008E
008E
0093
0095
                     cs:flag25
i25a
45h
                                                                                                                                                                              ;<*> if flag was
;<*> set, must
;be hardware
                                                                                                                                                                              ; service it.
;(*) enable ints,
;(*) set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                                                    iret
sti
mov
cli
                                                                                            + 125a:
+
+
+
                                                                                                                                        cs:flag25,1
                                                                                            + cli
+ cmp
+ jnz
+ iret
+ 125b: mov
+ jmp
+ int25 endp
                                                                                                                                       cs:flag25,0
i25b
                                                                                                                                       cs:flag25,0
dword ptr dos25vec
```

```
The Microsoft MACRO Assembler Rainbow Interrupt fix routines
                                                                                                                                                 02-20-84
                                                                                                                                                                                            PAGE
                                                                                                                                                                                                                       1 - 9
                                                                                                                                        page
;Keyboard interrupt. Set a flag, reenable
;interrupts. If flag gets cleared, it was
;hardware.
206
207
208
209
210
211
212
213
214
215
216
217
218
219
220
221
222
                                                                                                                                ; int mac 25, uart + flag25 db 0 + int25 proc far + dsc cs:flag2 i 25a
                             OOB4 OO
OOB5
OOB5 2E: FE OE OOB4 R
OOBA 75 O3
OOBC CD 46
OOBE FB
OOC0 2E: C6 O6 OOB4 R O1
OOC6 FA
OOC7 2E: 80 3E OOB4 R O0
OOC7 75 O1
OOCF CF
OOD0 2E: C6 O6 OOB4 R OO
OOD0 2E: FF 2E OO10 R
OODB
                                                                                                                                                                                                                                                  ;(*) if flag was
;(*) set, must
;be hardware
;service it.
;(*) enable ints,
;(*) set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                                                                                                                             cs:flag26
i26a
uart
                                                                                                                                                                dec
jnz
int
iret
sti
mov
cli
                                                                                                                                                                                             cs:flag26,1
                                                                                                                                                                                            cs:flag26,0
i26b
                                                                                                                                + cmp
+ jnz
+ iret
+ i26b: mov
+ jmp
+ int26 endp
223
224
225
226
                                                                                                                                                                                             cs:flag26,0
dword ptr dos26vec
```

```
The Microsoft MACRO Assembler ainbow Interrupt fix routines
                                                                                           02-20-84
                                                                                                                       PAGE
                                                                                                                                        1-10
                                                                                     page
;280 interrupt. Set the flag, i
;reinterrupted, it was the Z80.
if we get
                                                                                                       intmac 27,280
                                                                                     flag27 db
int27 proc far
dec
jnz
int
                    00DB 00
                   00DC
00DC
00E3
00E5
00E5
00EE7
00EE
00FF
00FF
00FD
                              2E: FE OE OODB R
75 03
CD 47
CF
FB
                                                                                                                       cs:flag27
i27a
z80
                                                                                                                                                         ;<*> if flag was
;<*> set, must
;be hardware
                                                                                                                                                         ;De naroware;
;Service it.
;<*> enable ints,
;<*> set flag,
;off again,
;if flag clear,
;it's hardware
                                                                                 +
+ 127a:
+
                                                                                                       iret
sti
mov
cli
                                FS 2E: CS 06 00DB R 01 FA 2E: S0 3E 00DB R 00 75 01 CF
                                                                                                                       cs:flag27,1
                                                                                                                       cs:flag27,0
i27b
                                                                                                       cmp
                                                                                 +
+
+ 127b:
                                                                                                      jnz
iret
mov
jmp
                                                                                                                       cs:flag27,0
dword ptr dos27vec
                                2E: C8 06 00DB R 00
2E: FF 2E 0014 R
                                                                                  + int27 endp
                    0102
                                                                                     code ends
```

```
The Microsoft MACRO Assembler
Rainbow Interrupt fix routines
                                                                                                     02-20-84
                                                                                                                                   PAGE
                                                                                                                                                      Symbols-1
  Macros:
                                      Name
                                                                                              Length
  Segments and groups:
                                      N a m e
                                                                                              Size
                                                                                                                 align
                                                                                                                                   combine class
 GROUP
0102
                                                                                                                 BYTE
                                                                                                                                   PUBLIC 'CODE
  Symbols:
N a m e

AUXDP.
AUXP
AUX PRN
CLK_INT
CDMDMA
DDS20VEC
DDS21VEC
DDS24VEC
DDS25VEC
DDS25VEC
DSS28VEC
DSS26VEC
DSS10
EXCOM.
FLAG20
FLAG21
FLAG225
FLAG27
120A
121B
124A
125A
125B
124A
125B
124A
125B
125A
125B
1126B
121A
121B
1126B
1127A
1127B
11NT20
1NT21
1NT25
1NT20
1NT25
1NT25
1NT26
1NT27
KDP.
                                                                                                                 Value
                                                                                              Type
                                                                                                                                   Attr
                                                                                                                 0040
                                                                                              Number
                                                                                               Number
                                                                                                                 0042
                                                                                              Number
Number
Number
Number
L DWORD
L DWORD
                                                                                                                0044
002C
0043
0000
0004
                                                                                                                                   CODE
                                                                                                                                                     Global
Global
Global
Global
Global
Global
                                                                                              L DWORD
L DWORD
L DWORD
L DWORD
                                                                                                                0008
000C
0010
0014
                                                                                                                                   CODE
                                                                                              0065
                                                                                                                 0045
                                                                                                                 0018
003F
0066
008D
                                                                                                                                   CODE
                                                                                                                                   CODE
CODE
CODE
                                                                                                                 00B4
                                                                                                                 00DB
0023
0034
004A
                                                                                                                                   CODE
CODE
CODE
CODE
                                                                                                                 005B
                                                                                                                 0071
                                                                                                                                    CODE
                                                                                                                 0082
0098
00A9
                                                                                                                                   CODE
CODE
CODE
                                                                                                                 OOBF
                                                                                              L NEAR
L NEAR
L NEAR
F PROC
F PROC
F PROC
F PROC
F PROC
                                                                                                                                   CODE
                                                                                                                 CODO
                                                                                                                 00D0
00E6
00F7
0019
0040
00B7
00B5
00DC
                                                                                                                                                     Global
Global
Global
Global
Global
                                                                                                                                                                        Length
Length
Length
Length
Length
Length
                                                                                                                                                                                        :0026
:0026
:0026
:0026
:0026
                                                                                                                                   CODE
CODE
CODE
                                                                                                                                                       Global
                                                                                               Number
```

Rainbow	rosof Inter									02-2	0-84	PAGE	Symb	015-2
KSP PRMDP PRMPP PROPILE. ROM SPARE41. SPARE42. UART. VERT. WPRSNT. XCPRSNT. Z80 Warning	Sever	•							N N N N N N	umber umber umber umber umber umber umber umber umber	0041 0043 0084 0018 0041 0042 0048 0040 0001			
Errors 0	Error O	s												
Rainbow Symbol		-			out	i n	es		ſ	# is d	efiniti	ion)	Cref	- 1
AUXDP AUXP AUX_PRN.						:	 	5	6# 8# 6#					
CGROUP . CLK_INT. CODE COMDMA .	<i>.</i> 	: :				:		3	1 2# 1 5#	23 22#	22	248		
DOS 20VEC							 		9 9	70# 71# 72#	153 170 187			
DOS 24 VEC						:	 	2 2 2		73# 74# 75#	204 225 245			
DOS24VEC DOS25VEC DOS26VEC DOS27VEC				:					7#					
DOS24VEC DOS25VEC DOS26VEC DOS27VEC DSKIO EXCOM			: : 						0#	142	147	149	152	
DOS24VEC DOS25VEC DOS26VEC DOS27VEC DSKIO. EXCOM. FLAG20 FLAG21 FLAG24 FLAG25 FLAG25						:		14	0# 7# 4#	142 159 176 193 214	164 181 198	149 166 183 200 221	169 186 203	
DOS24VEC DOS28VEC DOS28VEC DOS27VEC DOSCOM FLAG20 FLAG21 FLAG24 FLAG25 FLAG25 FLAG27 I20A								14 15 17	0# 7# 4# 1# 2# 2#	159 176 193	164 181	166 183 200	169 186	
D05249EC D0525VEC D0525VEC D0527VEC D5K10. EXCOM. FLAG20 FLAG21 FLAG24 FLAG25 FLAG25 FLAG27 I20A I21B I21A I21B I24A								14 15 17 19 21 23 14 15 16 16 17	0# 7# 4# 1# 2# 3 0 0 7	159 176 193 214 234 145# 152# 169# 180# 186#	164 181 198 219	166 183 200 221	169 186 203 224	
DOS 24 YEC DOS 25 YEC DOS 25 YEC DOS 25 YEC DOS 27 YEC								145 157 179 213 145 166 178 190 212	074# 14# 122 3007744152	159 176 1934 2134 15339 1699 1867 1887 1973 2014 2014 2014 2014 2014 2014 2014 2014	164 181 198 219	166 183 200 221	169 186 203 224	
D05249EC D0525VEC D0525VEC D0527VEC D0527VEC D5K10. EXCOM. FLAG20 FLAG21 FLAG25 FLAG25 FLAG27 I208 I218 I248 I248 I248 I258 I258								145 179 1179 123 145 118 118 120 122 123 145 145 145 145 145 145 145 145 145 145	0744# 4## 30077441525	159 176 1976 1914 234 1452 1452 1452 1452 1452 1452 1452 145	164 181 198 219	166 183 200 221	169 186 203 224	

54# 55# 57# 59# 31#

50# 43#

KDP.
KSP.
PRMDP.
PRNPP.
PROFILE.

ROM.
SPARE41.
SPARE42.

```
The Microsoft MACRO Assembler 02-20-84
DEC Rainbow Interrupt vector initialization
                                                                                                                                                                                           PAGE
                                                                                                                                                                PAGE
TITLE
NAME
                                                                                                                                                                                          60,132
DEC Rainbow Interrupt vector initialization
REINIT
23456789111111111122222222333333333334444444445555555
                                                                                                                                                                COMPANY CONFIDENTIAL Copyright (C) 1983 Digital Equipment Corporation All rights reserved.
                                                                                                                                     caroup group code
                                                                                                                                       ; Copies the MSDDS interrupt vectors; from the 2x block to the Fx block, installs; the magic routine at 2x, which will determine; the interrupt source, and dispatch to the right
                                                                                                                                     public re_init,r100,xoption
                                                                                                                                                             int 20: near, int 21: near
int 25: near, int 24: near
int 26: near, int 27: near
dos 20vec: dword, dos 21vec: dword
dos 25vec: dword, dos 24vec: dword
dos 25vec: dword, dos 27vec: dword
                                                                                                                                       extrn
                                                                                                                                       extrn
extrn
extrn
extrn
                                                                                                                                     code segment byte public 'code'
assume cs:cgroup,ds:cgroup
include iodef.ash
;Rainbow Interrupt numbers.
;1-Apr-83 sgs added dskie int vector
;19-Mar-83 sgs added profile int vector [user clock]
;the int profile is called by the RTC interrupt service for each tick.
;The ax and ds register don't need to be saved [done in clkisr].
                              0000
                                                                                                                           C; 1-Apr-83 sg
; 19-Mar-83 s
C; the int pro
; The ax and
C;
C profile equ
C clk_int equ
C dskio equ
C;
C; 17-Mar-83 s
C; interrupt 2
C; These are t
C; interrupts,
C; ATTENTION
C; CATTENTION
C yert equ
C spare41 equ
C spare42 equ
C comdma equ
C aux_prn equ
C aux_prn equ
C excom equ
uart equ
                                                                                                                                     ;
profile equ 64h ;100. user interface to clock interrupt
clk_int equ 2ch ;80Hz int,
dskio equ 65h ;direct disk io for format
                              = 0084
= 002C
= 0085
                                                                                                                                     17-Mar-83 sgs changed to include int 20-26; interrupt 22-24h are duplicated at 42-44h for consistency. These are the relocated Rainbow interrupts.; interrupts, 20h-26h moved to 40h-46h; ATTENTION Modules IDINIT and REINIT may have to be changed if int vectors 40h to 46h are changed
                                                                                                                                                                                                                 ;new vert. freq.
                               = 0040
= 0041
                                                                                                                                                                                           40h
                                                                                                                                                                                            41h
42h
43h
44h
45h
                               : 0041
: 0042
: 0043
: 0044
: 0045
                                                                                                                                                                                                           ;DMA ctrl optional comm. board
;7201 comm./printer
;extented comm. option
;new UART vector,
                                                                                                                                                                                            46h
```

```
The Microsoft MACRO Assembler 02-20-84 DEC Rainbow Interrupt vector initialization
                                                                                                             PAGE
                                                                                                                             1 - 2
                                                                                                                             ; Z80 interrupt,
; new ROM access,
5675890612364566768971
                                                                              ;DEC Rainbow ID port stuff.
                 = 0010
= 0011
= 0040
= 0041
= 0042
= 0043
                                                                             kdp
ksp
auxdp
prndp
auxp
                                                                                             edn
edn
edn
                                                                                                             10h
11h
40h
41h
42h
                                                                                                                            ;8251 data port,
;8251 status,
;7201 data,
                                                                                                                            ;7201 command.
                                                                                             equ
                                                                             ; Values in XOPTION
                                                                        C ;
C wprsnt equ
C xcprsnt equ
                                                                                                                             ;Winnie preset
;XCOMM present
                 : 0001
: 0002
                                                                                                             01
02
```

```
The Microsoft MACRO Assembler 02-20-84 DEC Rainbow Interrupt vector initialization
                                                                                                  PAGE
                                                                                                              1 - 3
page
               0000
                                                                       re_init proc far
                                                                       ;
Copy the DOS interrupts to a safe place, then
;install our magic routines there.
                0000
0001
0002
0003
0004
0005
                         FA
50
53
51
52
56
57
1E
06
                                                                                     c 1 i
                                                                                     push
                                                                                                   ax
bx
                                                                                     push
push
                                                                                                   cx
dx
                                                                                     push
push
push
push
                                                                                                   si
di
ds
                0005
0007
0008
               OOOS OE
OOOA 1F
OOOB F6 OS OO77 R FF
OO10 74 O3
OO12 EB 59 90
                                                                                     push
                                                                                                   CS
                                                                                     pop
test
jz
jmp
                                                                                                  ds
ds
byte ptr r100,0ffh
r100a
skip
                                                                                                                                             ; check 100A or 100B
                                                                       ;Copy INT 20, 21, 24, 25, 26 and 27 vectors to the BIOS;dword places. dosxxvec must be squential [intfix.asm]
                                                                                                  ax,0
ds,ax
cs
es
                0015 B8 0000
0018 8E D8
001A 0E
001B 07
                                                                                                                  ;DS:= low mem,
                                                                                      mov
                                                                                     push
                                                                                     pop
                                                                                                                              ; ES: BIOS,
                001C FC
001D B9 0004
0020 BE 0080
0023 BF 0000 E
0026 F3/ A5
                                                                                     c 1 d
                                                                                                   cx,2*2 ;2 vectors
si,20h*4 ;save INT 20,21
di,offset dos20vec ;in our CS:,
                                                                                     mov
                                                                                     MOV
                                                                                      rep movsw
                0028 B9 0008
002B BE 0090
002E BF 0000 E
0031 F3/ A5
                                                                                                   cx,4*2 ;4 vectors
si,24h*4 ;save INT 24, 25, 26, 27
di,offset dos24vec ;in our CS:,
                                                                                      mov
                                                                                     mov
                                                                                     rep movsw
                                                                       ;Install our drivers in the interrupt vectors.
                0033 B8 0000
0036 BE CO
                                                                                                                            ;ES:= low mem,
                                                                                     mov
                                                                                     mov
                0038 BF 0080
0038 B8 0000 E
003E AB
003F 8C C8
0041 AB
                                                                                                   di,20h*4 ;install ours, ax,offset int20
                                                                                     mov
                                                                                      mov
stosw
                                                                                                   ax,cs
                                                                                      stosw
                0042 B8 0000 E
0045 AB
                                                                                                  ax, offset int 21
                                                                                     stosw
```

```
The Microsoft MACRO Assembler
DEC Rainbow Interrupt vector initialization
                                                                                                  PAGE
                                                                           02-20-84
               0048 SC C8
                                                                                                ax,cs
                                                                                    mov
stosw
128
139
130
133
133
133
133
133
133
141
142
144
144
144
144
144
145
146
147
                                                                      restore original vector in 22, 23; msdos changed them at init time.
               0049 B9 0004
004C BE 0108
004F F3/ A5
                                                                                    mov
                                                                                                 Cx.2*2
                                                                                                                              :2 vectors
                                                                                                                              duplicated vectors
                                                                                    mov
                                                                                                  si.42h+4
                                                                                    rep movsw
                                                                     di now points to int 24h
               0051
0054
0055
0057
0058
                         B8 0000 E
AB
8C C8
AB
B8 0000 E
                                                                                    mov
                                                                                                  ax, offset int 24
                                                                                    stosw
                                                                                    mov
stosw
                                                                                                  ax,cs
                                                                                                  ax, offset int 25
                                                                                    mov
                          AB
8C C8
AB
                                                                                    stosw
                005B
                005C
005E
005F
0062
                                                                                                  ax,cs
                                                                                    mov
stosw
mov
stosw
                                                                                                  ax, offset int 26
                          AB
8C C8
                                                                                                  ax,cs
                                                                                    mov
                0065
                          AB
                                                                                    stosw
                0066
0069
006A
006C
                          B8 0000 E
                                                                                    mov
stosw
                                                                                                  ax, offset int 27
151
152
153
154
155
156
157
158
159
160
                         AB
8C C8
AB
                                                                                                  ax,cs
                                                                                    stosw
                                                                      ; Both 100A/B come here to finish system vector setup
               006D
006E
008F
0070
0071
0072
0073
0074
                          07
1F
5F
5E
                                                                      skip:
                                                                                    POP
                                                                                                  d i
                                                                                    POP
                                                                                                  si
dx
cx
bx
                                                                                    DOD
162
163
164
165
166
                                                                                    POP
POP
                          5A
59
5B
5B
FB
CB
                                                                                    pop
st i
                                                                                                  ax
                                                                                                                             ;Re-enable ints
                0076
                                                                                    ret
168
169
170
171
172
173
174
175
                0077
0078
                          00
                                                                      r 100 db
xoption db
                0079
                                                                      re_init endp
                0079
                                                                      code ends
```

```
The Microsoft MACRO Assembler
DEC Rainbow Interrupt vector initialization
                                                                                                                                                                                                                                                                                  02-20-84
                                                                                                                                                                                                                                                                                                                                                                     PAGE
                                                                                                                                                                                                                                                                                                                                                                                                                  Symbols-1
    Segments and groups:
                                                                                                      Name
                                                                                                                                                                                                                                                             Size
                                                                                                                                                                                                                                                                                                                  align
                                                                                                                                                                                                                                                                                                                                                                     combine class
   GROUP
0079
                                                                                                                                                                                                                                                                                                                  BYTE
                                                                                                                                                                                                                                                                                                                                                                      PUBLIC 'CODE'
    Symbols:
N a m e

AUXDP.
AUXP
AUXP
AUXP
AUX_PRN
CLK_INT
COMDMA
DDS20VEC
DDS21VEC
DDS25VEC
DDS25VEC
DDS25VEC
DDS25VEC
DSS25VEC
DSS25VEC
DSS25VEC
DSS25VEC
DSS21VEC
DSS25VEC
DSS
                                                                                                    N a m e
                                                                                                                                                                                                                                                                Type
                                                                                                                                                                                                                                                                                                                   Value
                                                                                                                                                                                                                                                                                                                                                                     Attr
                                                                                                                                                                                                                                                                                                                   0040
0042
0044
0020
                                                                                                                                                                                                                                                                  Number
                                                                                                                                                                                                                                                                   Number
                                                                                                                                                                                                                                                               Number 002E
Number 0003
V DWDRD 0000
L NEAR 0000
                                                                                                                                                                                                                                                                  Number
                                                                                                                                                                                                                                                                                                                                                                                                                        External
External
External
External
                                                                                                                                                                                                                                                                                                                                                                                                                          External
                                                                                                                                                                                                                                                                                                                                                                                                                          External
                                                                                                                                                                                                                                                                                                                                                                                                                         External
                                                                                                                                                                                                                                                                                                                   0010
                                                                                                                                                                                                                                                                  Number
                                                                                                                                                                                                                                                                                                                   0011
0041
0043
0064
0077
0015
                                                                                                                                                                                                                                                                  Number
Number
Number
                                                                                                                                                                                                                                                                Number
L BYTE
L NEAR
F PROC
Number
L NEAR
                                                                                                                                                                                                                                                                                                                                                                      CODE
                                                                                                                                                                                                                                                                                                                                                                                                                        Global
                                                                                                                                                                                                                                                                                                                                                                       CODE
                                                                                                                                                                                                                                                                                                                   0015
0000
0018
006D
0041
0042
                                                                                                                                                                                                                                                                                                                                                                      CODE
                                                                                                                                                                                                                                                                                                                                                                                                                        Global Length =0079
                                                                                                                                                                                                                                                                  Number
                                                                                                                                                                                                                                                                  Number
Number
Number
Number
                                                                                                                                                                                                                                                                                                                   0045
0040
0001
                                                                                                                                                                                                                                                                                                                   0002
                                                                                                                                                                                                                                                                        BYTE 0078
                                                                                                                                                                                                                                                                                                                                                                     CODE
                                                                                                                                                                                                                                                                                                                                                                                                                        Global
   Warning Severe
Errors Errors
O O
```

```
DEC Rainbow Interrupt vector initialization
Symbol Cross Reference
                      (# is definition)
63#
                    65#
                    53#
11
                           30
                       29#
                           29
                               174
                    11
52#
                    25#
25#
26#
26#
27#
27#
40#
                       105
                       111
EXCOM. . . . . . . . . . . . .
                    54#
22#
22#
23#
23#
24#
                       120
125
139
143
147
                    24#
61#
62#
84#
20
92
20
57#
                       91
98#
74#
                           169#
                           172
158#
                    50#
55#
70#
170#
                    56#
```

```
The Microsoft MACRO Assembler
Disk definition tables for MSDOS 2.00
                                                                    02-20-84
                                                                                         PAGE
                                                                                                    1 - 1
                                                                            PAGE
TITLE
Name
Disk definition tables for MSDOS 2.00 DSKTBL
                                                                            COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
All rights reserved.
                                                               cgroup group code
code segment byte public 'code'
assume cs:cgroup,ds:cgroup
              0000
                                                               public dsktbl,inittbl
extrn decdisk:near
                                                                                                     ;;,serdsk:near
                                                                Define the disk data block used by the BIDS.
                                                               endm
                                                                ;Create a BPB block for the system.
                                                               ;
bpb macro secsiz,clssiz,ressec,fats,dirs,dsksiz,media,spf
dw secsiz ;sector size,
db clssiz ;cluster size,
dw ressec ;resv'd sectors
db fats ;# FATs,
dw dirs ;# dirs,
dw dsksiz ;total sectors,
db media ;media byte,
dw spf ;sectors/FAT,
                                                                            endm
```

```
The Microsoft MACRO Assembler
Disk definition tables for MSDOS 2.00
                                                                                                        02-20-84
                                                                                                                                         PAGE
                                                                                                                                                            1 - 2
                                                                                                  page
50
51
52
53
54
55
56
57
58
                                                                                                  ;
;Initialization table. Merely a set of
;pointers, one per unit, to the largest
;BPB for that unit.
                      0000
0001
0003
0005
                                  04
004C R
004C R
004C R
                                                                                                  inittb1 db
                                                                                                                     bpbdec
bpbdec
bpbdec
                                                                                                                                         bpbdec
                                                                                                                                         bpbibm ; DEBUG
61234556788017777777780
                                                                                                  ;
¡BIOS internal data tables. These define all
¡physical characteristics and current format
;for each drive. The first byte is the number
;of drives.
                      0008
000A
000C
                                  04
0012 R
001A R
0022 R
                                                                                                                                         4
diska
diskb
diskc
                                                                                                  dsktb1 db
                                                                                                                                                            ;# disks,
                                                                                                                     dw
dw
dw
dw
                                                                                                                                                           ; DEBUG
                                                                                                  ;
;For each unit there are three alternative
;formats. This table defines the three formats
;for each drive, and pointer to the byte value
;that tells which to use.
                                                                                                  ;
;;diskx dw
                                                                                                                                         dm i bma
                                                                                                  ::
                                                                                                                      dw
dw
                                                                                                                                          dm i bma
81
82
83
84
85
85
87
88
89
                                                                                                                                         currx
                      0012 0059 R
0014 00B9 R
0016 0119 R
0018 0181 R
                                                                                                                      dw
dw
dw
                                                                                                                                         deca
ibma
ibm9a
                                                                                                  diska
                                                                                                                                          curra
                      001A
001C
001E
0020
                                    0071 R
00D1 R
0131 R
0182 R
                                                                                                                     dw
dw
dw
                                                                                                                                         decb
ibmb
ibm9b
                                                                                                  diskb
9 1
9 2
9 3
                                                                                                                                          CUTTO
                      0022
                                    0089 R
                                                                                                                     dw
                                                                                                  diskc
                                                                                                                                         decc
                      0024
0028
0028
                                    00E9 R
0149 R
0183 R
                                                                                                                                          ibmc
ibm9c
currc
94
95
95
97
98
99
100
101
                      002A 00A1 R
002C 0101 R
002E 0161 R
0030 0184 R
                                                                                                  diskd
                                                                                                                      dw
                                                                                                                                          decd
                                                                                                                      dw
dw
                                                                                                                                          i bmd
                                                                                                                                          i bm9 d
```

```
The Microsoft MACRO Assembler Disk definition tables for MSDDS 2.00
                                                                                                                  02-20-84
                                                                                                                                                     PAGE
                                                                                                                                                                           1 - 3
                                                                                                          page
103
104
105
107
108
110
111
112
113
114
115
115
117
118
                                                                                                           ;
;These tables describe each logical drive's
;physical characteristics. Each logical disk
;has a table entry.
;ATTENTION the fat id byte for each table
; must be different.
                                                                                                                                                   0032
0032 0200
0034 01
0035 0001
0037 02
0038 0040
003A 0140
                                                                                                           bpbibm: bpb
                                                                                                                               qA
pbr
                                                                                                                                dЬ
                        003A
003C
003C
003F
003F
                                                                                                                                dw
                                                                                                                                                    bpbibm9: bpb
                                                                                                                            dw
                                       0200
                                       0200
01
0001
02
0040
0158
                                                                                                                               db
db
121
122
123
124
125
126
127
128
130
131
132
133
134
135
136
                         0041
                        0042
0044
0045
0047
                                                                                                                                dw
dw
                         0049
                                                                                                                                db
                         004A
                                       0002
                                                                                                         bpbdec: bpb
dw
db
                        004C
004C
004E
004F
                                       0200
                                        01
0014
                                                                                                                                dw
                                       02
0060
0320
FA
0003
                                                                                                                                db
dw
db
                         0051
                         0052
0054
0058
0057
                                                                                                                                dw
138
141
142
143
144
145
148
148
151
151
                                                                                                           ;Density here is:
                                                                                                           ;
;
O == RX-50
;
1 == IBM 8 sectors
;
2 == IBM 9 sectors or (Robin)
;This is used by the Z80 for 80/40 track switch.
If 0 then 80 tracks, else 40 tracks.
                      0059
0059 004C R
0058 0000
005D 0000 E
005F 0179 R
0081 0200
                                                                                                                               dskblk bpbdec,0,decdisk,tracka,512,10,0,0,2,0,0,0 dw bpbdec ;BPB ptr, dw 0 ;1= fixed dsk, decdisk ;driver addr, dw tracka ;curr trk ptr, dw 512 ;sector size, dw 10 ;sectors/trk, dw 0 ;density: O=RX 1=1EM8 2=IBM9 dw 0 ;drive #, dw 2 ;dens. chk trk dw 0 ;785 gap len,
                                                                                                           deca:
 153
                         0065
                                        0000
                                        0002
```

```
The Microsoft MACRO Assembler
Disk definition tables for MSDDS 2.00
                                                                                                                                                02-20-84
                                                                                                                                                                                             PAGE
                                                                                                                                                                                                                        1 - 4
                                                                                                                                                                                         157
158
159
160
                               006D
006F
0071
0071
                                                                                                                                                                  dskbik
dw
dw
dw
                                                                                                                                       decb:
                                                   004C R
                               0073
0075
0075
0077
0078
                                                   0000
0000 E
017A R
0200
  161
162
163
164
165
167
167
168
170
171
172
177
177
177
177
177
178
179
181
                                                   OOOA
                               007D
007F
0081
0083
                                                   0000
0001
0002
0000
                                                                                                                                                                  dw
dw
dskb1k
                                                   0000
                                0087
                                                   0000
                               0089
0089
008B
008D
008F
                                                   004C R
0000
0000 E
017B R
                                                                                                                                                                   dw
dw
                               0081
0093
0095
0097
0099
                                                   0200
                                                   000A
0000
0002
0002
                                                   0000
                               009B
009F
00A1
00A1
00A3
 183
184
185
186
187
188
189
190
191
192
193
                                                                                                                                       decd:
                                                                                                                                                                  dskblk
                                                   004C R
                                                                                                                                                                  0000
                                                  0000 E
017C R
0200
000A
                               00A7
00A9
00AB
00AD
                                                                                                                                                                  dw
                                                   0003
                               00B1
                                                   0002
                               0083
0085
0087
                                                   0000
 195
196
197
198
199
200
201
202
203
204
205
                                                                                                                                                                  dw
dw
                                                                                                                                                                                           bpbibm,0,decdisk,tracke,512, 8,2,0,0,0,0,0
bpbibm ;BPB ptr,
0 ;1= fixed dsk,
decdisk ;driver addr,
tracke ;curr trk ptr,
512 ;sector size,
8 ;sectors/trk,
2 ;density: O-RX 1=1BM8 2=1BM9
0 ;drive #,
                                                                                                                                                                  dskb1k
dw
dw
dw
                               0089
                                                                                                                                        ibma:
                               0089
0088
008D
008F
00C1
                                                   0032 R
                                                   0000
0000 E
017D R
0200
                                                                                                                                                                  dw
                                                   0008
                               00C3
00C5
00C7
00C9
00CB
 206
                                                   0002
207
208
209
210
211
                                                                                                                                                                                                                                                  ;dens. chk trk
;765 gap len,
;765 N,
;785 data len,
                                                   0000
                                                   0000
                                                                                                                                                                   dw
 The Microsoft MACRO Assembler Disk definition tables for MSDDS 2.00
                                                                                                                                               02-20-84
                                                                                                                                                                                             PAGE
                                                                                                                                                                                                                 1 - 5
                                                                                                                                                                                         bpbibm, O, decdisk, trackf, 512, 8, 2, 1, 0, 0, 0, 0
bpbibm ; BPB ptr,
O ; 1: fixed dsk,
decdisk ; driver addr,
trackf ; curr trk ptr,
512 ; sector size,
8 ; sectors/trk,
2 ; density: O:RX 1:IBM8 2:IBM8
1 ; drive #,
O ; dens. chk trk
O ; 785 gap len,
O ; 785 N,
O ; 785 N,
O ; 785 N,
O ; 785 data len,
bpbibm, O, decdisk, trackg, 512, 8, 2, 2, 0, 0, 0, 0
bpbibm ; BPB ptr,
512 ; sector size,
8 ; driver addr,
trackg ; curr trk ptr,
512 ; sector size,
8 ; driver addr,
trackg ; curr trk ptr,
512 ; sector size,
8 ; density: O:RX 1:IBM8 2:IBM8
2 ; driver #,
O ; dens. chk trk
O ; 785 N,
O ; 785 gap len,
O ; 785 data len,
bpbibm, O, decdisk, trackh, 512, 8, 2, 3, 0, 0, 0, 0
bpbibm , 785 data len,
bpbibm, O, decdisk, trackh, 512, 8, 2, 3, 0, 0, 0, 0
bpbibm ; BPB ptr,
212
213
214
215
216
217
218
219
                                                                                                                                                                 dskblk
dw
dw
                                                  0032 R
0000
0000 E
017E R
0200
                               00D1
00D3
00D5
                               0009
                                                   0008
0002
0001
0000
0000
                               CODE
                               00DD
00DF
00E1
00E3
220
221
222
223
224
                                                                                                                                                                  dw
                                                                                                                                                                  dw
                                                                                                                                                                 dw
dw
dw
dskb1k
dw
dw
                               00E5
                                                   0000
                               00E3
00E9
00E9
00EB
00ED
                                                   0000
225
226
227
228
229
230
231
232
233
234
235
                                                  0032 R
0000
0000 E
017F R
0200
                               00F1
00F3
00F5
00F7
                                                   0008
0002
0002
                                                                                                                                                                  dw
dw
                                                   0000
                               OOFB
                                                   0000
236
237
238
240
241
242
243
244
245
246
247
                               OOFD
                                                   0000
                               00FF
0101
0101
0103
0105
                                                                                                                                                                                           O ;765 data len,
bpbbibm,O,decdisk,trackh,512, 8,2,3,0,0,0,0
bpbibm ;8P8 ptr,
O ;1= fixed dsk,
decdisk ;driver addr,
trackh ;curr trk ptr,
512 ;sector size,
                                                                                                                                       i bmd :
                                                                                                                                                                  dskblk
                                                   0032 R
                                                                                                                                                                 dw
dw
                                                   0000
                                                  0000 E
0180 R
0200
0008
                               0105
0107
0108
010B
010D
010F
0111
0113
0115
                                                                                                                                                                                                                       ; sector size,
; sectors/trk,
; density: O-RX 1=IBM8 2=IBM9
; drive #,
; dens. chk trk
; 765 gap len,
; 765 N,
; 765 data len,
                                                                                                                                                                  dw
dw
dw
dw
                                                   0003
                                                   0000
248
249
250
251
252
253
254
255
256
257
                                                                                                                                                                  dw
                                                                                                                                                                  dw
                                                                                                                                                                                           0119
0118
011B
011D
011F
                                                   003F R
0000
0000 E
017D R
                                                                                                                                                                  dw
                                                                                                                                                                  dw
                                                                                                                                                                  dw
                                                                                                                                                                  dw
dw
dw
                                                   0200
258
259
260
261
                               0123
0125
0127
0128
0128
                                                   0009
0002
0000
                                                                                                                                                                  dw
dw
dw
                                                   0000
 262
                                                   0000
263
264
265
266
                               012B
012D
012F
0131
0131
                                                   0000
                                                                                                                                                                                             bpbibm9,0,decdisk,trackf,512,8,2,1,0,0,0,0
bpbibm9; BPB ptr,
                                                                                                                                       ibm9b:
                                                                                                                                                                  dskblk
                                                  003F R
                                                                                                                                                                  dw
```

```
The Microsoft MACRO Assembler
Disk definition tables for MSDDS 2.00
                                                                                                                                                                                                                         02-20-84
                                                                                                                                                                                                                                                                                             PAGE
                                                                                                                                                                                                                                                                                                                                    1 - 6
                                                                                                                                                                                                                                                                                         O ; 1= fixed dsk,
decdisk ; driver addr,
trackf ; curr trk ptr,
512 ; sector size,
9 ; sectors/trk,
2 ; density: O=RX 1=IBM8 2=IBM9
1 ; drive #,
0 ; 765 gap len,
0 ; 765 N,
0 ; 765 data len,
bpbibm9 O,decdisk, trackg, 512,9,2,2,0,0,0,0
bpbibm9 ; BPB ptr,
0 ; 1= fixed dsk,
decdisk ; driver addr,
trackg ; curr trk ptr,
512 ; sector size,
9 ; sectors/trk,
2 ; density: O=RX 1=IBM8 2=IBM9
2 ; drive #,
0 ; dess. chk trk
0 ; 765 data len,
bpbibm9 O,decdisk, trackh, 512,9,2,3,0,0,0,0
j765 N,
0 ; 765 data len,
bpbibm9 O,decdisk, trackh, 512,9,2,3,0,0,0,0
bpbibm9 ; BPB ptr,
0 ; 1= fixed dsk,
decdisk ; driver addr,
trackh ; curr trk ptr,
512 ; sectors/trk,
2 ; density: O=RX 1=IBM8 2=IBM9
decdisk ; driver addr,
trackh ; curr trk ptr,
512 ; sectors/trk,
2 ; density: O=RX 1=IBM8 2=IBM9
decdisk ; driver addr,
trackh ; curr trk ptr,
512 ; sectors/trk,
2 ; density: O=RX 1=IBM8 2=IBM9
dens. chk trk
0 ; dens. chk trk
0 ; 765 gap len,
0 ; 765 N,
0 ; 765 N,
0 ; 765 data len,
267
268
269
                                            0133
0135
0137
0139
0138
0138
0137
0141
0145
0147
0149
0148
0140
0145
0145
0145
0145
0145
0145
                                                                           0000 E
017E R
0200
0008
0002
                                                                                                                                                                                                                                                  dw
dw
dw
dw
dw
dw
dw
dw
dskb1k
                                                                                                                                                                                                                                                     dw
277345677890122773456222883458878890
                                                                             0000
                                                                            0000
                                                                             0000
                                                                                                                                                                                                            ibm9c:
                                                                            003F R
                                                                                                                                                                                                                                                     dw
                                                                             0000
                                                                                                                                                                                                                                                    dw
dw
dw
dw
                                                                            0000 E
017F R
0200
                                                                             0008
                                              0155
0157
0158
0158
015D
015F
0161
0163
0165
0167
                                                                             0002
                                                                                                                                                                                                                                                    dw
dw
                                                                              0002
                                                                            0000
                                                                                                                                                                                                                                                     dw
                                                                                                                                                                                                                                                    0000
                                                                                                                                                                                                            ibm9d:
                                                                            003F R
0000
0000 E
0180 R
0200
292
293
294
295
296
287
298
300
301
302
303
304
305
                                               0167
0168
0168
0160
0167
0171
0173
0175
                                                                             0008
0002
0003
0000
                                                                              0000
                                                                              0000
                                                                              0000
                                                                                                                                                                                                                                                                                              ò
                                                                                                                                                                                                                                                                                                                                                                                ;765 data len,
                                                                                                                                                                                                            ;;dmibma:
                                                                                                                                                                                                                                                                                            dskblk bpbibm, -1, serdsk, trackx, 512, 8, 3, 1, 0, 0, 0, 0
```

```
The Microsoft MACRO Assembler
Disk definition tables for MSDOS 2.00
                                                                                                                                02-20-84
                                                                                                                                                                       PAGE
                                                                                                                                                                                               1 - 7
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
                                                                                                                        page
                                                                                                                        ; Current tracks. The current track is kept ; for each PHYSICAL drive; note that the tables ; above that use the same physical drive point ; to the same current track. No need to fiddle ; with these, except maybe to add one if more ; drives are added.
                                                                                                                        tracka db
trackb db
trackc db
trackd db
                                                                                                                                                                       255
255
255
255
                          0179
017A
017B
017C
                                                                                                                                                                                              ;same drives as
;below, but different
;number of tracks.
                          017D
017E
017F
0180
                                                                                                                        tracke db
trackf db
trackg db
trackh db
                                                                                                                                                                       255
255
                                                                                                                        tracke
trackf
                                                                                                                                                                       255
255
322
323
324
325
326
327
328
329
330
331
                                                                                                                        ;;trackx
                                                                                                                                                                        db
                                                                                                                                                                                              255
                                                                                                                        ;
;Current format selections. One per logical
;drive. These get updated by the media check
;disk call, to either 0,1 or 2.
                           0181
0182
0183
0184
                                            00
00
00
                                                                                                                        curra
curra
                                                                                                                                                db
                                                                                                                                                dЬ
332
333
334
335
336
337
338
339
340
                                                                                                                        curre
                                                                                                                                                db
db
                                                                                                                       ;;currx db
                                                                                                                                                                        0
                           0185
                                                                                                                       code ends
```

The Microsoft MACRO Assembler Disk definition tables for MSDOS 2.00 02-20-84 PAGE Symbols-1 Name Length 0006 Seaments and groups: Size align combine class BYTE PUBLIC 'CODE' 0185 Symbols: N a m e Type Value Attr L NEGYTTEE
L NEGYTTEE
L NEGYTTEE
L NEGYTTEE
L NEGAGAR
L 004C CODE 0032 003F 0181 0182 0183 0184 CODE CODE CODE CODE CODE 0059 0071 0089 00A1 CODE CODE CODE CODE CODE CODE CODE CODE External 0012 001A 0022 002A 0009 0118 0131 0149 0161 0089 Global CODE CODE CODE CODE CODE OOES 00E9 0101 0000 0179 017A 017C 017C 017C 017F 0180 CODE CODE CODE CODE CODE Global CODE CODE CODE Warning Severe Errors Errors

The Microsoft MACRO Assembler Disk definition tables for MSDOS 2.00 02-20-84

PAGE Symbols-2

. .

```
Disk definition tables for MSDOS 2.00
  Symbol Cross Reference
                                                                               (# is definition)
                                                                     110
56
110#
119#
119
57
                                                                                                 128
58
213
266
                                                                                                                            128#
239
292
                                                                                                              59
226
279
                                                                                                                                        147
                                                                                                                                                       160
                                                                                                                                                                     173
                                                                                                                                                                                   186
                                                                                   200
253
                                                                      11
11
86
91
96
                                                                                   13
12#
331#
332#
333#
334#
 CGROUP .
CODE .
                                                                                                              338
CHERR
CURRC.
DECA
DECB
DECC
DECC
DECDISK
DISKA
DISKA
DISKB
DISKC
DISKC
DISKC
DISKC
DISKC
DISKC
DISKC
                                                                       88
88
93
88
                                                                                   146#
159#
172#
185#
                                                                     98
16#
68
69
70
71
146
15
                                                                                                 162
                                                                                                               175
                                                                                                                             188
                                                                                                                                          202
                                                                                                                                                       215
                                                                                                                                                                     228
                                                                                                                                                                                   241
                                                                                                                                                                                                255
                                                                                                                                                                                                              268
                                                                                                                                                                                                                            281
                                                                                                                                                                                                                                          294
                                                                                   149
83#
88#
93#
98#
159
67#
                                                                                                 172
                                                                                                               185
                                                                                                                            199
                                                                                                                                         212
                                                                                                                                                       225
                                                                                                                                                                     238
                                                                                                                                                                                   252
                                                                                                                                                                                                265
                                                                                                                                                                                                              278
                                                                                                                                                                                                                            291
                                                                                   252#
265#
278#
291#
199#
212#
225#
238#
                                                                       85
90
95
 I BM9 A
 IBMSE.
IBMSC.
IBMSD.
                                                                      100
84
89
94
99
 I BMA
 IBMB . . . IBMC . . . IBMD . . . . INITTBL .
                                                                                   315#
316#
317#
318#
256
 TRACKA .
                                                                     150
163
176
189
203
216
229
242
 TRACKE
TRACKE
TRACKE
TRACKE
                                                                                                 320#
 TRACKG
 TRACKH
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                                                               02-20-84
                                                                                                                                                PACE
                                                                                                                                                                    1 - 1
                                                                                                                           PAGE
TITLE
                                                                                                                                                60.132
                                                                                                                                                MSDOS 2.00 Universal Disk driver
DISK
23456789111111111122222222233333333333444444444555555
                                                                                                                           COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
All rights reserved.
                                                                                                                           10/05/83
                                                                                                       cgroup group code
                                                                                                       public dsk, MC_FLG
public cmdstr, dsktmr
                                                                                                                           ptrsave:dword
dsktbl:word,inittbl:word
                                                                                                       ;This is a special version for the Rainbow;It prevents disk access outside the range;of 01000 - Offffh. Much optimization has;been done to speed things up.
                                                                                                       include iodef.ash
                                                                                               include iodef.ash;
Rainbow Interrupt numbers.;
11-Apr-83 sgs added dskio int vector;
19-Mar-83 sgs added profile int vector [user clock];
the int profile is called by the RTC interrupt service for each tick.;
The ax and ds register don't need to be saved [done in clkisr].
                                                                                                       .
profile equ
                                                                                                                                                64h
                                                                                                                                                                 ;100. user interface to clock interrupt
;60Hz int,
;direct disk io for format
                       = 00B4
                       : 002C
                                                                                                       clk_int equ
dskio equ
                                                                                                                                                2ch
65h
                                                                                                      ;

;17-Mar-83 sgs changed to include int 20-26

;interrupt 22-24h are duplicated at 42-44h for consistency.

;These are the relocated Rainbow interrupts.

;interrupts, 20h-26h moved to 40h-46h

;ATTENTION Modules IDINIT and REINIT may have to be

;changed if int vectors 40h to 46h are changed
                       - 0040
                                                                                                                                                40h
41h
42h
43h
44h
45h
46h
47h
18h
                                                                                                      vert equ
spare41 equ
spare42 equ
comdma equ
aux_prn equ
excom equ
uart equ
z80 equ
                            0041
0042
0043
0044
                                                                                                                                                                   ;DMA ctrl optional comm. board
;7201 comm./printer
;extented comm. option
;new UART vector,
;280 interrupt,
;new ROM access,
                            0045
                           0046
                        = 0018
                                                                                                       rom
                                                                                                                           eau
                                                                                                       DEC Rainbow IO port stuff.
                       : 0010
                                                                                                       kdp
                                                                                                                                                                   :8251 data port.
                                                                                                                           equ
                                                                                                                                                10h
```

```
The Microsoft MACRD Assembler
MSDOS 2.00 Universal Disk driver
                                                                                                           02-20-84
                                                                                                                                           PAGE
                                                                                                                                                               1 - 2
                        = 0011
= 0040
= 0041
= 0042
= 0043
                                                                                                 ksp
auxdp
prndp
auxp
                                                                                                                                           11h
40h
41h
42h
43h
                                                                                                                                                               ;8251 status,
;7201 data,
                                                                                                                        egu
5678906123456678901123456778901234567899190
                                                                                             000000
                                                                                                                        edn
                                                                                                                                                               ;7201 command,
                                                                                                                        equ
                                                                                                     prnp
                                                                                                                        equ
                                                                                                     , Values in XOPTION
                                                                                                    wprsnt equ
xcprsnt equ
                                                                                                                                            0 1
0 2
                                                                                                                                                               ;Winnie preset
;XCOMM present
                         = 0001
= 0002
                                                                                                     extrn buffer:near
                                                                                                                                                               :defined in END.ASM
                                                                                                     code segment byte public 'code' assume cs:cgroup,ds:cgroup
                                                                                                    High level disk interface for MSDDS ;2.00. The MSDDS request for N logical sectors ;is broken down to a bunch of calls to the ;phsical driver to do sectors within a given ;physical track. Address wrap around due to ;hardware limitations (16 bit address, 4 bit ;page register) is taken care of here, as well ;as I/D requests larger than 64k bytes that ;would wrap around a segment.
                                                                                                     include ms200.ash
                       0000 ??
0001 ??
702 ??
3 ????
08 [
                                                                                                      MSDDS 2.00 ID device data packet layout.
                                                                                                      .
iodata struc
                                                                                                                                            (?)
(?)
(?)
(?)
                                                                                                     cmdlen
unit
                                                                                                                                                                ;packet length,
;unit number,
                                                                                                                         db
                                                                                                                        ďb
                                                                                                                        db dw db 8 dup
                                                                                                                                                                ;command,
;returned status,
                                                                                                     cmd
                                                                                                                      db
dd
dw
dw
ends
                                      ????
?????????
??
                                                                                                     media
trans
count
start
                                                                                                                                                                ;descriptor byte
;transfer address
;data count
;starting record
                         000E
0012
0014
0016
                                                                                                      iodata
```

```
The Microsoft MACRO Assembler
MSDOS 2.00 Universal Disk driver
                                                                                                                                                           02-20-84
                                                                                                                                                                                                          PAGE
                                                                                                                                                                                                                                        1 - 3
                                                                                                                                                page
fwrite equ
fread equ
fdens equ
fwritev equ
dskdrct equ
                              = 0000
= 0001
= 0002
= 0003
= 0065
= 0002
                                                                                                                                                                                                                                       ;write
;read
;check density
;write with verify
;direct disk access interrupt
;2 sec. timeout
102
                                                                                                                                                                                                          o
1
2
3
065h
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
                                                                                                                                                  tmrcnt equ
                                                                                                                                                  ;
;Physical disk driver parameter block. The
;read or write command is built here, and
;a pointer to it is passed to the actual
;driver.
                                                                                                                                                cmdstr:
command db
dskdrv db
track db
physec db
physent db
curtrk db
density db
gaplen db
                                0000
                                0000
0001
0002
0003
                                                     33
33
33
33
                                                                                                                                                                                                                                        ;floppy command,
;disk drive,
;seek track,
;seek sector,
                                                                                                                                                                                                           (?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
                                0004
                                                                                                                                                                                                                                         ; sector count
                                                                                                                                                                                                                                        ;sector count,
;current track,
;density/size fla
;765 gap length,
;765 sec size,
;765 data length,
;sector size,
;DMA offset,
;DMA segment,
1212
11225
11225
11227
11229
11323
11333
11333
1140
11443
11445
11447
11447
11449
                                0005
                                0005
0006
0007
0008
0009
                                                                                                                                                                              db
db
                                                                                                                                                  enn
dt 1
                                                                                                                                                 secsiz dw
dskoff dw
dskseg dw
                                AOOO
                                0000
                                                                                                                                                  ;
End of the command string, start of
;internal stuff.
                                                                                                                                                                                                                                       ;driver number,
;ptr to current track,
;ptr to BPB,
;ptr to current format,
;l if non removable,
;trk for density test,
;sectors per track,
;physical sector count,
;status mask,
;driver address,
;DMA offset,(don't change these DMA order)
;DMA segment,
;MSDOS sector number,
;MSDOS sector count,
                                                                                                                                                 driver db
trkptr dw
bpbptr dw
typptr dw
chkbyte db
dentrk db
spt dw
scount dw
errmsk db
                                0010
0011
0013
0015
                                                    (?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
(?)
                                0017
0018
0019
001B
001D
                                                                                                                                                  errmsk db
calladdr dw
                                0020
0022
0024
0026
                                                                                                                                                 dmaoff dw
dmaseg dw
sector dw
numsec dw
                                0028
                                                                                                                                                 tmrdrv db
dsktmr db
                                                                                                                                                                                                           Offh ;Last drive used successfully tmrcnt ;timer loc.
                                                      F F
0 2
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                                                              02-20-84
                                                                                                                                               PAGE
page
                                                                                                       Disk function for the BIOS.
                       002A
                                                                                                      dsk:
                                                                                                      dsk:
dispatch proc far
pushf
push a
                       002A
002A
002B
                                 9 C
5 O
5 3
                       002C
                                                                                                                           push
                                                                                                                                               bx
                       002D
002E
002F
0030
0031
                                                                                                                           push
push
push
                                                                                                                                               cx
dx
si
                                                                                                                           push
                                                                                                                           push
push
push
push
pop
les
                                                                                                                                               bp
ds
es
                       0032
0033
0034
0035
0036
                                                                                                                                                c s
d s
                                                                                                                                               Dx,cs:ptrsave
es:byte ptr [bx.cmd],0 ;Init call?
noint ;Don't enable ints on INIT
                                                                                                                           cmp
je
sti
                       0040
0042
0043
0048
                                                                                                                                              word ptr es:[bx.status],0
al,es:[bx.unit]
driver,al
ax,es:[bx.count]
numsec,ax
ax,es:[bx.start]
sector,ax
ax, word ptr es:[bx.trans]
dmaoff,ax
ax, word ptr es:[bx.trans+2]
dmaseg,ax
bl,es:[bx.cmd]
bl,12
cmdok ;check in
crange bound
                                                                                                      noint:
                                                                                                                           mov
                                                                                                                           mov
                                                                                                                           mov
                       0050
0054
0057
005B
0062
0065
0065
0060
0073
0075
0077
                                                                                                                           mov
                                                                                                                           mov
                                                                                                                           mov
mov
mov
                                                                                                                                                                                        ;check in
;range, bound
;it,
                                                                                                                           cmp
jbe
                                                                                                                                                cmdok
                                                                                                                                               cmdok ;range, b
bl,13 ;it,
bh,0
bx,1
bx,offset dtbl
word ptr [bx]
bx,cs:ptrsave ;set DONE
word ptr [bx.status],100h
                                                                                                                           mov
mov
shl
add
call
                                                                                                      cmdok:
                        0081
                                                                                                                                                                                          ; set DONE,
                        0086
                       008B
008C
008D
008E
008F
                                                                                                                            POP
POP
                                                                                                                            POP
                                                                                                                                                bp
di
                                                                                                                            POP
                                                                                                                            P 0 P
P 0 P
P 0 P
                                      5E
59
58
58
50
CB
                                                                                                                                                s i
dx
                       008F
0090
0091
0082
0093
0084
                                                                                                                            POP
                                                                                                                           popf
                        0096
                                                                                                      dispatch endp
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
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                                                                                                                               PAGE
                                                                                                                                                 1 - 5
205
205
205
207
208
209
                                                                                           Command error.
                    OO96 2E: C5 1E 0000 E
OO9B 81 4F 03 8003
OOA0 C3
                                                                                           cmderr: 1ds
                                                                                                                              bx,cs:ptrsave
word ptr [bx.status],8003h
210
211
212
213
214
215
216
217
218
219
220
221
                                                                                           return: ret
                                                                                           Return busy.
                    OOA1 2E: C5 1E 0000 E
OOA6 81 4F 03 0200
OOAB C3
                                                                                          busy:
                                                                                                                              bx,cs:ptrsave
word ptr [bx.status],200h
                                                                                                            1ds
                                                                                                            or
ret
                                                                                                                                                                  ; 0 init disks,
; 1 check dens,
; 2 get BPB,
; 3 direct disk reads
; 4 read,
; 5 not used,
; 6 not used,
; 7 not used,
; 8 write,
; 9 write/ver,
;10 not used,
;11 not used,
;11 not used,
;12 direct disk write/format
                                 0286
024D
029B
                                                                                          dtbi
                                                                                                             dw
                                                                                                                               dskinit
                                                                                                                               mediachk
                    OOAE
OOBO
                                                                                                             dw
                                                                                                                                get bpb
RETURN
                    00B2
00B4
00B6
00B8
                                 00A0
00C8
00A1
222
223
224
225
226
227
                                                                                                                               read
busy
return
                                                                                                             dw
dw
                                                                                                             return
write
writev
return
                     OOBA
                                  OOAO
                     OOBC
                                  0006
227
228
229
230
231
232
                    00BE
00C0
00C2
00C4
                                 OOLF R
OOAO R
OOAO R
                                                                                                                               return
RETURN
                    00C6 0096 R
                                                                                                             d٧
                                                                                                                               cmderr
                                                                                                                                                                   ;13 bad command.
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                                      02-20-84
                                                                                                                 PAGE
                                                                                                                                 1 - 6
                                                                                page
235
236
237
238
                                                                                 ,Disk read/ write entry point. PTRSAY points; to the data packet. Perform the function,
                                                                                ; to the data ...
; return:
;  # sectors actually read,
; status byte (See below)
; error byte (see below)
write protect,
unknown unit
not ready,
command error
data error,
bad structure length,
seek error.
                                                                                                                seek error,
unkown media,
sector not found,
out of paper,
write fault,
read fault,
other failure.
                                                                                                  10
                                                                                                 11
                  00C8 C6 06 0000 R 01
                                                                                                mov command, fread
jmp short comn
                                                                                 read:
                  00CF
00D4
                              C6 06 0000 R 03
EB 05
                                                                                 writev: mov
                                                                                                                 command, fwritev
                                                                                                 jmp short comn
                  OODS C6 O5 OOOO R OO
OODB E8 O1EC R
OODE 72 5C
                                                                                                mov
call
jc
                                                                                                                 command, fwrite
                                                                                                                 set_drv ;set parms,
rdwret ;ret bad unit
                                                                                 comn:
```

```
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                                                                                                                                                                      PAGE 1-7
                                                                                                                         page
267
268
269
270
271
272
273
274
275
                                                                                                                         ;(Calculate the track, given the logical sector; and the number of sectors/track. Loop over; this point til all sectors read.
                                                                                                                         ; What it does:
                                                                                                                                               Find phys. track, sector,
If (scount < sectors left on track) {
    read sectors on track
} else {
    read (scount) sectors
276
277
278
279
280
281
282
283
284
285
286
                                                                                                                                                }
scount =scount-sectors read,
log. sec = log. sec + sectors read,
if scount .gt. 0, goto 1/.
                                                                                                                          ;3/
                                                                                                                         4/
                                                                                                                         ;
;All necessary physical parameters are calcu-
;lated here, and passed to lower level guys
;that actually do the read or write.
287
288
289
290
291
292
                                                                                                                         ;
rdwsec: mov ax,numsec ;if all done,
cmp ax,0 ;return.
jz rdwret
                            00E0 A1 0026 R
00E3 3D 0000
00E6 74 54
293
294
295
296
297
298
299
300
301
303
305
306
307
                                                                                                                         ;
;offset is as small as possible (will be less
;reading or writing more than 64K per BIOS
;call. Assumes less than 64K per disk track.
                                            A1 0020 R
D1 C8
D1 C8
D1 C8
D1 C8
D1 C8
B2 D0
C5 0FFF
D1 05 0022 R
B1 E2 F000
D1 C2
D1 C2
D1 C2
D1 C2
B3 C2
B4 C2
B5 C2
B6 C2
B7 C2
B7 C2
B8 C020 R
                                                                                                                                                                       ax,dmaoff
ax,1
ax,1
ax,1
dx,ax
ax,Offfh
dmaseg,ax
dx,Of000h
dx,1
dx,1
dx,1
dx,1
dx,1
                                                                                                                                                 MOV
FOF
FOF
                           OOE8
OOEB
OOEF
OOF1
OOF5
OOF8
OOFC
O100
O102
O104
O108
                                                                                                                                                                                                                        ;divide offset
;by 16, leave
;remainder in
;bits 15-12,
                                                                                                                                                 mov
                                                                                                                                                 and
add
and
rol
                                                                                                                                                                                                                        ;adjust segment
                                                                                                                                                                                                                        ;put remainder
;in LSB's,
                                                                                                                                                 rol
rol
rol
mov
 309
310
311
                                                                                                                                                                                                                        ;adjusted off,
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                                                                                                  02-20-84
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                                                                                                                                                                                                                          1 - 8
                                                                                                                                        page
312
313
314
315
316
317
318
319
320
321
                                                                                                                                        ;
Figure out how many sectors to read/write and
;the track to do it on.
                              010C A1 0024 R
010F 8B 0E 0018 R
0113 F6 F1
0115 FE C4
0117 A2 0002 R
                                                                                                                                                                                                                                                    ;logical sector
;sectors/track,
;now AL= track,
;AH= sector,
;save track,
;save sector
                                                                                                                                                                                              ax,sector
cx,spt
c1
ah
                                                                                                                                                                    mov
                                                                                                                                                                    inc
                                                                                                                                                                                               track, a1
                                                                                                                                                                    mov
322
323
324
325
326
                                                    88 26 0003 R
                                                                                                                                                                                               physec, ah
                                                                                                                                        ;

Start the read or write process. The number of

sectors to do may bring us past the end of the

cylinder; if so, do (sectors left on track)

sectors, then repeat for each track until done
 327
 327
328
329
330
331
332
                              011E 8A 0E 0003 R
0122 B5 00
0124 A1 0019 R
0127 40
0128 2B C1
012A 8B 0E 0026 R
012E 3B C1
0130 72 02
0132 8B C1
0133 A3 0018 R
0137 E8 015A R
                                                                                                                                                                                              cl,physec
ch,0
ax,spt
ax
ax,cx
cx,numsec
ax,cx
doleft
                                                                                                                                                                                                                                                    ;find # sectors
;left on
;this track,
;(start at 1,)
;leave in AX,
;comp to total
;if .lt. total,
;do left track,
                                                                                                                                         dosec:
                                                                                                                                                                    mov
                                                                                                                                                                    mov
inc
 332
333
334
335
336
337
338
                                                                                                                                                                    sub
mov
cmp
                                                                                                                                                                    jb
                                                                                                                                                                                                                                                     ;else requested
;amt. set count
;do the oper
;rpt no error,
                                                                                                                                                                                               mov
                                                                                                                                                                                                                          ax,cx
                                                                                                                                                                                               scount, ax
readwrite
rdwsec
                                                                                                                                        doleft: mov
 339
340
341
342
343
344
345
346
347
348
350
351
352
353
354
                                                                                                                                                                   call
jnc
                                                                                                                                        ;
Return to MSDOS. If no error, just return: the
number of sectors read ('count') is correct.
;If error, set the error code, and adjust the
;count to reflect the number actually done.
                                013C
013E
0143
0145
0149
014C
0150
0153
                                                  73 12
2E: C4 1E 0000 E
84 80
26: 09 47 03
A1 0025 R
26: 29 47 12
A0 0005 R
88 1E 0011 R
88 0F
                                                                                                                                        rdwret: jnc
les
                                                                                                                                                                                                                                                    ; if error,
                                                                                                                                                                                              rwr1 ;if error,
bx,cs:ptrsave
ah,80h ;err flag,
es:[bx.status],ax ;set error,
ax,numsec
es:[bx.count],ax
al,curtrk ;save current
bx,trkptr
[bx],al ;track,
                                                                                                                                                                    mov
or
                                                                                                                                                                    mov
                                                                                                                                        rwr1:
                                                                                                                                                                    mov
mov
                                 0159
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
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357
                                                                                                                                  page
358
359
360
361
362
363
                                                                                                                                  ; Read or write physical sectors. The physical ; parameters are all set; the sector count is ; equal to or less than the number of sectors ; on the track. Returns carry set and the MSDDS; error code in AL if error.
364
365
366
367
368
                                                                                                                                  ; Do all Rainbow sectors one at a time for 2 ;reasons: a bug in the Z80 code prevents ;multiple sectors from working, and the ;Z80 can only access addresses from 01000 -;0ffffh.
369
370
371
372
373
374
                                                                                                                                ;Offr.;
;readwrite:
call
jc
mov
                             015A
015A
015D
015F
0162
0165
                                             E8 0184 R
72 24
A0 0001 R
A2 0028 R
C6 06 0029 R 02
                                                                                                                                                                                     physbuf ;do buffered,
rdwrret ;exit if err,
al,dskdrv ;no error - re:
tmrdrv,al
dsktmr,tmrcnt ;reset counter
                                                                                                                                                                                                                                        ;do buffered,
;exit if err,
;no error - reset timer/drive
375
376
377
378
380
381
382
383
384
385
                                                                                                                                  ;
;Update the number of sectors, physical sector,
;and all that stuff. If no more to do, return.
;NOTE: This assumes that only one sector is
;done per pass.
                             016A
016D
0171
0175
0179
017D
0181
0183
                                               A1 000A R
01 06 0020 R
FE 06 0003 R
FF 06 0024 R
FF 0E 0026 R
FF 0E 001B R
75 D7
                                                                                                                                   rwchk:
                                                                                                                                                                                      ax, secsiz
dmaoff, ax
                                                                                                                                                                                                                                         adjust offset; next phys sec; next log sec; total to do; new count,
                                                                                                                                                            add
inc
inc
                                                                                                                                                                                      physec
sector
 387
388
389
390
                                                                                                                                                                                       numsec
                                                                                                                                                            dec
                                                                                                                                                                                      scount
readwrite
                                                                                                                                                             jnz
                                                                                                                                  rdwrret:ret
                                                                                                                                                                                                                                         ;done this trk
```

```
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391
392
393
394
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397
                                                                                                   page
                                                                                                   ;

Read or write data to our local buffer. Copy

;the data in or out of the buffer as necessary.

;Return carry set and AL error code if bad.
                     0184
                                                                                                   physbuf:
                                  C7 O6 OOOC R OOOO E
8C 1E OOOE R
C6 O6 OOO4 R O1
8O 3E OOOO R O3
74 O7
8O 3E OOOO R OO
75 1D
                                                                                                                                          dskoff,offset buffer
dskseg,ds ;set local buf
physcnt,1 ;one sector,
command,fwritev ;write verify
wrtbuf
command,fwrited
dord ;if write,
mov
                                                                                                                       mov
                                                                                                 cmp
je
cmp
jne
wrtbuf:
                                                                                                                       CMD
                                                                                                                                        cx, secsiz
si, dmaoff ;from DMA
di, offset buffer ; to buffer ,
dx, ds ;save DS,
es, dx ;ES= buffer ,
ds, cs: dmaseg ;DS= dest.,
                                                                                                                       mov
mov
                                                                                                                       mov
                                                                                                                       mov
                                                                                                                      cld
rep mov
mov
call
                                                                                                                                                                                 ;copy to buf
;restore DS,
;write to disk,
;return status,
                                                                                                                                        sb
                                                                                                                                          dophys
                                                                                                                      ret
                      01BE E8 01D7 R
01C1 72 13
01C3 8B 0E 0000A R
01C7 BE 0000 E
01CA 8B 3E 0020 R
01CE 2E: 8E 05 0022 R
01D3 FC
01D4 F3/ A4
01D6 C3
                                                                                                                                         dophys ;read, do it, pret ;stop if error, cx,secsiz si,offset buffer ;copy data out di,dmaoff
                                                                                                  dord:
                                                                                                                     ca11
                                                                                                                      jc
mov
mov
                                                                                                                       mov
                                                                                                                                          d1, dmaort
es, cs: dmaseg ; to seg: off,
                                                                                                                       mov
                                                                                                                      cld
rep movsb
ret
                                                                                                                                                                                ;copy from buf
;return status.
                                                                                                   pret:
```

```
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page
                                                                                                                            ; Do the physical read or write as specified by ;the disk command block. The driver to use was ;found in the 'dsktbl'. The physical drivers ;return carry set and the MSDOS error code in ;AL if error.
                                                                                                                            ;
;After the read or write, update the current
;track for this disk. If error, set the current
;track to -1, to indicate an error. (The driver
;will recal the disk before seeking if the
;current track is -1.)
                           01D7
01D7
01DA
01DE
01E3
01E5
01E8
                                                                                                                            dophys:
                                            BB 0000 R
FF 16 001E R
C6 06 0005 R FF
72 06
A0 0002 R
A2 0005 R
C3
                                                                                                                                                    mov
call
mov
jc
                                                                                                                                                                             bx,offset cmdstr ;setup command
word ptr calladdr ;do it,
curtrk,-1 ;assume bad,
db1
                                                                                                                                                     jc
mov
                                                                                                                                                                              dp1
a1,track
                                                                                                                                                                                                                               ; if good, set ; current track,
                                                                                                                                                                              curtrk, al
                                                                                                                            dp1:
```

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```
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                                                                                                                                                                                            1-12
page
                                                                                                                      ;Given the drive number, pull out the physical
;parameters necessary to read and write.
;The parameters are:
    BPB pointer,
    fixed/removable disk,
    read/write driver address,
    Physical drive number,
    Density flag,
    Sector size,
    Current track,
    Sectors per track,
    Density check track,
    NEC765 parameters.
                                                                                                                     ;
;
set_drv:
mov
cmp
jb
                         01EC 8A 1E 0010 R
01F0 3A 1E 0000 E
01F4 72 04
01F6 F8
01F7 B0 01
01F9 C3
01FA B7 00
01FC D1 E3
01FE 8B 9F 0001 E
0202 8B 77 06
0205 89 36 0015 R
0209 8A 04
0208 25 0007
020E 03 D8
0210 03 D8
                                                                                                                                                                  bl,driver
bl,byte ptr dsktbl;legal unit?
sdrv1
stc ;no,
mov al,1 ;unknown unit,
ret
bh,0 ;1st time, get
bx,1 ;dsk data ptr,
bx,dsktbl[bx+1];BX= disk ptr to ptr,
si,[bx+6] ;SI= type ptr (ptr to currxx)
typptr,si ;save type ptr,
al,[si] ;get curr formt
ax,7 ;three choices,
bx,ax ;add word offset
bx,ax ;get dsk data,
si.[bx]
                                                                                                                       sdrv1:
                                                                                                                                             mov
sh 1
mov
                                                                                                                                             mov
mov
mod
and
                                                                                                                                              add
                                                                                                                                              mov
                          0214 FC
0215 AD
0216 A3 0013 R
0219 AD
021A A2 0017 R
021D AD
021E A3 001E R
0221 AD
0222 A3 0011 R
0225 8B D8
0227 BA 07
0229 A2 0005 R
                                                                                                                                             cld
lodsw
mov
lodsw
                                                                                                                                                                                                                    ; BPB pointer,
                                                                                                                                                                     bpbptr,ax
                                                                                                                                                                                                                    removable flag
                                                                                                                                             mov
lodsw
mov
lodsw
                                                                                                                                                                     chkbyte, a 1
                                                                                                                                                                                                                    ;driver address
                                                                                                                                                                     calladdr,ax
                                                                                                                                                                                                                    ; cur. trk ptr,
                                                                                                                                                                      trkptr,ax
                                                                                                                                              Mov
Mov
                                                                                                                                                                     bx,ax
a1,[bx]
curtrk,a1
                                                                                                                                                                                                                    current track.
                                                                                                                                             mov
mov
                          O22C AD O23D A3 OOOA R O23D AD O231 A3 OO19 R O234 AD O235 A2 OOO6 R O238 AD O239 A2 OOO1 R
                                                                                                                                             lodsw
                                                                                                                                                                                                                    ; sector size,
                                                                                                                                                                     secsiz, ax
                                                                                                                                             mov
lodsw
                                                                                                                                                                                                                    :sectors/track,
                                                                                                                                             mov
lodsw
mov
lodsw
mov
                                                                                                                                                                     spt, ax
                                                                                                                                                                                                                    ;density byte,
 501
502
503
                                                                                                                                                                     density,a1
                                                                                                                                                                                                                    ;physical drive
                                                                                                                                                                     dskdrv,a1
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```

MSDOS	2.00 Universal Disk driver		
504	023C AD	lodsw	
505	023D A2 0018 R	mov dentrk.al	;dens. chk. trk
0.6	0240 AD	lodsw	•
07	0241 A2 0007 R	mov gaplen,al	gap length.
08	0244 AD	lodsw	
09	0245 A2 0008 R	mov enn.al	:sector size.
10	0248 AD	lodsw	,
11	0249 A2 0009 R	mov dtl.al	;data length.
12	024C C3	ret	,

```
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                                                                                              02-20-84
                                                                                                                           PAGE
                                                                                                                                            1-14
                                                                                        page
514
515
516
517
518
519
520
                                                                                         ;
;Disk change detect routine.
;always returns dont know
;Returns 1 if fixed disk (didn't change).
                    024D
024D
0250
                                                                                         mediachk:
                              E8 01EC R
B0 01
B4 FF
72 16
                                                                                                                           set_drv
al,1
ah,-1
samdsk
                                                                                                                                                               ;select drive,
;unknown unit,
;unknown change
;return error,
                                                                                                          call
521
522
523
524
525
526
                                                                                                          jс
                   0256 33 C0

0258 8A 1E 0001 R

025C 3A 1E 0028 R

0280 75 08

0282 80 3E 0029 R 00

0287 74 02

0288 84 01

0288 64 01

0288 F8

028C 028C 8B 36 0011 R

0270 8A 0E 0005 R

0274 88 0C
                                                                                                                                                               ;assume don't know
                                                                                                         XOF
                                                                                                          mov
cmp
jnz
                                                                                                                           bl,dskdrv
bl,tmrdrv
maybec
dsktmr,0
                                                                                                                                                               ;current disk
;match last one used?
;timer expired?
;jump if too late
;flag not changed
                                                                                                          cmp
                                                                                                          je
mov
                                                                                                                            maybec
                                                                                        maybec:
                                                                                                         c1c
                                                                                                                                                               ;no error,
                                                                                                          mov
                                                                                                                           si,trkptr
                                                                                                                           cl,curtrk
[si],cl ;phys track=cur.track
                                                                                                          mov
                                                                                                          mov
                    bx,cs:ptrsave    ;set for ret,
[bx.media+1],ah    ;set ret code,
dcr1    ;if error,
                                                                                                          mov
                                                                                                          jnc
                    0280 B4 80
0282 09 47 03
0285
0285 C3
                                                                                                          mov
or
                                                                                                                           ah,80h ;set err bit,
[bx.status],ax ;return error.
                                                                                        dcr1:
                                                                                                         ret
```

```
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                                                                                                                  02-20-84
                                                                                                                                                     PAGE
                                                                                                                                                                           1 - 15
547
548
549
550
551
                                                                                                           page
                                                                                                           ,
;Disk initialization. Return a pointer to the
;initialization table, and the number of units.
                        0286 BE 0000 E 0289 8A 04 0288 46 028C 2E: C5 1E 0000 E 0291 88 47 0D 0294 89 77 12 0297 8C 4F 14 029A C3
                                                                                                           dskinit:
si,offset inittbl;table ptr, al,[si] ;# disks, si ;ptr to ptrs, bx,cs:ptrsave [bx.media],al [bx.count],si [bx.start],cs
                                                                                                                                mov
mov
                                                                                                                                 inc
                                                                                                                                 lds
                                                                                                                                mov
mov
                                                                                                           ; Build BPB pointer. All it does is 'select'; the disk internally, and return a pointer to; the BPB table.
                        0298
0298 E8 0281 R
029E E8 01EC R
02A1 A1 0013 R
02A4 C5 1E 0000 E
02A8 89 47 12
02AB 8C C8
02AD 89 47 14
                                                                                                            get bpb:
                                                                                                                                call
call
mov
lds
                                                                                                                                                      chkmed
                                                                                                                                                                                                ;check media type for drive
                                                                                                                                                      set_drv
ax,bpbptr
bx,ptrsave
[bx.count],ax
                                                                                                                                                                                                ;return DWORD ;ptr to table,
                                                                                                                                mov
                                                                                                                                 mov
                                                                                                                                                       ax,cs
                                                                                                                                                      [bx.start].ax
                                                                                                                                 mov
ret
                                                                                                           iofun
iodrv
iosec
iotrk
iocnt
iooff
                         = 0000
= 0001
= 0002
= 0004
                                                                                                                                                                                                ;ioct1 packet function offset
;ioct1 packet drive offset
;ioct1 packet sector offset
;ioct1 track offset
;ioct1 sector count
;ioct1 buffer offset
;ioct1 buffer segment
                                                                                                                                 equ
                                                                                                                                 edn
edn
edn
                         = 0006
= 0008
= 000A
                                                                                                                                                      Oah
                       02B1 AO 0010 R
02B4 3A 06 0000 E
02B8 72 06
02BA BO 01
02BC F9
02BD EB 45 90
02C0 80 3E 0339 R 01
02C5 75 04
02C7 33 C0
02C9 EB 25
02CB C2CB C2CB BB 0334 P
                                                                                                            ioseg
                                                                                                                                  equ
                                                                                                                                                      al, driver ; get current drive to check al, byte ptr dsktbl ; see if it is a valid drive ckmdal
                                                                                                           chkmed: mov
                                                                                                                                cmp
jb
mov
                                                                                                                                                       a1,1
                                                                                                                                 stc
                                                                                                                                                      bailout
                                                                                                                                jmp
                                                                                                           ckmda1:
                                                                                                                                                      byte ptr mc_flg,1
normal
ax,ax
                                                                                                                                                                                                                     ; check media check flag
; do check
; set to RAINBOW media
; and skip media check codes
                                                                                                                                cmp
                                                                                                                                 jnz
xor
                                                                                                                                                       ax,ax
short okmdia
                                                                                                                                 jmp
                                                                                                                                                     bx,offset iopkt ioseg[bx],ds ;io xfer buffer segment word ptr iooff[bx],offset buffer io xfer buffer iodrv[bx],al ;drive to check byte ptr iofun[bx],04 ;check media function
                                       BB 033A R
8C 5F 0A
C7 47 08 0000 E
88 47 01
C6 07 04
                         02CB
02CE
02CE
02D1
02D6
                                                                                                                                 mov
                                                                                                                                 mov
 601
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                                                                                  02-20-84
                                                                                                                                                                          PAGE
                                                                                                                                                                                                   1 - 16
02DC CD 65
                                                                                                                                                                          dskdrct
                                                                                                                                                                                                                              ;direct disk driver interrupt
                                                                                                                                                  int
                                          BO 01
72 22
BB 0000 E
33 C0
8A 07
3C 01
76 03
E8 0305 R
                                                                                                                                                                          al,01 ; in case of error bailout ; blast out of here bx,offset buffer ; media type is in first byte of buffer ax,ax ; clear field for weird al,[bx] ; get media type al,01 ; if this is a rainbow or ibm8, Ok
                            02DE
                                                                                                                                                  mov
                            02E0
02E2
02E5
02E7
                                                                                                                                                  jc
mov
                                                                                                                                                  XOF
                                                                                                                                                                                                                          get media type
;if this is a rainbow or ibm8, Ok
;else fin out what it really is
;do a soft media check (read disk ID)
                                                                                                                                                  mov
                           02E7
02E8
02ED
02F0
02F0
02F2
02F8
02F6
                                                                                                                                                   CMD
                                                                                                                                                                           okmdia
softchk
                                                                                                                                                  jbe
call
                                                                                                                          okmdia:
                                             33 DB 8A 1E 0010 RD1 E3 8B 9F 0001 E8B 77 06 88 04 32 C0 F8
                                                                                                                                                  xor bx.bx
                                                                                                                                                                                                                           :clear pointer
                                                                                                                                                                          bx ;
b1,driver
bx,1
bx,dsktb1[bx+1]
si,[bx+6]
[si],a1
a1,a1
                                                                                                                                                                                                                              clear pointer; get current disk again; get current disk again; make worb index; and get media type table for drive; get pointer to media type holder; and save media type; clear status; accross the board
                                                                                                                                                  mov
sh 1
mov
                                                                                                                                                  mov
                                                                                                                                                   mov
xor
                            0301
0303
0304
0304
                                                                                                                         bailout:
ret
                                              С3
                                                                                                                                                                          ;coft media check to see if real 9 sector bx, offset iopkt ;read the disk id byte to check illogica al,driver ; get drive type iodrv[bx],al ;drive to read byte ptr ioscc[bx],2 ;type byte is on sector 2 word ptr iotrk[bx],0 ;on track 0 word ptr iocnt[bx],1 ;only reat 1 sector, of course word ptr iooff[bx],offset buffer;default sector buffer ioseg[bx],ds ;default data segment byte ptr iofun[bx],0 ;read data command
                             0305
                                                                                                                          softchk:
                           0305
0305
0308
0308
030E
0312
0317
031C
0321
                                             BB 033A R
AO 0010 R
88 47 01
C5 47 02 02
C7 47 04 0000
C7 47 05 0001
C7 47 08 0000 E
8C 5F 0A
C6 07 00
                                                                                                                                                  mov
                                                                                                                                                  mov
mov
                                                                                                                                                   mov
                                                                                                                                                   mov
                                                                                                                                                  mov
mov
mov
                                                                                                                                                                                                                          ;direct disk interrupt
;exito: greek for throwing garbage out
                            0327 CD 65
0329 72 OD
                                                                                                                                                   int
                                                                                                                                                                           dskdrct
                                                                                                                                                   jС
                                                                                                                                                                                                           buffer ;get data byte
;get id byte
;is it an ibma
;if so, say so
;and leave
                           O328 BB OOOO E
O32E 8A O7
O33O 3C FE
O332 BO O1
O334 74 O2
O336 BO O2
O338 C3
                                                                                                                                                                           bx, offset buffer al, [bx] ; al, ofeh ; al, ol ; exito ; ...
                                                                                                                                                   mov
                                                                                                                                                   cmp
mov
                                                                                                                                                                                                                          ;and leave
;else it is an ibm9
;return with media type in al
```

```
The Microsoft MACRO Assembler MSDOS 2.00 Universal Disk driver
                                                                  02-20-84
                                                                                       PAGE
                                                                                                   1 - 17
646
647
648
649
650
651
652
653
654
              0339 00
                                                                                                               ; media check flag, 0 = check default
; 1 = don't check assume RAINBOW disk
                                                              MC_FLG DB
                          10 [ 77 ]
              AEEO
                                                              iopkt: db 10h dup (?)
                                                                                                               ;space for packet
655
656
657
658
              034A
                                                                          ends
end
                                                              code
```

he Microsoft MACRO Assembler DOS 2.00 Universal Disk driver	02-2	v-04	PAGE	
ructures and records:				
N at m e	Width Shift	# field: Width	S Mask	Initial
DATA	0016	0009		
CMDLEN	0000	****		
CMD	0002			
MEDIA	OOOD			
TRANS	000E 0012			
START	0014			
gments and groups:				
Name	Size	align	combine	class
ROUP	GROUP 034A	BYTE	PUBLIC	'CODE'
mbo1s:	0344	5112	PUBLIC	
N a m e	•	W = 3	***-	
	Type	Value	Attr	
XDP	Number	0040		
X_PRN	Number L NEAR	0044	CODE	
BPTR	L WORD L NEAR	0013 0000	CODE	External
SY	L NEAR L WORD	00A1	CODE	
KBYTE	L BYTE L NEAR	0017 0281	CODE	
4DA1	L NEAR	02C0	CODE	
K_INT	Number L NEAR	002C	CODE	
DOK	L NEAR L NEAR	0000	CODE	Global
MDMA	Number L BYTE	0043	CODE	
MN	L NEAR L byte	00DB 0005	CODE	
R1	L NEAR L Byte	0285 0006	CODE	
NTRK	L BYTE F PROC	0018 002A	CODE	Length =006C
ADFF	L WORD L WORD	0020	CODE	tangth - ooc
LEFT	L NEAR	0134	CODE	
PHYS	L NEAR L NEAR	01D7 01BE	CODE	
SEC	L NEAR L NEAR	011E 01EB	CODE	
	02-2	0 - 8 4	PAGE	Symbols-2
DOS 2.00 Universal Disk driver				Symbols-2
DDS 2.00 Universal Disk driver	L BYTE L NEAR	0010 002A	PAGE CODE CODE	Symbols-2 Global
ODS 2.00 Universal Disk driver IVER	L BYTE L NEAR Number L BYTE	0010 002A 0065 0001	CODE CODE	
IVER	L BYTE L NEAR Number L Byte L NEAR Number	0010 002A 0065 0001 0286 0065	CODE CODE CODE	
IVER	L BYTE L NEAR NUMBET L BYTE L NEAR NUMBET L WORD L WORD	0010 002A 0065 0001 0286 0065 000C	CODE CODE	Global
IVER	L BYTE L NEAR NUMBET L BYTE L NEAR NUMBET L WORD L WORD V WORD L BYTE	0010 002A 0065 0001 0286 0065 000C 000C	CODE CODE CODE CODE CODE	
IVER	L BYTE L NEAR NUMBER L NEAR NUMBER L WORD L WORD V WORD L BYTE L WOYD L BYTE	0010 0025 0065 0061 0285 0060 0060 0000 0029 0000	CODE CODE CODE CODE CODE	Global
IVER	L BYTE L NEAR NUMBER L BYTE L NEAR NUMBER L WORD L WORD V WORD L BYTE L WORD	0010 002A 0065 0001 0286 0065 000C 000E 0000	CODE CODE CODE CODE CODE CODE	Global
IVER	L BYTE L NEDGT L BYTE L NEDGT L WORD L WORD L WORD L WORD L BYTE L BYTE L BYTE	00 1 0 00 2 A 00 6 5 00 0 8 5 00 0 0 E 00 0 0 E 00 0 0 2 9 00 0 A C 00 0 0 8 00 0 1 5	CODE CODE CODE CODE CODE CODE CODE CODE	Global
VER	L BYTEL NEWSTEEL NEWSTEEL NEWSTEEL NEWSTEEL WOORD L WORD L BYTEL BYTEL BYTEL BYTEL BYTEL NUMBER	0010 002A 0065 0065 0065 0006 0000 0002 0002 0004 0008 001D 0045 0033 0002	CODE CODE CODE CODE CODE CODE CODE CODE	Global
IVER (C	L BYTE L NEAR NUMBER L NEAR NUMBER L NEAR NUMBER L WORD L WORD L BYTE L BYTE L BYTE L BYTE NUMBER NUMBER NUMBER NUMBER NUMBER NUMBER	0010 002A 0065 00001 0286 0006C 0000C 0000C 00029 0000A 0000B 0000B 0000B 0000B 0000B 0001D 0045 0001D	CODE CODE CODE CODE CODE CODE CODE CODE	Global
IVER	L BYTE L NEDER NUMBER L NEDER L NEDER L NEDER L WORD L WORD L BYTE NUMBER NUMBE	00 1 0 00 2 A 00 6 5 00 0 0 1 02 8 6 00 0 0 5 00 0 0 0 00 0 0 0 0 0 00 0 0 0 0 0 0 00 0 0 0 0 0 0 0 00 0 0 0 0 0 0 0 0 00 0 0 0 0 0 0 0 0 0 0 00 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CODE CODE CODE CODE CODE CODE CODE CODE	Global
VER	L BYTELL WOYTELL BYTELL WOYTELL BYTELL	00 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CODE CODE CODE CODE CODE CODE CODE CODE	Global
IVER	L BYAGE L WORD L WORD L BYTE L WORD L WOYTE L BYTE L BYTE L BYTE L BY MEADE TE NUMBER TO NUMBER	0010 0024 0065 00001 0286 00060 00060 0000 0000 0000 0000 00	CODE CODE CODE CODE CODE CODE CODE CODE	Global External Global
VER	L BY TELL WOOD TELL WOOD TELL BY TELL	0010 0025 0005 0005 0006 0006 0006 0006 000	CODE CODE CODE CODE CODE CODE CODE CODE	Global External Global
IVER	L BYEAR L WOORD L WOORD L WOORD L WOORD L BYTEL BY TELL BY TEL	00 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CODE CODE CODE CODE CODE CODE CODE CODE	Global External Global
IVER	L BYTELL WORDD L BYTELL WORDD L BYTELL WORDD L BYTELL BYTE	0012A 00025 00065 00060 00060 00000 00029 00000 00045 00000 0001D 00000 00007 00000 00000 00000 00000 00000 00000 0000	CODE CODE CODE CODE CODE CODE CODE CODE	Global External Global
DDS 2.00 Universal Disk driver IVER K. K. K. K. KORCT KORV KINIT KIO KOFF KSEG KTBL KTMR BBL L N N RMSK COM IITO ENS EAD RITE RITE	L BYTELL BYTELL BYTER NUMBER OF THE NUMBER OF THE L BYTELL BYTER NUMBER OF THE NUMBER	0012A 00025 00065 00060 00060 00060 0000 00029 0000 0000	CODE CODE CODE CODE CODE CODE CODE CODE	Clobal External Clobal
ODS 2.00 Universal Disk driver IVER	L NEDOTE L NOTE L NOTE L NEDOTE L NOTE L NOT	0012A 00025 00065 00065 00060	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
ODS 2.00 Universal Disk driver IVER	ERAPTER L NIBORD DE PROPERTO DE PROPERTO DE LA NIBORD DEL NIBORD DE LA	0012A50000000000000000000000000000000000	CODE CODE CODE CODE CODE CODE CODE CODE	Clobal External Clobal
IVER	ERAFER TO TEDE TERFORM TO THE TERFOR	0012A50000000000000000000000000000000000	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
IVER	ERFERENCE ER STERFER S	OOO 1 A A S A C A C A C A C A C A C A C A C A	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
IVER	ERTERTOR TO THE TERM TO THE TE	001245 00025 00065 00065 00065 00065 00006 00065 00006 00065 00006 00065 00006	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
IVER	ERTERTODE EDETETET RITTER PROPERTY AGAINST AGA	00025 00025 00025 00025 00025 00025 00029	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
IVER K	ERFERENDE E E EFER FER FER FER FER FER FER FER F	OOO 25 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
IVER K	ERFERENDE E E E EFER PETER PET	OOO 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal Global
YBEC	ERTERTOR TO THE TERM TO THE TOTAL TO THE TERM TAGE TO THE TOTAL TO THE TOTAL T	OOO 1 D S S S S S S S S S S S S S S S S S S	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal External
DUS 2.00 Universal Disk driver IVER K	ERTERTO DE E E E TETRETE E PER EN LA PARE DE LA	OOO 10 A B B S C C C C C C C C C C C C C C C C C	CODE CODE CODE CODE CODE CODE CODE CODE	External Clobal Global
DDS 2.00 Universal Disk driver IVER K. K. K. K. K. K. K. KORV KINIT KINIT KINIT KIDE KOFF KSEG KTBL KTMR BL L. N N RMSK COOM ITO ENS EAD ENS EAD FITE RITEE RITEE RITEE RITEE RITEE RITEE RITEE RITEE RITE PLEN DRV PLEN TRK P.	ERTERTODEEDETETETRITTER TO THE STATE TO THE	00025 00025 00025 00025 00000 00000 00000 00000 00000 00000 0000	CODE CODE CODE CODE CODE CODE CODE CODE	Clobal External Clobal External

	crosoft MACRO .00 Universa	D Assembler 1 Disk driver	02-20-8	4 PAGE	Symbols-3	
WCHK.			L NEAR OI	6A CODE		
			L NEAR OI	50 CODE		
MDSK			L NEAR OZ	SC CODE		
COUNT			L WORD OC	1B CODE		
RV1.			L NEAR OI	FA CODE		
ECSIZ			L WORD OG	OA CODE		
ECTOR			L WORD O	24 CODE		
ET_DRV			L NEAR O	EC CODE		
				OS CODE		
PARE41			Number 00	41		
				42		
				19 CODE		
				02		
				28 CODE		
				02 CODE		
				11 CODE		
				15 CODE		
				46		
				40		
				0 1		
				D6 CODE		
				CF CODE		
				A1 CODE		
				002		
80			Number 00	47		
arning	Severe					
rrors	Errors					
	0					

Symbol Cross Reference		(# is d	eriniti	on,	Cref	- 1		
AUXDP	. 57#							
AUXP								
AUX_PRN	. 47#							
_								
BAILOUT		606	622#					
BPBPTR		485	570					
BUFFER		398	408	421	599	607	632	639
BUSY	215#	224						
CALLADDD	. 142#	443	489					
CALLADDR		70	70					
CHKBYTE		487	,,					
CHKMED		585#						
CKMDA1		591#						
CLK INT								
CMD		169	183					
CMDERR	209#	233						
CMDLEN	. 87#							
CMDOK		187#						
CMDSTR		115#	442					
CODE		69#	69	657				
COMDMA								
COMMAND		258	261	264	401	403		
COMN		262	265#					
COUNT		175 353	352 444	558 447	572 494	536		
CORIRR	. 121#	353		/	454	336		
DCR1	. 541	545#						
DENSITY		501						
DENTRK		505						
DISPATCH		204						
DMAOFF		180	298	311	384	407	422	
DMASEG		182	305	411	423			
DOLEFT		338#						
DOPHYS		418	441#					
DORD		418#						
DOSEC								
DP1		448#						
DRIVER		174	466	585	615	627		
DSK		154# 603	636					
DSKDRV		374	503	526				
DSKINIT		552#	303	320				
DSKIO		552#						
DSKOFF		398						
DSKSEG		399						
DSKTBL		467	474	586	617			
DSKTMR		149#	376	529				
DTBL	. 189	219#						
DTL		511						
ENN	. 124#	509						
ERRMSK								
EXCOM								
EXITO	. 637	643	645#					
FDENS	. 104#							

MSDDS 2.00 Universal Disk driver

MSDDS 2.00 Universal Disk driver				,						
Symbol Cross Reference		definit	ion)	Cref	- 2					
READ	103# 258 102# 264	403	3							
WRITEV	105# 261	401		ri p						
APLEN	123# 507 221 567	,	3							
NITTBL	17# 553									
OCNT	581# 631 86# 99									
ODRV	578# 600 577# 601	628 634								
OOFF	582# 599 597 626	632 652#								
OSEC	579# 629 583# 588	633								
OTRK	580# 630	000								
DP	55# 56#									
MAYBEC	528 530	532#								
C_FLG	13 592 95# 540	648# 557								
MEDIACHK	220 519									
OINT	170 172									
IORMAL	593 596 146# 176	289	334	351	387					
KMDIA	595 611	613#								
HYSBUF	372 397	*								
PHYSCHT	120# 400 119# 322		385							
RET	419 426 58#	•								
RNP	60# 32#									
TRSAVE	16# 168	191	209	215	348	539	556	571		
DWRET	266 291 373 390	347#								
EDWSEC	289# 340 223 258									
EADWRITE	339 371 211# 222									
tom	51#	225	226	229	230	231				
WCHK	383# 347 353	,								
AMDSK	523 534									
DRV1	140# 338 468 472									
ECSIZ	126# 383 145# 178	406 317	420 386	497						
SET_DRV	265 465	520	569							
MSDCS 2.00 Universal Disk driver										
Symbol Cross Reference	(# is	definit	ion)	Cref	- 3					
OFTCHK	612 625 44#	,								
PARE42	45# 139# 318	331	499							
TART.	98# 177 90# 172	559	574	216	750	E 4 4				
MRCNT	107# 149	192	210	216	350	544				
MRDRV	148# 375	376 527								
RACK	118# 321 96# 179	446 181								
RKPTR	134# 354 136# 476	491	535							
ART	49#									
NIT	88# 173									
ERT	43#									
/PRSNT	64# 227 264									
/RITE	228 261									
VRITE	228 261 402 405	#								
VRITEVRITEV		*								
VRITE	402 405	•								

```
The Microsoft MACRO Assembler Hard Disk driver
                                                                                             02-20-84
                                                                                                                          PAGE
                                                                                                        PAGE 60,132
TITLE Hard Disk draver
NAME HDISK
234567891113456789012345678901
                                                                                                        COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
All rights reserved.
                                                                                                        Hard Disk Driver for Version 2.05 of MS-DDS. 09/22/83
                                                                                                        GROUP CODE
SEGMENT BYTE PUBLIC 'CODE'
CS:CGROUP, DS:CGROUP, ES:CGROUP, SS:CGROUP
                                                                                       CGROUP
                   0000
                                                                                       CODE
                                                                                       ASSUME
                                                                                                                         HREAD: NEAR, HWRITE: NEAR, HWRITEV: NEAR
HFORMAT: NEAR, HINIT: NEAR, HMCHK: NEAR
HVDISK: NEAR, INTHDL: NEAR, DSK_INIT: NEAR
KLT_F: BYTE, PTRSAVE: DWORD, NDRV: BYTE
HIOCTL: NEAR, UP_BAT: NEAR
TEMP_TRK: WORD, TEMP_SEC: BYTE
                                                                                                        EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
                                                                                                        PUBLIC INI_TAB, RDXLT
PUBLIC GETFTN, GETFTNO, GETPEA
PUBLIC PARTITION, MNPARTS, PART_SIZE
PUBLIC HDSKO,DSK_INT
                                                                                                         LIST
                                                                                                        SUBTTL Common Drive parameter block definitions
```

```
The Microsoft MACRO Assembler 
Hard Disk driver
                                                                                      02-20-84
                                                                                                                PAGE
                                                               Common Drive parameter block definitions
;----- Start of Drive Parameter Block.
                             3333
3333
3333
3333
3333
3333
3333
                                                                                SECSI DW
ALLOC DB
RESSEC DW
FATS DB
MAXDIR DW
SECTORS DW
MEDIAID DB
FATSEC DW
                                                                                                                                                ;Sector size in bytes.
;Number of sectors per alloc. block.
;Reserved sectors.
;Number of FAT's.
;Number of root directory entries.
;Number of sectors per diskette.
;Media byte ID.
;Number of FAT Sectors.
                  0000
0002
0003
0005
                                                                                                                                                                                                                                  (dpb)
(dpb)
(dpb)
                                                                                                                                                                                                                                   (dpb)
                   0006
                                                                                                                                                                                                                                  (dpb)
                   0008
000A
000B
                                                                                 ;----- End of Drive Parameter Block. (Extensions follow)
                                                                                SECTRK
                                                                                                                                                 Number of Sectors per track.
Number of heads per cylinder.
Number of hidden sectors.
                                                                                                DW
                                                                                 HIDDEN
                  0013
                                                                                DBP
                                                                                                 ENDS
                            0200
04
0020
02
0100
0000
F8
0000
0010
                  0000
0002
0003
0005
                                                                                HDSKO
                                                                                                 DBP
                                                                                                               <512.4.2*16.2.256.0.0F8H.0.16.4.0>
                   0008
                   000A
000B
                   OOOF
                   0011
                              0000
                  0013
0015
0016
0018
0018
0018
001D
001E
0020
0022
                             0200
04
0020
02
0100
0000
F8
                                                                                 HDSK1
                                                                                                 DBP
                                                                                                                 (512,4,2*16,2,256,0,0F8H,0,16,4,0)
                              0010
                              0004
                            0200
04
0020
02
0100
0000
F8
0000
0010
                  0026
                                                                                                                (512,4,2*16,2,256,0,0F8H,0,16,4,0)
                                                                                 HDSK2 DBP
                  0028
0028
0028
002C
002E
80
81
82
83
84
85
                   0031
                   0033
```

```
The Microsoft MACRO Assembler 
Hard Disk driver
                                                                02-20-84
                                                                                    PAGE
                                                                                               1 - 3
                                               Common Drive parameter block definitions
             0035
                      0004
0039
                      0200
                                                           HDSK3
                                                                                    <512,4,2*16,2,256,0,0F8H,0,16,4,0>
             0038
003E
003E
003F
                      04
                      02
             003F
0041
0043
0044
0046
                      0000
F8
                      0010
                      0000
             004E 0000 R
004E 0013 R
0050 0026 R
0052 0039 R
                                                                                   OFFSET HDSKO
OFFSET HDSK1
OFFSET HDSK2
OFFSET HDSK3
                                                           INI_TAB DW
                                                                       DW
DW
DW
                                                                       SUBTIL Strategy and Software Interrupt routines.
```

```
The Microsoft MACRO Assembler 
Hard Disk driver
                                                                                                   02-20-84
                                                                                                                                 PAGE
                                                                                                                                                1 - 4
                                                                          Strategy and Software Interrupt routines.
;Define offsets for io data packet
                                                                                            IDDAT
                                                                                                               STRUC
                                                                                            CMDLEN
UNIT
CMD
STATUS
                                                                                                              DB
DB
                                                                                                                                                                      ;LENGTH OF THIS COMMAND;SUB UNIT SPECIFIER;COMMAND CODE;STATUS
                     0000
                    0001
0002
0003
0005
                                                                                                                                 8 DUP (2)
                                                                1
                                                                                            MEDIA
TRANS
COUNT
                                                                                                              DB
DD
DW
DW
                                                                                                                                                                      ;MEDIA DESCRIPTOR
;TRANSFER ADDRESS
;COUNT OF BLOCKS OR CHARACTERS
;FIRST BLOCK TO TRANSFER
                                                                                            IDDAT
                                                                                                               FNDS
                                                                                                               DSK_INT - Disk driver interrupt routine
                                                                                                              When perform disk function command, register contains:
AL = Unit code.
AH = Media descriptor.
CX = Contains byte/sector count.
DX = Starting Logical sector.
ES:DI = Transfer address
                                                                                            dispatch proc far
                    0054
0054
0055
0056
0057
                                                                                            DSK_INT:
PUSH
PUSH
PUSH
                                  50
53
51
52
56
                                                                                                               PUSH
                                                                                                               PUSH
PUSH
PUSH
PUSH
PUSH
                                 56
57
55
1E
02E: C5 1E 0000 E
80 7F 02 00
74 01
FB
C7 47 03 0000
                    0058
0059
005A
005B
005C
005D
0062
0066
0068
                                                                                                                                 BP
DS
                                                                                                                                 ES
                                                                                                              LDS
CMP
JE
STI
MOV
                                                                                                                                 BX,CS:[PTRSAVE] ;Retrieve pointer to I/O Packet.
BYTE PTR [BX.CMD],O ;INIT?
NOINT ;DON'T RENABLE INTS ON INIT
                                                                                            NOINT:
                                                                                                                                 WORD PTR [BX.STATUS],O ;Clear status initially
                                  8A 47 01
8A 67 0D
8B 4F 12
8B 57 14
97
8A 47 02
                                                                                                               MOV
MOV
MOV
MOV
XCHG
MOV
                                                                                                                                 AL,[BX.UNIT]
AH,[BX.MEDIA]
CX,[BX.COUNT]
DX,[BX.START]
                                                                                                                                                                     ;AL = Unit number
;AH = Media descriptor.
;CX = Contains byte/sector count.
;DX = Starting Logical sector.
;Save Unit and Media Temporarily.
;Retrieve Command type. (1 => 11)
                     008E
                     005E
0071
0074
0077
007A
                                                                                                                                  AL,[BX.CMD]
```

```
The Microsoft MACRO Assembler 
Hard Disk driver
                                                                                                      02-20-84
                                                                                                                                     PAGE
                                                                                                                                                     1 - 5 है
                                                                          Strategy and Software Interrupt routines.
164
165
167
168
170
171
175
177
177
177
178
179
180
183
183
185
186
187
188
188
189
191
191
191
                                                                                                                                                        ; CHECK IN RANGE, BOUND
                    007E
0080
0082
0084
                                 3C OC
76 O2
80 OD
                                                                                                                  CMP
JBE
MDV
                                                                                                                                     AL,12
CMDOK
AL,13
                                                                                             CMDOK:
                    OO84 32 E4 OO86 D1 EO OO88 O5 OOA9 R OO8B 8B FO OO8D 97 OO8E C4 7F OE
                                                                                                                  XOR
                                                                                                                                      AH, AH
                                                                                                                                     AM, AH
AX, OFFSET DSK_TBL
SI, AX
AX, DI ; get unit and media back
DI, [BX.TRANS] ; DI = transfer addr
; ES=segment
                                                                                                                  XOR
SHL
ADD
MOV
XCHG
LES
                    0081 OE
0082 1F
0083 FF 14
0085 2E: C5 1E 0000 E
008A 81 4F 03 0100
                                                                                                                  PUSH
POP
Call
LDS
OR
                                                                                                                                     DS
WORD PTR [SI]
BX,CS:[PTRSAVE]
WORD PTR [BX.STATUS],100H
                                                                                                                                                                                                            ; set done flag
                                   07
1F
5D
5F
5E
59
58
58
CB
                                                                                                                  POP
POP
POP
POP
POP
POP
POP
RET
                                                                                                                                      ES
BP
DI
SI
DX
CX
BX
                     0040
00A1
00A2
00A3
00A4
00A5
                      00A7
                      8A00
                                                                                            DISPATCH ENDP
                      00A9
```

```
The Microsoft MACRO Assembler
Hard Disk driver
                                                                                                                                                      02-20-84
                                                                                                                                                                                                  PAGE
                                                                                                                                                                                                                            1 - 6
                                                                                                            Strategy and Software Interrupt routines.
                                                                                                                                         PAGE
DSK_TBL:
194
195
196
197
198
199
200
201
202
203
204
205
206
                              OOA9
OOA9
OOAB
OOAD
                                                                                                                                                                                                   DSK_INIT
MEDTA_CHK
GET_BPB
HIDGTL
DSK_READ
BUSY
RETURN
DSK_WRT
DSK_WRT
DSK_WRT
VRETURN
RETURN
RETURN
RETURN
RETURN
RETURN
RETURN
LOCAL
                                                                                                                                                                                                                                                  ;0 - Initialize Driver.
:1 - Return current media code.
:2 - Get Bios Parameter Block.
:3 - Hard disk IDCTL
:4 - Block read.
:5 - (Not used, return busy flag)
:6 - Return status. (Not used)
:7 - Flush input buffer. (Not used.)
:8 - Block write.
:9 - Block write with verify.
:10 - Return output status.
:11 - Flush output buffer. (Not used.)
:12 - Hard disk IDCTL
:13 - Error
                                               0000 E
00DB R
00E5 R
0000 E
                                                                                                                                                                       DW
DW
DW
                                                                                                                                                                       DW
DW
DW
DW
DW
                                00B1
                                                   OOFB R
                                                  OOFB R
OODO R
OOCF R
O103 R
OOCF R
OOCF R
OOCF R
                               0083
0085
0087
0089
                                COBB
                                                                                                                                                                        DW
DW
DW
                                OOBD
207
208
209
210
211
                                00BF
00C1
00C3
                                                                                                                                                                      SUBTIL Common error and exit points.
```

```
The Microsoft MACRO Assembler Hard Disk driver
                                                                                                   02-20-84
                                                                                                                                 PAGE
                                                                         Common error and exit points.
                                                                                             PAGE
; Common error processing routine.
; AL contains actual error code.
212
                                                                                                     Error # 0 = Write Protect violation.

1 = Unkown unit.

2 = Drive not ready.

3 = Unknown command in I/O packet.

4 = CRC error.

5 = Bad drive request structure length.

6 = Seek error.

7 = Unknown media discovered.

8 = Sector not found.

9 = Printer out of paper.

10 = Write fault.

11 = Read fault.

12 = General failure.

command error
222
223
2245
225
227
2230
2331
2333
2334
2335
2337
2338
2340
2441
2442
2444
                     OOC5
OOC5 2E: C5 1E 0000 E
OOCA 81 4F 03 8003
OOCF C3
                                                                                             CMDERR:
                                                                                                              LDS
                                                                                                                                 BX,CS:[PTRSAVE]
WORD PTR [BX.STATUS],8003H
                                                                                             OR
RETURN: RET
                                                                                                              return busy
                                                                                             ;
                      BUSY:
                                                                                                               LDS
                                                                                                                                  BX,CS:[PTRSAVE]
WORD PTR [BX.STATUS],200H
                                                                                                               OR
Ret
                                                                                                               SUBTTL Media check routine
```

```
PAGE
The Microsoft MACRO Assembler Hard Disk driver
                                                                02-20-84
                                                                                                1 - 9
                                                Build and return Bios Parameter Block for a diskette.
PAGE
                                                             ; Build Bios Parameter Blocks.
                                                                        On entry: ES:BX contains the address of a scratch sector buffer.
                                                                                        AL = Unit number.
AH = Current media byte.
                                                                                        Return a DWORD pointer to the associated BPB in the Request packet.
                                                                        On exit:
                                                           GET_BPB:
XOR
SHL
             00E5
00E5
00E7
00E9
                    32 E4
D1 E0
8B F0
8B 84 004C R
                                                                                    AH, AH ; clear h
AX, 1 ; shift to
SI, AX
AX, WORD PTR INI_TAB[SI] ; get BPB
                                                                                                                        ; clear high byte ; shift to word offset
                                                                        MOV
              OOEB
                                                                        MDV
             00EF
00F4
00F7
00FA
                      2E: C5 1E 0000 E
89 47 12
8C 4F 14
C3
                                                                                    BX,CS:[PTRSAVE]
WORD PTR [BX.COUNT],AX
WORD PTR [BX.COUNT+2],CS
                                                                        LDS
                                                                        MOV
MOV
RET
                                                                        SUBTTL MSDOS 2.x Disk I/O drivers.
```

```
The Microsoft MACRO Assembler Hard Disk driver
                                                                                                             1 - 10
                                                                           02-20-84
                                                                                                 PAGE
                                                        MSDOS 2.x Disk I/O drivers.
288
289
290
291
2993
294
295
297
298
3001
3003
3004
3006
3007
                                                                      ENTRY:
                                                                                                 AL = Disk I/O driver number
AH = Media byte.
CX = Number of sectors to transfer
DX = Logical starting sector.
ES:DI = Disk transfer address
                                                                                   EXIT:
                                                                      *********************
                00FB
00FB
00FE
0101
                                                                      DSK_READ
                         E8 0167 R
E8 0000 E
EB 0E
                                                                                   CALL
                                                                                                 SETPAR
HREAD
Short comm
                                                                                    JMP
0103
0103
0106
0108
                                                                      DSK_WRTY:
CALL
CALL
JMP
                          E8 0167 R
E8 0000 E
EB 06
                                                                                                 SETPAR
HWRITEV
SHORT COMM
                010B
010B
010E
                                                                                                  SETPAR
HWRITE
                                                                                   CALL
               0111
0111
0113
0115
0117
0117
0124
0126
0128
0128
0129
0127
0131
0134
0138
                        73 2B
3C OF
74 27
2E: C5 1E 0000 E
C7 47 12 0000
B9 0102
3C 03
74 10
B1 04
F5 C4 40
75 09
B1 08
F6 C4 10
75 02
B1 0C
                                                                      COMM:
                                                                                                 COMM1 ; exit if no error AL,SERR ; soft error CDMM1 ; return as good BX,CS:[PTRSAVE] WORD PTR [BX.COUNT],O ; any erro CX,O102H ; assume drive not AL,DRIVEDF CDMM2
                                                                                    JNC
CMP
JZ
LDS
                                                                                                                            MOV
                                                                                    MOV
CMP
JZ
MOV
TEST
                                                                                                  CL,4
AH.CRCER
                                                                                                                             ; assume CRC error
                                                                                    JNZ
MOV
TEST
JNZ
MOV
                                                                                                  COMM2
                                                                                                 CL,8
AH,IDNF
CDMM2
CL,OCH
                                                                                                                             ; assume sector not found
                                                                                                                              ; set as general error
                                                                      COMM2 .
                0138 09 4F 03
                                                                                    DR
                                                                                                 WORD PTR [BX.STATUS],CX
                013B E8 0000 E
                                                                                   CALL
                                                                                                 UP_BAT
                                                                                                                             : update BAT
                013E
013E C3
                                                                      COMM1:
                                                                                   RET
                                                                                    SUBTTL Partition Table
```

```
PAGE
  The Microsoft MACRO Assembler
                                                                            02-20-84
                                                         Partition Table
PAGE
                                                                                     PARTITION
                                                                                     This is the logical drive partitions for this OS.

If any one of the drive is not present, the first track #
should contain a O. (since track O will not be used for any OS)
Note: this table will have more information about each partition.
                                                                                      So, PART_SIZE will change!!!
                = 0004
= 0006
                                                                       MNPARTS
Part_Size
                                                                                                                               ; # of max. partitions in this system ; partition info size
                                                                                                    EOU
                                                                                                    EQU
                013F
                                                                        PARTITION
                                                                                                    LABEL WORD
                                                                                                                                ; first track #
; last track #
; reserved
                           0000
                           0000
                0143
                         0000
                                                                                      DW
                0145
014B
0151
                           0000 0000 0000
0000 0000 0000
0000 0000
                                                                                     DW
DW
DW
                                                                                                    0,0,0
                0157 01 08 0F 06 0D 04 08 02 015F 09 10 07 0E 05 0C 03 0A
                                                                       RDXLT DB
                                                                                                   1,8,15,6,13,4,11,2
                                                                                     DB
                                                                                                   9, 16, 7, 14, 5, 12, 3, 10
```

```
The Microsoft MACRO Assembler
                                                                  02-20-84
                                                                                       PAGE
                                                                                                  1 - 12
Hard Disk driver
                                                  Partition Table
SETPAR - set up disk interface parameters
                                                                          ENTRY:
                                                                                      AL = unit code
DX = logical sector #
BL = sector #
BH = surface #
DX = Cylinder #
                                                                          EXIT:
                                                                                       DX = cylinder #
(XLT_F) = logical/physical flag
              0167
                                                              SETPAR:
              0167
016C
016E
                      C6 06 0000 E 01
8B F2
E8 01A7 R
                                                                                       BYTE PTR XLT_F,1 ; assume using translate table SI,DX ; save sector # GETFTN ; get partition first track #
                                                                           MOV
                                                                           MO V
Call
              0171
0173
0177
                       88 C6
81 E6 OOOF
8A 9C O157 R
                                                                           MOV
                                                                                       AX,SI ; get sector #
SI,OFH ; form logical sector #
BL,BYTE PTR RDXLT[SI] ; get sector # from translate table
                      D1 E8
D1 E8
D1 E8
D1 E8
D1 E8
3D 0002
7D 09
8B DE
FE C3
C6 06 0000 E 00
              017B
017D
017F
                                                                          SHR
SHR
SHR
                                                                                       AX.1
                                                                                                              ; shift to track offset
                                                                                       AX, 1
AX, 1
AX, 1
AX, 1
AX, 2
SETPAR1
              0181
0183
0186
0188
018A
                                                                           SHR
CMP
JGE
                                                                                                                ; check within first 2 track
                                                                                                                ; no skew on first 2 track
; sector # is base 1
0 ; don't use translate table
                                                                           MOV
                                                                                        BX,SI
                                                                                        BYTE PTR XLT_F,O
              0191
0191
0193
0197
                                                                                      DX, AX
TEMP_TRK, DX
TEMP_SEC, BL
CLTPN
                       03 DO
89 16 0000 E
88 1E 0000 E
                                                                                                              ; add partition offset
; save track # in case error
; save sector #
                                                                           MOV
                                                                           MDV
                                                               <u></u>
                                                                           CLTPN - Convert Logical Track # to Physical surface/cylinder #
                                                                          ENTRY: DX = logical track #
EXIT: BH = surface #
DX = cylinder #
                                                                           USE:
              019B 8A FA
019B 8O E7 03
01AO D1 EA
                                                                                                             ; get low track byte
; keep surface only
; get rid of surface bits
                                                                          MOV
```

The Microsoft MACRO Assembler 02-20-84 PAGE 1-13
Hard Disk driver
Partition Table

430 01A2 D1 EA SHR DX,1
431 01A4 C3 RET

```
The Microsoft MACRO Assembler Hard Disk driver
                                                                                                                 PAGE
                                                                Partition Table
                                                                                 ENTRY:
                                                                                                                GETFIN: AL = hard disk partition # [from 0 to MNPARTS-1]
GETFINO: AL = MS-DOS disk drive offset
DX = start track # for this partition
2 flag is not set if the partition is not exist
(start track # = 0), or wrong drive #
BX = partition entry start addr
AX, BX
                                                                                                USES:
                 01A5
01A7
01A7
01A7
33 D2
01A9 3C 04
01AB 73 05
01AD 8 011
01B0 85 17
01B2
01B2 23 D2
01B4 C3
                                                                                 GETFTNO:
                                                                                SUB
GETFTN:
                                                                                                                 AL, HDISK
                                                                                                                                                 ; set to Wini offset
                              33 D2
3C O4
73 O5
E8 O1B5 R
8B 17
                                                                                                XOR
CMP
JNB
CALL
MOV
                                                                                                                 DX,DX
AL,MNPARTS
GETFTN1
GETPEA
DX,[BX]
                                                                                GETFTN1:
AND
RET
                                                                                                                                                 ; get partition start addr
; get start track #
                                                                                                                 DX,DX
                                                                                                 GETPEA - GET Partition Entry Addr
                                                                                                ENTRY: AL = hard disk partition # (from 0 to MNPARTS-1)
EXIT: AX = BX = partition entry start addr
USES: AX, BX
                  0185
0185
0187
0189
018C
018E
                                                                                 GETPEA:
                              83 06
F6 E3
05 013F R
88 D8
C3
                                                                                                 MOV
MUL
ADD
MOV
RET
                                                                                                                 BL, PART_SIZE
BL
AX, OFFSET PARTITION
                                                                                                                                                                ; get partition size
; set to partition entry offset
; add start addr
; be nice to BX
                  01BF
                                                                                CODE
                                                                                                 ENDS
END
```

e Microsoft MACRO Assembler d Disk driver	02-2			
uctures and records:				
N a m e	Width Shift	# field Width		Initial
	0013	ОООВ		
ECSZ	0000 0002			
ESSEC	0003			
AXDIR	0006			
EDIAID	OOOA			
ECTRK	000D 000F			
IDDEN	0011 0016	0009		
MDLEN	0000	0009		
MD	0001 0002 0003			
TATUS	000D			
RANS	000E 0012			
TART	0014			
ments and groups: Name	. :	:		
N a m e	Size Group	align	combine	Class
ODE	018F	BYTE	PUBLIC	'CODE'
bo 1s :				
Name	Туре	Value	Attr	
TC	Number Number	0004		
ETRY	Number	0000		
AST /	Number	0012 0005		
OFF	Number	0002 0008		
_BPD	Number Number	0003 0001		
OSN	Number Number	0004 0006		
PBT	Number	0000		
ō	Number Number	0002		
		8000		
15	Number Number		******************************	
e Microsoft MACRO Assembler		0004	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver	Number	0004	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver	Number 02-2 Number Number	0-84	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver	Number 02-2 Number Number Number	0-84 0-008 0010 0020 0040	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number	0-84 0-84 0008 0010 0020 0040 0080 0008	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number Number Number Number	0-84 0-84 0008 0010 0020 0040 0008 0008 0003	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number Number Number Number Number	0004 0008 0010 0020 0040 0008 0008 0003 0005 0021 0000	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number Number Number L NEAR Number	0004 0008 0010 0020 0040 0008 0008 0003 0005 0001		Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number	0-84 0-84 008 0010 0020 0040 0080 0008 0003 0006 0021 0000 0080 0001	CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number Number Number Number Number L MEAR Number Number L MEAR Number Number L MEAR	0-84 0-84 0008 0010 0020 0040 0008 0008 0009 0001 0000 0001 0004 0004 0004 0004		Symbols-2
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 7 7 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Number O2-2 Number Number Number Number Number Number Number L NEAR	0004 0008 0010 0020 0040 0080 0003 00021 0000 0001 0000 0001 0008 019B 019B 019B 019B	CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 7 7 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Number O2 - 2 Number Number Number Number Number Number Number Number L NEAR	0004 0008 0010 0010 0020 0040 0008 0001 0001 0001 0001 0001 000	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number O2 - 2 Number Number Number Number Number Number Number L NEAR Number L NEAR L NEAR L NEAR L NEAR L NEAR	0004 0008 0010 0020 0040 0020 0040 0008 00021 00D0 0080 0001 00D0 0080 0001 0001 0013E 0013E 0013E 0013E	CODE CODE CODE CODE	Symbols-2
2	Number O2 - 2 Number Number Number Number Number Number Number Number L NEAR Number L NEAR	0004 0-84 0008 0010 0020 0040 0008 0002 0080 0001 0000 0080 0001 0001 0001 0001 0001 0001 0001 0001 0001 0001 0001	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number 02-2 Number Number Number Number Number Number Number Number L NEAR Number L NEAR Number	0004 0-84 0008 0010 0020 0040 0008 0003 0005 0021 0004 0008 0001 0004 019B 00C5 0021 013E 0001 013E	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number O2 - 2 Number Number Number Number Number Number Number Number L NEAR Number L NEAR Number	0004 0-84 0008 0010 0020 0040 0008 0008 0003 0001 0004 0008 0138 0001 0138 0001 0138 0001 00040 0013	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3	Number O2-2 Number Number Number Number Number Number Number Number L NEAR Number L NEAR Number Number Number Number Number Number Number Number Number	0004 0-84 0008 0010 0020 0040 0008 0008 0008 0001 0004 0004 000	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 FC OFF EC OFF WEC OFF Y2 FL OFF Y2 FL OFF Y3 FL OFF Y4 FL OFF Y4 FL OFF Y5 FL OFF Y6 FL OFF Y8 FR F	Number O2-2 Number Number Number Number Number Number Number Number Number L NEAR Number L NEAR Number	0004 0-84 0008 0010 0020 0040 0008 0008 00021 0004 00021 0004 00011 00011 00011 0138 00011 00011 00011 00011 00011 00011 00011 00010 00000	CODE CODE CODE CODE CODE	Symbols-2
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7	Number Number Number Number Number Number Number Number Number Number Number Number L NEAR Number	0-84 0-84 0-84 0-84 0-10 0-10 0-10 0-10 0-10 0-10 0-10 0-1	CODE CODE CODE CODE CODE	Symbols-2 Length =0055
e Microsoft MACRO Assembler d Disk driver 3	Number O2-2 Number Number Number Number Number Number Number Number L NEAR Number	0004 0-84 0008 0010 0040 0008 0006 0008 0002 0008 0001 0000 0008 0111 00138 00001 00138 00011 00138 00011 00138 00011	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 0FF COFF COFF TY OFF Y Y Y 1 SPL	Number O2-2 Number Number Number Number Number Number Number Number Number L NEAR Number L NEAR Number	0004 0004 0008 00000 0008 00001 00000 00001 00001 00001 00001 00001 00001 00001 00001 00001 00001 00001 00001 00001 00001 000000	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3	Number O2 - 2 Number	0004 0004 0008 00010 00040 0008 00001 00080 00010 00080 000111188 000110 001380 000110 00080 000110 00080 000110 00080	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 4 5 6 6 7 6 7 6 7 7 7 7 7 7 7 7 7 7 7 7 7	Number O2 - 2 Number	0004 0-84 0008 0010 0008 0008 0008 0008 0008 00	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3	Number O2 - 2 Number	0004 0-84 0010 0040 0080 00080	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 5 5 6 7 0FF 6 7 0FF WEC_OFF WEC_OFF Y Y Y 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Number O2 - 2 Number	0004 0-84 0010 0010 0010 0008 0008 0008 0008 00	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 0FF COFF ECOFF HECOFF Y Y Y Y 2 IL S P N H H H H H H H H H H H H H H H H H H	Number	0004 0-84 0010 0040 0010 0040 0008 0008 0008 00	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 0FF ECOFF LBR OFF MEC OFF TY Y2 1L SPL PN ESPL PN ESPL PN ERR IP OK M M1 M1 M2 8660 BR M1 M1 M2 8660 BR M1 M2 M5 M6 M1 M1 M1 M1 M2 M6 M6 M6 M1 M1 M1 M2 M6 M6 M6 M6 M7 M8	Number O2-2 Number L NEAR Number L NEAR L	0 - 8 4 0 - 8 4 0 0 10 0 0 0 8 0 0 10 0 0 0 8 0 0 10 0 0 0 8 0 0 0 10 0 0 0 8 0 0 0 10 0 0 0 8 0 0 0 10 0 0 0 8 0 0 0 10 0 0 0 10 0 0 0 10 0 0 0 11 0 0 0 11 0 0 0 11 0 0 0 11 0 0 0 11 0 0 0 11 0 0 0 11 0 0 0 0	CODE CODE CODE CODE CODE CODE	
e Microsoft MACRO Assembler d Disk driver 3 4 5 6 7 7 0FF COFF COFF TY Y Y Y Y Y Y 1 L S P N H H H H H H H H H H H H H H H H H H	Number O2-2 Number L NEAR Number	0004 0-84 0008 0010 0040 0080 00080	CODE CODE CODE CODE CODE CODE	
### Company	Number	0004 0-84 0010 0020 0040 0080 0008	CODE CODE CODE CODE CODE CODE	

crosoft MACRO Assembler sk driver	02-20-84	PAGE	Symbols-3	
<u>.</u>	L NEAR 0054	CODE	Globel	
ND	L NEAR OOFB	CODE		
	L NEAR 010B L NEAR 0103	CODE		
	Number 0068 Number 0069			
	Number 0020 Number 0020			
	Number 0001 Number 0061			
	Number 0000			
	Number 0050 L NEAR 01A7	CODE	Global	
	L NEAR 01A5 L NEAR 01B2	CODE	Global	
	L NEAR 01B5 L NEAR 00E5	CODE	Global	
	Number 0008	CODE		
	Number 0004 L 0013 0000	CODE	Global	
	L 0013 0013 L 0013 0026	CODE		
	L 0013 0039	CODE		
	Number 0000 Number 0001			
	Number 0002			
	Number 0002 Number 0004			
	Number 0003 Number 0006			
	Number 0007			
	Number 000E L NEAR 0000	CODE	External	
	L NEAR 0000 L NEAR 0000	CODE	External External	
	Number 0114 Number 0294	-	····	
	L NEAR 0000	CODE	External	
	L NEAR 0000 L NEAR 0000	CODE	External External	
	L NEAR 0000 L NEAR 0000	CODE	External External	
	Number 00E0 Number 00A0	32 22		
	Number 0010		83 -5-3	
	L WORD 004C L NEAR 0000	CODE	Global External	
	Number 000A Number 0002			
	Number 0004 L NEAR 00DB	CODE		
	Number 0004 Number 0004	CODE	Global	
icrosoft MACRO Assembler	02-20-84	PAGE	Symbols-4	
isk driver		PAGE	Symbols-4	
sk driver	Number 0002 Number 0040 V BYTE 0000	CODE	Symbols-4 External	
sk driver	Number 0002 Number 0040			
FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 0004	CODE		
Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 0004 Number 001C L WDRD 013F	CODE		
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 0001C L WORD 013F Number 0000 Number 0000	C O D E C O D E	External	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 001C L WDRD 013F Number 0010	C O D E C O D E	External	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 002D Number 001C L WORD 013F Number 0000 Number 00002 Number 00002 Number 00004 Number 0004 Number 0005	C O D E C O D E	External	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 002D Number 0015 L WORD 013F Number 0000 Number 0000 Number 0000 Number 0004 Number 0004 Number 0005 Number 0005 Number 0005 Number 0004 Number 0005	C O D E C O D E	External	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 002D Number 001C L WORD 013F Number 0000 Number 0000 Number 0000 Number 0004 Number 0005 Number 0005 Number 0005 Number 0003 Number 0003 Number 00003 Number 00003 Number 00003	C O D E C O D E	External	
Sk driver OFF ON N N SS ZE F	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 001C L WDRD 013F Number 0000 Number 0000 Number 0002 Number 0004 Number 0005 Number 0005 Number 0004 Number 0004 Number 0003 Number 0003 Number 0000	C O D E C O D E	External	
FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 002D Number 001C L WDRD 013F Number 0000 Number 0002 Number 0004 Number 0004 Number 0005 Number 0005 Number 0003 Number 0003 Number 0003 Number 0003 Number 0000 Alias FALSE V DWDRD 0000	CODE	External Global Global	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 002D Number 001C L WORD 013F Number 0002 Number 0002 Number 0004 Number 0004 Number 0005 Number 0005 Number 0003 Number 0003 Number 0003 Number 0000 Alias FALSE V DWORD 0000 Number 0050 Number 0057 Number 0057 Number 0050	CODE	External Global Global	
N	Number 0002 Number 0040 V BYTE 0000 L NEAR 069 Number 0020 Number 001C L WORD 013F Number 0000 Number 0002 Number 0005 Number 0005 Number 0005 Number 0001 Number 0005 Number 0000 Number 0001 Number 0005 Number 0000 Number 0050 Number 0050	CODE	External Global Global	
Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 002D Number 001C L WORD 013F Number 0000 Number 0000 Number 0004 Number 0005 Number 0005 Number 0002 Number 0002 Number 0002 Number 0000 Alias FALSE V DWDRD 0000 Number 0060 Number 0067 Number 0058 Number 0028 Number 0028 Number 0020 Number 0058 Number 0058 Number 0008	CODE	External Global Global	
Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 065 Number 002D Number 001C L WORD 013F Number 0000 Number 0002 Number 0005 Number 0005 Number 0005 Number 0003 Number 0003 Number 0000 Number 0060 Number 0060 Number 0060 Number 0067 Number 0057 Number 0030 L BYTE 0157	CODE	External Global Global	
Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 0020 Number 0010 L WORD 013F Number 0000 Number 0002 Number 0004 Number 0005 Number 0005 Number 0005 Number 0003 Number 0000 Alias FALSE V DWDRD 0000 Number 0050 Number 0067 Number 0068 Number 0067 Number 0057 Number 0050	CODE	External Global Global External	
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Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 0020 Number 001C L WORD 013F Number 0000 Number 0000 Number 0004 Number 0005 Number 0005 Number 0005 Number 0000 Number 0000 Number 0005 Number 0005 Number 0000 Number 0000 Number 0000 Number 0000 Number 0000 Number 0060 Number 0060 Number 0057 Number 0058 Number 0020 Number 0057 Number 0050 L BYTE 0157 Number 0010 L NEAR 000F Number 0001 Number 0040	CODE	External Global Global External	
Sk driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 0020 Number 001C L WORD 013F Number 0000 Number 0002 Number 0005 Number 0005 Number 0005 Number 0005 Number 0003 Number 0000 Number 0006 Number 0060 Number 0067 Number 0057 Number 0050	CODE	External Global Global External	
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ik driver	Number 0002 Number 0040 V BYTE 0000 L NEAR 0059 Number 0020 Number 0015 Number 0015 Number 0002 Number 0004 Number 0006 Number 0005 Number 0005 Number 0007 Number 0056 Number 0056 Number 0056 Number 0057 Number 0056 Number 0010 L NEAR 00CF Number 0010 L NEAR 00CF Number 0010 Number 0040 Number 0056	CODE	External Global Global External	
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Sk driver FF	Number 0002 Number 0040 V BYTE 0000 L NEAR 0069 Number 0020 Number 0010 L WORD 013F Number 0000 Number 0000 Number 0005 Number 0005 Number 0005 Number 0000 Number 0000 Number 0000 Number 0005 Number 0000 Number 0006 Number 0057 Number 0057 Number 0000 L BYTE 0157 Number 0010 L NEAR 000F Number 0010 Number 0000 Number 0005 Number 0053 Number 0063 Number 0060 Number 0060 Number 0061	CODE	External Global Global External	
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### Company of the co	Number 0002 Number 0000 Number 0010 L NEAR 0009 Number 0010 L WORD 013F Number 0000 Number 0001 Number 0001 Number 0001 Number 0001 Number 0000	CODE	External Global Global External	
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Sk driver FF	Number 0002 Number 0000 Number 0000 Number 00010 Number 0000	CODE	External Global Global External	
Sk driver FF	Number 0002 Number 0000 Number 0000 Number 0001 Number 0001 Number 0000	CODE	External Global Global External	

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STEPR5	Number	0005		
STEPR6	Number	0006		
STEPR7	Number	0007		
STEPR8	Number	8000		
STEPRS	Number	0009		
STEPRA	Number	000A		
STEPRB	Number	000B		
STEPRC	Number	0000		
STEPRD	Number	000D		
STEPRE	Number	OOOE		
STEPRF	Number	000F		
STEPR_OFF	Number	004C		
ST_AST_OFF	Number	002B		
TEMP_SEC	V BYTE	0000	CODE	External
TEMP_TRK	V WORD	0000	CODE	External
TRK02	Number	0080		
TRKOE	Number	0002		
TRUE	Number	- 0001		
UP_BAT	LNEAR	0000	CODE	External
WGATE	Number	0010		
WINI_OFFSET	Number	0094		
WINI_SEG	Number	0096		
WPC_OFF	Number	004A		
WPRCMP	Number	0061		
WPRSNT	Number	0001		
WTVERR	Number	0000		
XBB_B0	Number	8000 A000		
XBB_BS	Number			
XBB_CN	Number	0004		
XBB_DISKF	Number	0000		
XBB_ERRC	Number	0000		
XBB LDN	Number	0001		
XBB SC	Number	0006		
XBB SN	Number	0002		
XCPRSNT	Number	0002		
XLT F	V BYTE	0002	CODE	External
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Warning Severe				
Errors Errors				
0 0				
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		efinitio	on J	Cref-1		
Symbol Cross Reference			- · · · ,			
ABRTC	29# 38#					
AST_OFF	29#					
ATRETRY	29#					
ADBD	29#					
BAT OFF	29#					
BID_AST	29#					
BID_BAT	29#					
BID_BOT	29# 29#					
BID_HOM	29#					
BID_OSN	29#					
BID_PAS	29# 29#					
BID UKN	29#					
ВІТО	29#					
BIT1	29#					
BIT15	29# 29#					
BIT3	29#					
BIT4	29#					
BITS	29# 29#					
BIT6	29# 29#					
BK_EC_OFF	29#					
BK_LBN_OFF	29#	*				
BK_MEC_OFF	29# 29#					
BUSY	29# 201 239#					
CBSY	29#					
CBSY2	29# 13 15	15	15	15		
CLRIL	29#					
CLRSPL	29#					
CLTPN	426# 115# 153	163				
CMDERR	209 231#	.03				
CMD1P	29#					
CMDLEN	113#					
CMDOK	166 168# 13 14#	14	481			
COMM	307 312	318#	70.			
COMM1	319 321	338#				
COMM2	326 329 123# 160	332	334#	323		
COUNT	29#	283	284	323		
CR	29#					
CRCER	29# 328					
CTRL_0	29# 29#					
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TA	' 294	*				
SKDF	294	*				
ATCH	294	*				
E_FTN						
E_LTN	294					
DE_OFF	296	#				
DE_DST	29	#			•	
DETPON	29	#				
DY	29					
0Y2	29:					
IVE1						
I ve3						
IVEM	29	#				
	10	# 196				
K_INT	200	304#				
K W K 1	204	195#				
K_WRTV	29	#				
RAT1	29	*				
RTF2						
F						
LSE						
rs	44	#				
TFTN		393	451#			
TFTNO	454					
TPEA	25	455	473#			
IR						
ISK	29	# 450	103			
SK1	66	# 104	, 05			
6K3	90	# 106				
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ard Disk driver ymbol Cross Refer	rence	(#isda	efinition)	Cref-3		
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ard Disk driver ymbol Cross Refer ADO2 D1 D1 D1 D2 D2 D3 D3 D3 D3 D3 D3 D3 D5 D6 NIT VEC_A VEC_B SITE RITE RITE RITE SIT SIT NP SIT DAT NDEX TEPP	29 29 29 29 29 29 29 29 29 29 29 29 29 2	(# is de # # # 199 # # # 199 # # # 103# # 311 # 331 103# # 125	208	Cref-3		
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ard Disk driver ymbol Cross Refer ADO2 AD11 AD21 AD22 AD33 AD32 ADM2 ADM2 ADM2 ADM5 ADM6 ADM7 ADM7 ADM7 ADM8 ADM7 ADM8 ADM8 ADM8 ADM9 ADM8 ADM8 ADM9 ADM8 ADM8 ADM8 ADM8 ADM8 ADM8 ADM8 ADM8	29	(# is de ## ## ## ## ## ## ## ## ## ## ## ## ##	208	Cref-3		
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IT DISK DIVER IMBO 1 Cross Refer ID 2 ID 1 ID 1 ID 2 ID 2 ID 2 ID 3 ID 3 ID 3 ID 3 ID 4 ID 5 ID 6 IT 6 IT 6 IT 7 IT 7 IT 8 IT 8 IT 8 IT 9	28 29 29 29 29 29 29 29 29 29 29 29 29 29	(# is de ## ## ## ## 199 ## ## 306 ## 316 ## 311 ## 125 ## ## 159 ## ## 156 ## ## ## ## ## ## ## ## ## ## ## ## ##	208	Cref-3		
ard Disk driver ymbol Cross Refer AD02 AD11 AD11 AD12 AD2 AD3 AD3 AD3 AD3 ADM2 AD5 ADM2 AD5 ADM2 AD5 ADM2 AD5 ADM1 AD5 AD6 AD7	28 29 29 29 29 29 29 29 29 29 29 29 29 29	(# is de ####################################	208	Cref-3		
Ind Disk driver mbol Cross Refer 1002 101 101 102 102 103 103 103 103 103 103 103 103 103 103	Pence	(# is defined as the state of t	208 280 453	Cref-3		
IT DISK DISK DISK DISK DISK DISK DISK DISK	Tence	## 199 ## 306 ## 311 ## 331 ## 125 ## 159 ## 155# ## 156# ## 358# ## 358#	208	Cref-3		

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	oss R e fe					(# is d	efiniti	on)	Cref	- 4								
S_BSN					29# 29#													
RE_READ .	: : : :	: : :	: :	: :	29# 20#	152	179	232	240	259	282	322						
BASE CMD	: : : :				29# 29#	29	29	29	29	29	29	29	29	29	29	29	29	29
CMD2 INTF					29# 29#													
READ					29# 29#													
DWRITE					29#	371#	397											
ESSEC Estor					39#													
ETURN					202	203	206	207	234#									
BUFR Canid					29# 29#													
CTEXT Dh					29# 29#													
ECPTRK Ecsize					29# 29#													
ECSZ ECTC					37# 29#													
ECTN Ectors					29# 42#													
ECTRK Ectsz					48# 29#													
EEK Eekc					29# 29#													
EEKC2 Err		: : :	: :		29# 29#	320												
ETPAR Etpari					305 404	310 408#	315	390#										
INIT SCTF					29# 29#													
SZ128 SZ1K					29# 29#													
SZ256 SZ512					29# 29#													
TART Tatus			: :	: :	124#	161 156	180	233	241	335								
TEPRO Tepri					29# 29#													
TEPR2 Tepr3					29# 29#													
TEPR4 Tepr5					29# 29#													
TEPR7					29# 29#													
TEPR8	: : : :				29# 29#													
TEPRA					29# 29#													
TEPRC Teprd					29# 29#													
Hard Disk Symbol Cr		ranca				(#isd	afini+i)	Cref	- E								
TEPRE					29#	.,, .,		,		•								
TEPR_OFF. T_AST_OFF					29# 29# 29#													
EMP SEC .					22#	411												
EMP_TRK . Rans					22# 122#	410 174	260											
RKO2 RKOE			: :		29# 29#													
RUE					29#	29												
					114#	158 337												
					29#													
NIT P_BAT			: :		29# 29#													
P_BAT Gate Ini_offse Ini_seg	r 				29# 29#													
P_BAT	r 				29# 29#													
P_BAT GATE INI_OFFSE INI_SEG PC_OFF PRCMP PRSNT	r																	
P_BAT GATE INI_OFFSE INI_SEG PC_OFF PRCMP PRSNT TVERR BB BO					29#													
P_BAT GATE. INI_OFFSE INI_OFF PC_OFF PRCMP PRSNT TVERR BB_B0 BB_CN					29# 29#													
P_BAT GATE. INI_OFFSE INI_SEG. PC_OFF. PRSNT TVERR BB_B0 BB_BS					29#													
P_BAT GATE GATE INI_SEG PC_OFF PRCMP PRSNT TYERR BB_BS BB_CS BB_CS BB_DSKF BBB_DSKF BBB_DSN BBB_DSN BBB_DSN					29# 29# 29#													
P_BAT GATE GATE INI _ SEG INI _ SEG INI _ SEG PC OFF PPRCMP PPRSNT TSER BB _ BS BB _ DS N					29# 29# 29# 29# 29#													
P_BAT GATE INI_SEG PC_OFF PRCMP PRSNT TYERR BB_BS BB_CN BB_DISKF BB_DISKF BB_DISKF					29# 29# 29# 29# 29# 29#	391	407											

```
The Microsoft MACRO Assembler Hard Disk I/O Control Functions
                                                        02-20-84
                                                                         PAGE
                                                                                  1 - 2
                                          Hard disk I/O Control functions
HIOCTL - Hard disk I/O Control functions
                                                     ,
            0000
                                                     HIDCTL:
;XB_DISK:
                                                                                               ; CP/M label
; get all information
; check logical or physical operation
            0000 E8 0079 R
0003 80 FC FF
0006 75 08
                                                               CALL
CMP
JNZ
                                                                         GET_REG
AH, OFFH
XBDSK1
                                                               physical disk operation
            0008 80 FF 40
000B 7C 0B
000D EB 0D 90
                                                               CMP
                                                                          BH,40H
XB_FLOPPY
W_DISK
                                                                                              ; check floppy or Winnie
; it's floppy
; it's Wini
                                                               JMP
                                                               logical disk operation
                                                                         AH, HDISK
XB_FLOPPY
W_DISK
            0010 80 FC 04
0013 7C 03
0015 EB 05 90
                                                     XBDSK1: CMP
                                                                                               ; check floppy or Winnie
                                                               J L
JMP
                                                                                              ; disk is Winnie
                                                               hard disk driver will not do the hard work for floppy
            0018
0018 B8 0005
001B C3
                                                     XB_FLOPPY:
MOV
Ret
                                                                          AX, DISKDF
                                                                                              ; set error flag to disk deffective
```

```
The Microsoft MACRO Assembler Hard Disk I/O Control Functions
                                                                           02-20-84
                                                                                                  PAGE
                                                        Hard disk I/O Control functions
W_DISK - Wini XBIOS functions
                                                                                    ENTRY:
                                                                                                 EXIT:
                001C
001C C6 06 0000 E 00
0021 80 FC FF
0024 74 26
                                                                      W_DISK:
                                                                                                  BYTE PTR XLT_F,0 ; assume physical operation AH,OFFH ; check physical or logical WDISK2 ; it's physical
                                                                                    MOV
                                                                                    CMP
JZ
                                                                                    logical disk operation
                                                                                                  AX
BX
SI,DX
SI,2
WDISK1
                 0025
0027
0028
002A
002D
002F
                          50
53
8B F2
83 FE O2
72 05
C6 06 0000 E 01
                                                                                                                             ; save function #
; save sector #
; save logical track #
; check if first 2 tracks?
                                                                                    PUSH
                                                                                    PUSH
MOV
CMP
                                                                                     JB
                                                                                    MOV
                                                                                                  BYTE PTR XLT_F,1
                                                                                                                                           ; set logical flag
                 002F
0034
0036
0036
003A
                                                                      WDISK1:
                          8A C4
E8 0000 E
5B
74 24
03 D6
F6 06 0000 E FF
74 03
E8 0065 R
                                                                                                                              ; get MS-DOS drive #
; get partition first track #
; restore sector #
; error if drive not exist
; add start track #
; IF WE'RE NOT SUPPOSED TO SKEW,
DONT
; XLT the first sector #
                                                                                    MOV
                                                                                    CALL
                                                                                                  BX
WDISK10
                                                                                     JZ
                003A
003C
003E
0043
0045
                                                                                                  DX,SI
XLT_F,OFFH
WDISK3
DO_XLT
                                                                                    ADD
TEST
                                                                                    JZ
                                                                       WDISK3:
                                                                                                                             ; check if bad sector
; convert logical track to physical
                                                                                    CALL
                                                                                                  DO_PASX
                0048 E8 0000 E
                                                                                     CALL
                 004B 58
                                                                                    POP
                                                                                                   AX
                                                                                    physical disk operation
                                                                       :
                004C
004C 80 E7 OF
004F 98
0050 D1 E0
0052 8B F0
                                                                       WDISK2:
                                                                                     AND
                                                                                                   BH,OFH
                                                                                                                              ; physical only drive 0 exist
                                                                                                                              ; word offset
```

```
The Microsoft MACRO Assembler 02-20-84 PAUE Hard Disk I/O Control Functions Hard disk I/O Control functions
                                                                                                              PAGE 1-4
                                                                                                              WORD PTR W_DISKF[SI] ; do
DI,TEMP_PTR ; get pointer
WORD PTR [DI.XBB_ERRC],AX
                 0054 FF 94 006D R
0058 C5 3E 0095 R
005C 89 45 OC
005F C3
                                                                                              CALL
                                                                                                                                                             ; do it
                                                                                               LDS
MOV
RET
                                                                                                                                                     et pointer back
116
117
118
119
120
121
123
124
125
127
127
128
131
131
133
133
133
133
134
141
141
143
                                                                                                                                                                         ; set error code
                 0060
0060 58
0061 B8 0005
0064 C3
                                                                               WDISK10:
                                                                                                                                              ; logical drive not exist
                                                                                              POP
MOV
Ret
                                                                                                              AX.DISKDF
                                                                                                                                            ; set error flag to disk deffective
                                                                                              DO_XLT - do translate for the first sector number
                                                                                                              BL = logical sector number **base 1
BL = sector number after skewing
BX
                                                                                               ENTRY:
                 0065
0065 32 FF
0067 4B
0068 8A 9F 0000 E
006C C3
                                                                               DO_XLT:
                                                                                                              BH,BH ; clear high byte
BX ; make it base O
BL,BYTE PTR RDXLT[BX] ; set logical sector #
                                                                                               X O R
D E C
                  006D
006F
0071
0073
0075
                             0000 E
0000 E
0000 E
0000 E
0000 E
                                                                               W_DISKF
                                                                                                              HREAD
HWRITE
HWRITEV
HFORMAT
HMCHK
HVDISK
                                                                                               DW
DW
DW
DW
```

```
1 - 5
                                                                     PAGE ; *********
--
                                                                                  GET_REG - get all block information into registers
                                                                                  ENTRY: ES:DI = block pointer
EXIT: AL = disk function
AH = MS-DOS logical disk drive #
= OFFH if physical operation
BL = sector #
BH = high 4 nibbles = physical drive #
low 4 nibbles = side or surface #
DX = track or cylinder #
CX = sector count
DI = buffer offset
ES = buffer segment
                                                                      ,
                0079
0079
007D
                                                                     GET_REG:
                       89 3E 0095 R
8C 06 0097 R
                                                                                                 WORD PTR [TEMP_PTR],DI
WORD PTR [TEMP_PTR+2],ES
                                                                                                                                                       ; save pointer
                                                                                   MOV
                                                                                                AX,ES:XBB_DISKF[DI] ; get logical disk # and disk function BX,ES:XBB_SN[DI] ; get drive/surface # and Sector # DX,ES:XBB_CN[DI] ; get track (cylinder) # CX,ES:XBB_SC[DI] ; get sector count DI,ES:DWORD PTR XBB_BD[DI] ; get buffer offset
               0081
0084
0088
008C
0090
                         26: 88 05
26: 88 5D 02
26: 88 55 04
26: 88 4D 06
26: C4 7D 08
                                                                                  MOV
                                                                                   MOV
MOV
MOV
                                                                                   LES
                0094
                                                                                   RET
                0095
                         00 00 00 00
                                                                     TEMP_PTR
                                                                                                 DD
                0099
                                                                     CODE
                                                                                ENDS
                                                                                  END
```

```
The Microsoft MACRO Assembler Hard Disk I/O Control Functions
  Segments and groups:
                                                                                  Name
                                                                                                                                                                                                          Size
                                                                                                                                                                                                                                                   align
                                                                                                                                                                                                                                                                                           combine class
 GROUP
0099
                                                                                                                                                                                                                                                   BYTE
                                                                                                                                                                                                                                                                                            PUBLIC 'CODE'
  Symbols:
N a m G

ABRTC

AST_OFF

ATRETRY

BADBD

BAT OFF

BID_BAT

BID_BAT

BID_BAT

BID_BAT

BID_BOT

BID_OSN

BID_OSN

BID_PAS

BID_VKN

BID_PAS

BITO

BITO
                                                                                  N a m e
                                                                                                                                                                                                           Туре
                                                                                                                                                                                                                                                   Value
                                                                                                                                                                                                                                                                                           Attr
                                                                                                                                                                                                           Number
Number
Number
                                                                                                                                                                                                                                                   0026
                                                                                                                                                                                                                                                   0000
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                    0080
                                                                                                                                                                                                                                                    0012
0005
0002
                                                                                                                                                                                                            Number
                                                                                                                                                                                                            Number
Number
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                   0008
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                   0003
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                   0001
                                                                                                                                                                                                                                                   0004
0006
0007
                                                                                                                                                                                                           Number
Number
                                                                                                                                                                                                             Number
Number
                                                                                                                                                                                                                                                   0000
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                   0001
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                   0002
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                   8000
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                    0008
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                   0010
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                   0020
                                                                                                                                                                                                             Number
Number
Number
                                                                                                                                                                                                                                                   0040
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                    0003
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                   0006
                                                                                                                                                                                                                                                   0021
0080
0001
                                                                                                                                                                                                              Number
                                                                                                                                                                                                             Number
Number
                                                                                                                                                                                                              Number
                                                                                                                                                                                                                                                   0004
                                                                                                                                                                                                             Number
                                                                                                                                                                                                                                                   0008
                                                                                                                                                                                                             L NEAR
Number
Number
                                                                                                                                                                                                                                                   0000
0002
0001
000D
                                                                                                                                                                                                                                                                                            CODE
                                                                                                                                                                                                                                                                                                                                     External
                                                                                                                                                                                                             Number
                                                                                                                                                                                                             Number
Number
                                                                                                                                                                                                                                                    0040
                                                                                                                                                                                                                                                    0011
0013
001A
                                                                                                                                                                                                             Number
Number
                                                                                                                                                                                                              Number
                                                                                                                                                                                                                                                    0065
                                                                                                                                                                                                                                                   0064
                                                                                                                                                                                                              Number
                                                                                                                                                                                                              Number
                                                                                                                                                                                                                                                     0001
                                                                                                                                                                                                            Number
                                                                                                                                                                                                                                                    0060
```

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PAGE

Symbols:1

licrosoft MACRO Assembler Fisk I/O Control Functions	02-20-	-84 PAGI			
		0005			
LAG		0065 CODE			
TN		000C 00F0			
.TN	Number (000E			
.U		0001 0020			
IST		000B 0002			
ON	Number (000A			
ART_OFF	Number (0017 0020			
		0040 0040			
	Number (0000			
	Number (0010			
6		0018 0003			
	Number (0018 0001			
I IT	L NEAR	0000 CDD	External		
	Number (0068 0069			
		0020 0020			
	Number (0001			
	Number 0	0061			
	L NEAR C	0050 0000 CODE	External		
l o	L NEAR C	0000 CDD	External		
:G	L NEAR C	0079 CODE			
	Number (0008 0004			
		0000 0000			
	Number (0001			
	Number (0002 0002			
		0004 0003			
	Number 0	0006			
	Number 0	000E			
·T		0000 CDDE			
A		0000 CDDE 0114	Global		
B	Number (0294 0000 CDDE	External		
	L NEAR C	0000 CODE	External		
	L NEAR C	0000 CODE	External		
licrosoft MACRO Assembler	02-20-	-84 PAGE	**************************************		
isk I/O Control Functions	L NEAR C	0000 CODE	Symbols-3		
isk I/O Control Functions V	L NEAR C L NEAR C Number C	0000 CODE	Symbols-3 External		
isk I/O Control Functions V	L NEAR C L NEAR C Number C Number C	0000 CODE 0000 CODE 00E0 00A0	Symbols-3 External External		
isk I/O Control Functions v	L NEAR C L NEAR C Number C Number C L NEAR C	0000 CODE 0000 CODE 0000 0000 0010	Symbols-3 External External		
v	L NEAR C Number C Number C Number C L NEAR C Number C Number C	0000 CDDE 0000 CDDE 0000 0010 0010 0000 CDDE	Symbols-3 External External		
v	L NEAR L NEAR Number C Number C L NEAR Number C Number C Number C Number C Number C	0000 CDDE 0000 CDDE 0010 0010 0000 CDDE 0004	Symbols-3 External External		
v	L NEAR C Number C Number C Number C L NEAR C Number C Number C Number C Number C	0000 CODE 0000 CODE 0000 CODE 00010 0000 CODE 00004	Symbols-3 External External		
v	L NEAR C Number Number Number C NEAR Number	0000 CDDE 0000 CDDE 0000 CDDE 0010 0000 0000 CDDE 0004 0004 0002	Symbols-3 External External		
v	L NEAR C Number Number Number L NEAR Number L NEAR Number Number C Number C Number	0000 CDDE 0000 CDDE 00000 0010 0000 CDDE 00004 00004 00002 00004 0002D	External External External		
OFF.	L NEAR L NEAR Number Number L NEAR Number L NEAR Number	0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 00004 0004 0004 0004 0004 0004	External External External		
OFF	L NEAR C Number Number Number C Number Number C	0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0004 0002 0004 0002 0004 0001	External External External		
OFF	L NEAR C Number Number Number C NEAR Number C Nu	0000 CDDE 0000 CDDE 0000 CDDE 00000 CDDE 00004 00002 0004 0001C 0000 CDDE	External External External		
OFF	L NEAR C Number Number Number C NEAR Number Number Number C Number Number C Number	0000 CODE 0000 CODE 0000 CODE 0000 CODE 00004 0002 0004 0001 0000 CODE 00004 00005 00004	External External External		
OFF	L NEAR C Number Number C Numbe	0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0004 0004 0002 0004 0006 0011	External External External		
OFF	L NEAR C Number Number C Numbe	0000 CDDE 0000 CODE 0000 CODE 0000 CODE 0000 CODE 00004 0002 0004 0001 C 0000 CDDE 0000 CDDE	External External External		
OFF	L NEAR C Number Number Number C Number Number C Number Number C Nu	0000 CDDE 0000 CDDE 0000 CDDE 00000 CDDE 00004 00004 00004 00001C 0000 CDDE 00005 00004 00005	External External External		
OFF	L NEAR L NEAR Number Number Number L NEAR Number	0000 CODE 0000 CODE 00000 CODE 00000 CODE 00004 00002 0004 00002 0004 00002 00004 00005 00004 00005 00004 00005 00006 00005	External External External		
OFF	L NEAR L NEAR Number Number Number L NEAR Number	0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0004 0002 0004 0001 CDDE 0000 CDDE 0000 CDDE	External External External		
OFF	L NEAR C Number Number C Numbe	0000 CDDE 0000 CODE 0000 CODE 0000 CODE 0000 CODE 00004 0002 0004 0001 CDDE 0000 CDDE 0000 CDDE	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CDDE 0000 CDDE 00000 CDDE 00004 00002 0004 0001C 0000 CDDE 00004 00005 00005 00005 00005 00006 00005 00006 00005 00006 00006 00006 00006	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CDDE 00000 CDDE 00000 CDDE 00004 00002 0004 00002 0004 00002 00004 00005 00006 00005 00006 00000 00000	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CODE	External External External		
OFF	L NEAR L NEAR Number Number Number L NEAR Number	0000 CDDE 0000 CODE 0000 CODE 0000 CODE 0000 CODE 0000 CODE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 00004 0002 0004 0001 CDDE 0000 CDDE 0000 CDDE 0000 CDDE 0000 CDDE	External External External		
OFF	L NEAR C Number	0000 CODE	External External External		
OFF	L NEAR C Number	00000 CDDE 00000 CDDE 00000 CDDE 00000 CDDE 00004 00004 00004 00005 00000 CDDE 00000 CDDE 00000 CDDE 00000 CDDE	External External External		
OFF	L NEAR C Number	00000 CDDE 00000 CDDE 00000 CDDE 00000 CDDE 00004 00004 00002 0004 0001C 0000 CDDE 00005 00005 00005 00006 00006 00006 00006 00000 CDDE	External External External		
OFF	L NEAR C Number	00000 CODE 00000 CODE 00000 CODE 00000 CODE 00004 00002 00004 00002 00004 00002 00004 00005 00005 00005 00005 00006 00006 00000 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006 00006	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CDDE 00000 CDDE	External External External		
OFF	L NEAR C Number	00000 CDDE 00000 CDDE 00000 CDDE 00000 CDDE 00004 00004 00002 00004 00002 00004 00005 00005 00005 00005 00006	External External External		
OFF	L NEAR C Number	00000 CDDE 00000 CDDE 00000 CDDE 00000 CDDE 00004 00004 00004 00004 00005 00005 00005 00005 00006	External External External		
OFF	L NEAR C Number	0000 CDDE 0000 CDDE 0000 CDDE 00000 CDDE 00000 CDDE 00004 00002 0004 0002 0004 0002 0006 0000 CDDE 0000 0006 0006 0006 0006 0006 0006 0	External External External		

Microsoft MACRO Assembler Disk I/O Control Functions	02-20-84	PAGE	Symbols-4	
	Number 0002			
13	Number 0003			
4	Number 0004			
5	Number 0005			
86	Number 0006			
87	Number 0007			
88	Number 0008			
89	Number 0009			
RA	Number 000A			
RB	Number 000B			
RC	Number 000E Number 000D			
RE	Number 000E			
RF	Number 000F			
CFF	Number 004C			
T OFF	Number 0028			
PTR	L DWDRD 0095	CODE		
ž	Number 0080			
E	Number 0002			
	Number - 000			
K1	L NEAR 0034	CODE		
K10	L NEAR OOBO	CODE		
K2	L NEAR OO4C	CODE		
K3	L NEAR 0048	CODE		
E	Number 0010 Number 0094			
SEG	Number 0096			
OFF	Number 004A			
MP	Number 0061			
NT	Number 0001			
RR	Number 000D			
SK	L NEAR OO1C	CODE		
SKF	L WORD OOSD	CODE		
80	Number 0008			
BS	Number 000A			
CN	Number 0004			
DISKF	Number 0000			
DSN	Number 0003 Number 0000			
ERRC	Number 0000 Number 0001			
SC	Number 0006			
SN	Number 0002			
K1	L NEAR OO10	CODE		
LOPPY	L NEAR OO18	CODE		
SNT	Number 0002			
7	V BYTE OOOO	CODE	External	
ing Severe				
rs Errors o				
O .				

mbol Cross Reference (# is definition) Cref-1
BRTC 26#
T_OFF
207
BD
AT_OFF
ID_BAT
ID_BOT
ID_DPD
ID_OSN
ID_PAS
ID_PBT
1170
IT1
PIT15
1173
1174
!IT5
1177
EK_EC_OFF
K_LBN_OFF
100T_OFF
:BSY
BSY
GROUP
LRIL
LKSPL
MDIP
ODE
PM8680
RCER
TRL_Q
TRL_S
YLDH
YLDL
AMNF
ATA
DATARQ
)ISKDF
PD_XLT 101
PDE_FTN
PDE_INIT
PDE_LTN
PDE_0FF

ard Disk I/O Control Functions ymbol Cross Reference	(# is def	inition)		Cref-	2								
DE DST	26# 26# 26#				J. 91 -	-								
D_OFF	26# 26#													
DY	26# 26# 26#													
IVE1	26# 26#													
VE3	26# 26# 26#													
SEL	26# 19#													
ATO	26# 26# 26#													
TF2	26# 26#													
OR	26#													
SE	26# 26#	26												
FTN	21# 21# 21#	95												
_REG	39	163#												
R	26# 26# 26#	5 1												
002	26# 26#													
012	26# 26# 26#													
3	26# 26# 26#													
DM2	26# 18#	141												
IT	18# 24 26#	37#												
EC_B	26# 18#	142												
ISK	17# 19# 17#	138 143 139												
ITEV	17#	140												
TV	26# 26# 26#													
rd Disk I/O Control Functions mbol Cross Reference		# is def	inition)	<i>31.1</i>	Cref-	3								
d Disk I/O Control Functions abol Cross Reference	19#	# is def	inition)		Cref-	3								
d Disk I/O Control Functions abol Cross Reference	(# is def	inition)		Cref-	3								
rd Disk I/O Control Functions mbol Cross Reference HDL	19# 26# 26#	# is def	inition)		Cref-	3								
d Disk I/O Control Functions abol Cross Reference IDL	1 9 # 2 6 # 2 6 # 2 6 # 2 6 # 2 6 # 2 6 #	# is def	inition)		Cref-	3								
d Disk I/O Control Functions abol Cross Reference IDL	19# 26# 26# 26# 26# 26# 26# 26#	# is def	inition)		Cref-	3								
d Disk I/O Control Functions abol Cross Reference BL BEX PP F. S. D. OFF. ST OFF. BEAD OFF.	19# 26# 26# 26# 26# 26# 26# 26# 26#	# is def	inition)		Cref-	3								
DL	19# 26# 26# 26# 26# 26# 26# 26# 26# 26# 26	# is def	inition)		Cref-	3								
d Disk I/D Control Functions bol Cross Reference DL EX PP F. S. D OFF. ST OFF EAD OFF. ITION FTN LTN PAS OFF. ASN	19# 26# 26# 26# 26# 26# 26# 26# 26# 26# 25# 25#	# is def	inition)		Cref-	3								
d Disk I/O Control Functions bol Cross Reference DL	19# ### 26## # 26### 266## # 226#### 226#### 226#### 226#### 226#### 226##### 226########	# is def	inition)		Cref-	3								
d Disk I/O Control Functions abol Cross Reference DL EX PP F. S. D_OFF SST_OFF EAD OFF ITION FTN LTN PAS OFF ASN ATN BSN BTN BSN BS	19# 26## 26## 26## 26## 26## 26## 26## 26	# is def		26	Cref-		26	25	26	25	25	26	28	25
d Disk I/O Control Functions abol Cross Reference DL EX PP F. S. D_OFF. ST_OFF. EAD. OFF. ITIONFTNLTNPASOFF. ASN. ASN. BSN. BSN. BSN. BSN. BSN. BSN. BSN. READ. SE	19# 26## 26## 26## 26## 26## 226### 226## 226## 226## 226## 226## 226## 226## 226## 226## 226#### 226### 226### 226#### 226#### 226#### 226########					26	26	26	26	26	26	26	26	26
d Disk I/O Control Functions abol Cross Reference DL EX PPP F. S. ST_OFF LEAD OFF LITION _FIN _LTN _PAS _OFF ASN ATN BSN BSN BSN BSN BSN BSN BSN B	19# 26## 26## 26## 26## 26## 26## 26## 26						26	26	26	26	26	26	26	28
d Disk I/O Control Functions abol Cross Reference DL EX PP F. S. D_OFF SST_OFF IEAD OFF ITION	1 9 # ### 2 6 6 ## # ### ### ### 2 6 6 ## ### #						26	26	26	26	26	26	26	28
d Disk I/O Control Functions abol Cross Reference IDL BEX PPP F. S. D. OFF. ST. OFF LEAD OFF. IIION FTN LTN PAS ASN ASN BSN BSN BSN BREAD MSE IDL MSE MSE MSE MSE MSE MSE MSE MS	1 9 # ### ### #########################	26					26	26	26	26	26	26	25	28
rd Disk I/O Control Functions mbol Cross Reference HDL DEX DEX FEPP IF. OS. LD OFF. SST OFF. HEAD OFF. TITION T_FTN T_LTN T_LTN T_LTN T_AS E_OFF. ASN BSN BSN BSN BSN BTN READ ASE MD MD MD MD MD MD MD MT MT MT	1 9 # ### ### #########################	26					26	26	26	26	26	26	26	26
d Disk I/O Control Functions abol Cross Reference DL EX EPP F. S. D_OFF ST_OFF HEAD OFF LITION FIN LTN LTN BAS OFF ASN ASN ASN ASN BSN BSN BSN BSN	1 9 6 6 6 7 7 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7	26					26	26	26	26	26	26	26	26
rd Disk I/O Control Functions mbol Cross Reference HDL DEX EPP IF. OS. LD_OFF. AST_OFF HEAD. OFF. IIIION. I_FIN I_LIN I_FIN I_LIN I_PAS EOFF ASN ASN ASN BSN BSN BSN BSN BSN	1 9 666 # ### ##########################	26					26	26	26	26	26	26	26	26
rd Disk I/O Control Functions mbol Cross Reference HDL DEX EPP HDS LD_OFF SST_OFF HEAD OFF HEAD OFF TITION FTN TTN TTN TAS EOFF ASN ASN ASN BSN BSN BSN BSN BSN BSN BSN BSN BSN B	1 9 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	26	26				26	26	26	26	26	26	26	26
rd Disk I/O Control Functions mbol Cross Reference HDL DEX EPP DEX EPP TF. OS LD_OFF. AST_OFF. HEAD T_TTN T_TTN T_TAN T_TAN T_AS BON BSN BSN	1 9 666 ## #############################	26					26	26	26	26	26	26	26	26
rd Disk I/O Control Functions mbol Cross Reference HDL DEX EPP TOS TOS LOTF HEAD OFF TITION T FTN T LTN T LTN T LTN T PAS E OFF ASN BSN BSN BSN BSN BTN TREAD ASE MD MD ASE MD ASE MD ASE MD TAT TOR FR NID EXT TOR FR NID EXT TOR TOR TOR TOR TOR TOR TOR T	1 2 5 5 6 6 6 7 7 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	26	26				26	26	26	26	2 5	26	26	26
rd Disk I/O Control Functions mbol Cross Reference HDL DEX EPP TF. OS. LD_OFF. AST_OFF. HEAD	1 2 2 5 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	26	26				26	26	26	26	26	26	25	26

```
Hard Disk I/O Control Functions
         Symbol Cross Reference
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      Cref-4
                                                                                                                                                                                                                                                                                                                                                         (# is definition)
STEPR2
STEPR3
STEPR4
STEPR5
STEPR6
STEPR7
STEPR8
STEPR8
STEPR8
STEPRA
STEPRB
STEPRC
STEPRC
STEPRC
STEPRC
STEPRC
STEPRF
ST
   STEPR2
STEPR3
STEPR4
                                                                                                                                                                                                                                                                                                                      116
26#
26#
26#
                                                                                                                                                                                                                                                                                                                                                                            164
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    174#
                                                                                                                                                                                                                                                                                                                                                                                                                                        165
                                                                                                                                                                                                                                                                                                                                                                                    26
                                                           WDISK1
WDISK10
WDISK2
WDISK2
WDISK3
WGATE.
WINI_OFFSET
WHNI_SEG
WPC_OFF
WPRCMP
WPRSNT
WTYERR
WDISK
                                                                                                                                                                                                                                                                                                                                                                              93#
120#
110#
                                                                                                                                                                                                                                                                                                                   91
97
83
100
26#
26#
26#
26#
47
115
                                                                                                                                                                                                                                                                                                                                                                               102#
      W_DISK .
W_DISKF.
                                                                                                                                                                                                                                                                                                                                                                            53
138#
 26#
26#
26#
26#
26#
26#
26#
                                                                                                                                                                                                                                                                                                                                                                            171
                                                                                                                                                                                                                                                                                                                                                                            117
                                                                                                                                                                                                                                                                                                                                                                              170
168
51#
52
                                                                                                                                                                                                                                                                                                                                                                                                                                                   57#
                                                                                                                                                                                                                                                                                                                          26#
                                                                                                                                                                                                                                                                                                                                                                                    8 1
                                                                                                                                                                                                                                                                                                                                                                                                                                                  92
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               99
```

```
The Microsoft MACRD Assembler Hard Disk Driver
                                                                                                02-20-84
                                                                                                                              PAGE
                                                                                                            PAGE
TITLE
NAME
                                                                                                                              60,132
Hard Disk Driver
HDD
1234567891111111111222222222333333333333
                                                                                          :
                                                                                                            08/30/83
                                                                                          CGROUP
CODE
ASSUME
                                                                                                           GROUP CODE
SEGMENT BYTE PUBLIC 'CODE'
CS:CGROUP, DS:CGROUP, ES:CGROUP, SS:CGROUP
                    0000
                                                                                                            EXTRN
                                                                                                                             RDXLT: BYTE
                                                                                                                             HREAD ; - read Sector(s)
HWRITE ; - write Sector(s)
HWRITE ; - write Sector(s) with verify
HFORMAT ; - format track(s)
HINIT ; - init hard disk drive
HMCHK ; - media check
HVDISK ; - verify disk
INTHDL ; - hard disk interrupt handler
KLT_F, CLTPN, MAXTRK,STEPRATE, PRECOMP
                                                                                                            PUBLIC
PUBLIC
PUBLIC
PUBLIC
                                                                                                            PUBLIC
                                                                                                            PUBLIC
PUBLIC
PUBLIC
PUBLIC
                                                                                                            LIST
                    0000
0002
0003
                                                                                          MAXTRK
Steprate
Precomp
                                                                                                                              DW
DB
DW
                                                                                                                                                152
STEPR6
128/4
                                                                                                                                                                                    ; max track #
; step rate value
; write pre-comp value
                                 0098
                                  06
0020
                    0005
0006
                                  00
                                                                                          RETNS DB
                                                                                                                                                                  ; # of sectors do retry
; retry count
                                                                                                                               0
                    0007
                                 0000
                                                                                          ERRCODE DW
                                                                                                                                                                  ; error code save area
; write with verify flag
                                                                                          WRTWVF
                                                                                          XLT_F
                                                                                                                              ٥
                                                                                                                                                                  ; translate table flag
                    000A 00
                                                                                                            DB
                                                                                                   ENDIF
```

```
02-20-84
                                                                                                                                        PAGE
   The Microsoft MACRO Assembler
                                                                                                                                                             1 - 2
 Hard Disk Driver
PAGE
Hard Disk routines are:
                                                                                                                     1. HREAD - read sector(s)
2. HWRITE - write sector(s)
3. HWRITEV - write sector(s) with verify
4. HFORMAT - format track(s)
5. HINIT - init hard disk drive
6. HMCHK - media check
7. HVDISK - verify disk
8. INTHDL - hard disk interrupt handler
                                                                                                                      Other support routines are:
                                                                                                                     1. RETRY - error recovery after a read
2. HOMET - restore head
3. MOVEMX - move head to max. track
4. MOVEH - move head
5. LDTSKF - load hard disk task file
6. DUTCMD - output command and wait for command completion
7. ADVTNS - advance to next logical sector *** this is OS dependent
8. ADVTNT - advance to next logical track
9. DELAY - delay 0.5 ms
                                                                                                                      SECPTRK = 16
SECSIZE = 512
NUMHEAD = 4
(RDXLT) =
                                                                                                                                                                               ; sector per track
; sector size
; number of surfaces per cylinder
; CP/M skew table with skew factor 7
this is the only item here that OS
dependent
                                                                                                                      Values has to be loaded for different drive:
                                                                                                                     RD51
MAXTRK = 305
STEPRATE = 0
PRECOMP = 200/4 = 50
                                                                                                                                                                                 RD50
                                                                                                                                                                                152 ; max. cylinder number
6 ; step rate value
128/4 ; write precomp value
```

```
The Microsoft MACRO Assembler
Hard Disk Driver
                                                                                                            02-20-84
                                                                                                                                           PAGE
                                                                                                                                                              1 - 3
HREAD - HARD DISK READ SECTOR
                                                                                                                          ENTRY:
                                                                                                                                              BH = surface/drive #
BIT 0-3 surface #
BIT 4-7 drive #
BL = sector #
CX = sector #
DX = cylinder #
ES:DI = buffer start address
                                                                                                                                             ES:DI : buffer start address

Z flag is set if error

AX = ERROR STATUS

AL = O, NO ERROR

= 1, ERROR

= 3, DRIVE DEFFECTIVE

= 5, DISK DEFFECTIVE

= 0DH, WRITE VERIFY ERROR

AH, BIT 0 = DAM NOT FOUND

BIT 1 = TRACK 0 ERROR

BIT 2 = ABORTED COMMAND

BIT 4 = ID NOT FOUND

BIT 4 = ID NOT FOUND

BIT 6 = CRC ERROR DATA FIELD

BIT 7 = BAD BLOCK DETECT

(RETNS) = NUMBER OF SECTORS PERFORMED THE RETRY
                                                                                                                          EXIT:
                        000B
                                                                                                      HREAD:
                       000B FC
000C C6 06 0005 R 00
                                                                                                                                                                                                           ; set forward flag
; no any sector do retry yet
                                                                                                                          C L D
MOV
                                                                                                                                               BYTE PTR RETNS, O
                                                                                                                ENDIF
                                                                                                                          read sector(s) loop
                       0011
0014
0016
0018
0018
001B
                                                                                                      HREAD5:
                                     E8 0105 R
75 37
B0 20
E8 014F R
                                                                                                                          CALL
                                                                                                                                                                                                           ; load task file
; error if pass limit
; output read command
                                                                                                                                               LDTSKF
                                                                                                                                               HREAD2
AL,RDREAD+ATRETRY
OUTCMD
                                                                                                                           JNZ
                                                                                                      HREAD10:
                                                                                                                           CALL
                                      OA CO
74 O7
E8 O1FO R
OA CO
75 27
                                                                                                                                                                                                           ; error?
; Skip if no error
; error recovery
; still have error?
; abort at this point
                                                                                                                                               AL,AL
HREAD3
                                                                                                                           JΖ
 128
129
130
131
132
133
                        001F
0022
0024
0026
                                                                                                                                               RETRY
AL, AL
HREAD2
                                                                                                                           CALL
                                                                                                                          OR
JNZ
                                                                                                      HREAD3:
                                                                                                                          PUSH
                                      5 2
5 1
                                                                                                                                               DX
                                                                                                                                                                                                           ; save cylinder #
; save sector count
                        0026
                                                                                                                                               CX
```

```
The Microsoft MACRO Assembler Hard Disk Driver
                                                                                              02-20-84
                                                                                                                           PAGE
                                                                                                                                        1 - 4
                                                                                                                                                                               ; get data port addr
; get sector size in words
; es:di has buffer addr
; (8) get data byte
; (2) save to high byte
; (8) get next byte
; (4) make into correct order
; (15) save into buffer
; (17) loop for whole sector
                    0028
                                BA 0080
B9 0100
                                                                                                                           DX.OFFSET DATA
CX.SECSIZE/2
134
135
137
138
140
142
143
144
145
147
148
149
151
152
153
154
155
156
157
158
159
160
162
163
                                                                                                         MD V
MD V
                    002B
002E
002E
                                                                                       HREAD1:
                              EC
                                                                                                          IN
                                                                                                                           AL . DX
                   002E
002F
0031
0032
0034
0035
                                BA EO
EC
86 C4
                                                                                                                           AH,AL
AL,DX
AL,AH
                                                                                                          MIT
                                                                                                         IN
XCHG
STOSW
                                AB
E2 F7
                                                                                                                           HREAD 1
                                                                                                          LOGP
                   0037
                              5 9
5 A
                                                                                                                                                                             ; restore sector count
; restore cylinder #
                    0039
                              BO 01
E6 68
                                                                                                         MOV
                                                                                                                           AL . SBUFR
                                                                                                                                                                              ; strobe buffer ready
                    003B
                                                                                                         OUT
                                                                                                                           RDCMD2,AL
                   003D
                                E8 018F R
E2 CF
                                                                                                         CALL
                                                                                                                           ADVTNS
HREADS
                                                                                                                                                                               ; advance to next sector ; loop for multi_sector
                                                                                                ENDIF
                   0042
0044
0049
004B
004D
                                33 CO
F6 O6 OOO5 R FF
74 O2
BO OF
                                                                                                        XOR
TEST
JZ
MOV
                                                                                                                           AX,AX
BYTE PTR RETNS,OFFH
HREAD2
AL,SERR
                                                                                                                                                                               ; assume no error ; retry before?
                                                                                                                                                                               ; set soft error
                                                                                       MOV
HREAD2:
IF NOT
JMP
ENDIF
                    004D
                              EB 62 90
                                                                                                                                                                              ; this is the way to handle LED
; if no pre_read
                    0050 C3
                                                                                                         RET
```

```
The Microsoft MACRO Assembler
Hard Disk Driver
                                                                             02-20-84
                                                                                                     PAGE
                                                                         HWRITE - hard disk write sector (without verify) HWRITEV - write sector with verify
                                                                                       ENTRY:
                                                                                                     BH = surface/drive number
BIT 0-3 surface #
BIT 0-3 for every
BL = sector #
CX = sector count
DX = cylinder #
ES:DI = buffer start address
                                                                                                     Z flag is set if error

AX = ERROR STATUS

AL = O, NO ERROR

= 1, ERROR
= 3, DRIVE DEFFECTIVE
= 5, DISK DEFFECTIVE
= 00H, WRITE VERIFY ERROR
= 0FH, SOFT ERROR

AH, BIT 0 = DAM NOT FOUND
BIT 1 = TRACK O ERROR

BIT 2 = ABORTED COMMAND
BIT 4 = ID NOT FOUND
BIT 6 = CRC ERROR DATA FIELD
BIT 7 = BAD BLOCK DETECT
                                                                                      EXIT:
                0051
0051
0056
                                                                        HWRITEV:
                           C6 06 0009 R 01
                                                                                                     BYTE PTR WRTWVF, 1
Short Hwrite1
                                                                                                                                                ; SET WRITE WITH VERIFY FLAG
                                                                                      MOV
                                                                        HWRITE: MOV
                0058
0058
005D
005D
                          C6 06 0009 R 00
                                                                                                     BYTE PTR WRTWYF, O
                                                                                                                                                : CLEAR WRITE WITH VERIFY FLAG
                                                                        HWRITE1:
                           FC
                                                                                      CLD
                                                                                                                                                ; SET FORWARD FLAG
                005E 88 F7
                                                                                   MOV
                                                                                                                                                ; SI = BUFFER ADDR
                                                                                                     SI,DI
                                                                                      write sector loop
                HWRITE5:
CALL
JNZ
MDV
CALL
OR
JNZ
                                                                                                     LDTSKF
HWRITE2
AL,RDWRITE+ATRETRY
OUTCMD
                                                                                                                                              ; LOAD TASK FILE
; error if pass limit
; OUTPUT WRITE CMD
                                                                                                     AL,AL
HWRITE2
                                                                                                                                                ; ERROR?
; ABORT IF ERROR
```

```
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                                                                                                                        PAGE
                                                                                                                                       1 - 6
   The Microsoft MACRO Assembler
Hard Disk Driver
                                                                                                                                                                            ; SAVE cylinder #
; SAVE SECTOR COUNT
; GET DATA PORT ADDR
; GET SECTOR SIZE in words
; SAVE DS
; DS=ES
                             52
51
BA 0060
B9 0100
1E
06
1F
220
221
222
                                                                                                        PUSH
PUSH
MOV
MOV
                                                                                                                         D X
C X
                    006F
0070
                                                                                                                         DX, OFFSET DATA
223
224
225
226
227
                    0073
0076
0077
0078
                                                                                                                         CX,SECSIZE/2
DS
ES
DS
                                                                                                        PUSH
PUSH
POP
                                                                                                                                                                             ; DS:SI HAS BUFFER ADDR
; (16) GET DATA FROM BUFFER
; (8) OUTPUT DATA TO RD
; (2) GET HIGH BYTE
; (8)
; (17) LOOP FOR WHOLE SECTOR
                                                                                       HWRITE21
                    0079
                                AD
EE
8A C4
EE
E2 F9
                    0079
007A
007B
007D
innsw
                                                                                                                         DX,AL
AL,AH
DX,AL
HWRITE21
                                                                                                        DUT
                    007E
                                                                                                        LOOP
                                                                                                                         DS
                   0080 1F
0081 59
0082 5A
                                                                                                                                                                             ; GET DS BACK
; RESTORE SECTOR COUNT
; RESTORE cylinder #
                                                                                                        POP
                                                                                                        POP
                                                                                                                         C X
                   0083
0089
008B
008D
0090
                                2E: C6 06 01E7 R 00
B0 01
E6 58
E8 0157 R
OA C0
75 1D
                                                                                                                         BYTE PTR CS: IRQDRQ, O
AL, SBUFR
RDCMD2, AL
DUTCMD1
                                                                                                                                                                             ; CLEAR INT FLAG
; STROBE BUFFER READY
                                                                                                        MOV
                                                                                                        MOV
OUT
CALL
OR
                                                                                                                                                                             ; WAIT UNTIL RD GETS IT
; ERROR?
; ABORT IF ERROR
                                                                                                                          AL,AL
HWRITE2
                                                                                                        JNZ
                    0092
                                                                                                        check for write with verify
                    0094 FS 06 0009 R FF
0099 74 0F
                                                                                                        TEST
J2
                                                                                                                         BYTE PTR WRTWVF,OFFH HWRITE11
                                                                                                                                                                             ; NO VERIFY
                                                                                                                                                                             ; DUTPUT READ COMMAND
; TASK FILE SHOULD CONTAIN THE SAME
; cylinder/surface/SECTOR INFORMATION
; ERROR?
                                                                                                                         AL, RDREAD+ATRETRY OUTCMD
                    009B B0 20
009D E8 014F R
                                                                                                        MOV
                                                                                                         CALL
                                                                                                                         AL,AL
AL,WTVERR
HWRITE2
                                 OA CO
                     OOAO
                                                                                                                                                                             ; ERROR?
; ASSUME WRITE VERIFY ERROR
; SKIP IF NO ERROR
                                                                                                        MDV
                     OOA2 BO OD
OOA4 75 OB
                                                                                                         JNZ
                    00A6 B0 01
00A8 E6 68
00AA
                                                                                       MOV
OUT
HWRITE11:
CALL
                                                                                                                          AL,SBUFR
RDCMD2,AL
                                                                                                                                                                             ; STROBE BUFFER READY
                    OOAA E8 018F R
OOAD E2 B1
                                                                                                                          ADVTNS
HWRITE5
                                                                                                                                                                             ; ADVANCE TO NEXT SECTOR ; LOOP FOR MULTI_SECTOR
                                                                                                        LOOP
262
263
264
265
266
267
268
269
270
271
272
273
274
                                                                                       XOR
HWRITE2:
; CALL
; RET
                    00AF 33 C0
00B1
                                                                                                                         AX,AX
                                                                                                                                                                             ; SET NO ERROR STATUS
                                                                                                                          LED_OFF
                                                                                                                                                                             ; wini blink
                                                                                      LED_OFF:
PUSH
MOVY
OUT
                  OOB1 50 00B2 BO 38 00B4 E6 66 00B6 58 00B7 C3
                                                                                                                                                                             ; save
; de-select drive
                                                                                                                          AL, SSZ512+DRIVE3
SDH, AL
 The Microsoft MACRO Assembler
Hard Disk Driver
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                                                                                                                    PAGE
                                                                                                                                      1 - 7
275627278228072288422886228842288622889122830012303304563300733043300756
                                                                                                         HEORMAT - HARD DISK TRACK FORMAT ROUTINE
                                                                                                         ENTRY:
                                                                                                                          BH = DRIVE/surface NUMBER
DX = cylinder #
CX = TRACK COUNT
ES:DI = FORMAT BUFFER
                                                                                                        This format routine is used to format (CX) tracks. ES:DI holds additional parameter information. Each sector requires a 2 bytes sequence. The first byte designates whether a bad bloock mark is to be recorded in the sector's ID field. A '00'H is normal; a '80'H indicates a bad block mark for that sector. The second byte indicates the logical sector number to be recorded.

ES:DI expecting total number of bytes:
                                                                                                         2 * 16 * (number of track to be formatted)
                                                                                                                        Z flag is set if error
                                                                                       HFORMAT:
                     OOBS FC
                                                                                                                                                                              ; SET FORWARD FLAG
                                                                                                ENDIF
 308
309
310
311
312
313
                     OOB9 88 F7
                                                                                                       MOV SI,DI
                                                                                                                                                                              ; GET BUFFER ADDR
                     HEDRMATS:
                                                                                                        MOV
CALL
JNZ
MOV
DUT
                                                                                                                          BL,40
LDTSKF
HFORMAT2
AL,SECPTRK
SECTC,AL
                                                                                                                                                                             ; # of bytes for gaps
; LOAD TASK FILE
; error if pass limit
; SET # OF SECTORS PER TRACK
 314
315
316
317
318
319
320
321
322
323
                                                                                                                          AL, FMTTRK
OUTCMD
                     00C8
00C8
00CB
                                 BO 50
E8 014F R
OA CO
75 2D
                                                                                                         MOV
                                                                                                                                                                             ; OUTPUT WRITE CMD
                                                                                                         CALL
DR
JNZ
                                                                                                                                                                              ; ERROR?
; ABORT IF ERROR
                                                                                                                           AL,AL
HFORMAT2
                                                                                                                          DX
CX
DX,OFFSET DATA
CX,SECPTRK
DS
 324
325
326
327
328
                     OOCF 52
OODO 51
OOD1 BA 0080
OOD4 89 0010
OOD7 1E
                                                                                                                                                                             ; SAVE cylinder #
; SAVE SECTOR COUNT
; GET PORT ADDR
; GET # OF SECTORS PER TRACK
; SAVE DS
                                                                                                         PUSH
                                                                                                          PUSH
MOV
MOV
                                                                                                          PUSH
                     OODS
                                                                                                          PUSH
```

	Microsoft Disk Driv		RO Assembler	02-20	-84	PAGE 1-8	
330	0009	1 F			POP	os	
331	OODA	• •		HFORMAT 1			: DS:SI HAS BUFFER ADDR
332	OODA	AD		•	LODSW		; (16) GET DATA FROM BUFFER
333	OODB	EE			DUT	DX,AL	: (8) OUTPUT BAD BLOCK MARK
334	OODE	8A (-4		MOV	AL, AH	: (2) GET DATA FROM BUFFER
335	OODE	EE .			DUT	DX AL	: (8) OUTPUT LOGICAL SECTOR #
336	OODE	E2 1	F 0		LOOP	HEORMAT 1	: (17) LOOP FOR ALL SECTOR
337	0001	E 2 1	•		LUUF	HEURMAII	, (17) COOP FOR ALL SECTOR
338	00E1	1 F			POP	DS	; GET DS BACK
339	00E1	5.9			POP	cx	RESTORE SECTOR COUNT
340	00E3	5 A			POP	DX	: RESTORE cylinder #
341	0053	5 M			- 0 -	U.A.	, RESTORE CYTTHOST #
342	00E4	25.	C6 06 01E7 R 00		MOV	BYTE PTR CS: IRODR	RO.O : CLEAR INT FLAG
343	OOEA	BO (MOV	AL .SBUFR	: STROBE BUFFER READY
344	OOEC	E6 (OUT	RDCMD2.AL	, SIROBE BOFFER READ!
345	OOEE		0157 R		CALL	OUTCMDI	: WAIT UNTIL RD GETS IT
346	00F1	OA I			OR	AL, AL	: ERROR?
347	00F3	75			JNZ	HFORMAT2	; ABORT IF ERROR
348	0013	,,,	• •		0 11 2	HI OKMAI Z	, ADOR! II ERROR
349	00F5		DIBD R		CALL	ADVINT	: ADVANCE TO NEXT TRACK
350	00F8	E2 1			LOOP	HFORMAT5	; LOOP FOR MULTI_TRACK FORMAT
351	0078	-2	• •		2007	III OKMAI 3	, LOUI TON MOLIT_IRACK FORMAT
352	OOFA	33 (XOR	AX,AX	: SET NO ERROR STATUS
352	OOFC	33		HFORMAT2		ma, ma	, SE! NO ERROR STATUS
353	OOFC	EB I		HE UKMA 12	JMP	LED_OFF	; blink and RET

	Microsoft Disk Driv		CRO Assembler	02-20	0-84	PAGE 1-9	
355 356				******	PAGE	*******	*********
357 358					нмснк	- MEDIA CHECK	
359 360 361				;	EXIT:	AL = 0, NO ERRO AH = 10, RD51 I	
362 363 364				;*****	******	*******	************
365 366 367 368	00FE 00FE 0101	8 8 C 3	0800	HMCHK:	MOV Ret	АХ,ОАООН	; return with RD51 ID
369 370 371 372				;*****		**************************************	***************************************
373 374 375 376					EXIT:		CTIVE ERROR VALUE
377 378 379	0102 0102		05	HVDISK:	MOV	AL, DISKDF	: RETURN WITH DISK
380	0104	C 3	Va		RET	AL, DISKUT	; DEFFECTIVE ERROR

```
The Microsoft MACRO Assembler Hard Disk Driver
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                                                                                                                PAGE
LDTSKF - load 'TASK FILE', controller registers 2 - 6
                                                                                                ENTRY:
                                                                                                                BH = drive/surface #
BL = sector #
DX = cylinder #
(MAXTRK) = max track #
AX = 0 and 2 flag is set if no error
AX = 1001H (1D not found error) and Z flag is cleared
if track # > (MAXTRK)
                  0105
0105
0108
010C
010E
0110
0112
                                                                                 LDTSKF:
                            B8 1001
3B 16 0000 R
77 18
8A C2
E6 84
8A C6
E6 65
                                                                                                                AX,1001H
DX,MAXTRK
LDTSKF1
AL,DL
CYLDL,AL
                                                                                                                                                                ; load error code
; check pass limit
; error if >
                                                                                                CMP
JA
MOV
                                                                                                DUT
MOV
DUT
                                                                                                                 AL,DH
CYLDH,AL
                                                                                                                                                                ; high cylinder
                                                                                                                 AL,SSZ512
AL,BH
SDH,AL
                  0116 BO 20
0118 OA C7
011A E6 66
                                                                                                 MOV
                                                                                                                                                                 ; get sector size
; or with surface #, assume drive0
                                                                                                 DR
DUT
                  011C 8A C3
011E E6 63
                                                                                                MOV
OUT
                                                                                                                 AL,BL
SECTN,AL
                                                                                                                                                                 : get sector #
                  0120 B0 01
0122 E6 62
0124 33 C0
0126
0126 23 C0
0128 C3
                                                                                                                 AL,1
SECTC,AL
AX,AX
                                                                                                 MOV
                                                                                                                                                                 ; do 1 sector
                                                                                                 DUT
                                                                                LDTSKF1:
                                                                                                AND
                                                                                                                 AX,AX
                                                                                                                                                                 ; set 2 flag
```

```
The Microsoft MACRO Assembler
Hard Disk Driver
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                                                                         PAGE
                                                     HINIT - hard disk init routine
                                                               ENTRY: Interrupt vector must be set EXIT: AX = error status
                                                     HINIT:
                                                          CLD
STI
Endif
                                                                                                          ; set forward direction ;TRY IT WITH INTERRUPTS ON
                                                               MOV
OUT
Call
                                                                          AL,SINIT
RDCMD2,AL
DELAY
           012B
012D
012F
                    BO 02
E6 68
E8 01E8 R
                                                                                                          ; set software init
                                                                                                          ; allow 0.5 ms
           0132 A1 0003 R
0135 E6 61
                                                               MOV
                                                                          AX,PRECOMP
WPRCMP,AL
                                                                                                          ; init write pre-comp value
           0137 OC 20
0139 E6 86
                                                                          AL,DRIVEO+SSZ512+HEADO ; set size/drive/surface register SDH,AL
                                                               OR
OUT
            013B
013E
0141
                   BB 0001
BA 0004
E8 0243 R
                                                                          BX,1
DX,4
MOVEO4
                                                               MOV
                                                               MOV
CALL
                                                                                                         ; move head to track 0
                                                                          AL,STEPRATE
AL,RESTOR
OUTCMD
LED_OFF
            0144
0147
0149
0140
                    AO 0002 R
04 10
E8 014F R
E9 00B1 R
                                                                                                         ; set step-rate and
; restore
; output the restore cmd
; turn off LED and RET
                                                               MOV
                                                               ADD
CALL
JMP
```

```
The Microsoft MACRO Assembler Hard Disk Driver
                                                                                  02-20-84
                                                                                                            PAGE
                                                                                                                            1-12
                                                                             44544445567890123456789077777777777777777789
                                                                                             OUTCMD - OUTPUT COMMAND TO WINNIE CONTROLLER
                                                                                            ENTRY: AL = COMMAND
EXIT: AX = ERROR STATUS
USED: AX
                 014F
C14F
0155
0157
0157
                                                                             OUTCMD:
                           2E: C6 06 01E7 R 00
E8 67
                                                                                            MOV
                                                                                                            BYTE PTR CS: IRQDRQ, O RDCMD, AL
                                                                                                                                                           ; CLEAR INT FLAG
; OUTPUT COMMAND
                                                                                                                                                         ; SAVE REGISTERS
                             53
51
                                                                                                            B X
C X
                                                                                             PUSH
                                                                             MOV
OUTCMD2:
MOV
OUTCMD11:
TEST
                 0159
0158
015E
015E
015E
0166
0168
016C
016C
                             B3 20
                                                                                                            BL,4*8
                                                                                                                                                           ; ABOUT & SECONDS TIME OUT
                           B9 807F
                                                                                                            CX,32895
                                                                                                                                                           ; 32885*38 CLOCKS*0.2US= 250MS
                           2E: F6 06 01E7 R FF
75 18
E2 F6
FE CB
75 EF
                                                                                                            BYTE PTR CS:IRQDRQ,OFFH ; WAIT FOR INT, 17 CLOCKS
OUTCMD4 ; JMP IF INT, 4 CLOCKS
OUTCMD11 ; 17 CLOCKS
                                                                                             JNZ
LOOP
                                                                                             DEC
JNZ
                                                                                                            BL
OUTCMD2
                                                                             OUTCMD3:
480
481
483
484
485
486
488
489
489
491
493
494
496
497
                           BO 03
                                                                                             MOV
                                                                                                            AL, DRIVEDF
                                                                                                                                                          ; SET HARDWARE ERROR FLAG
                                                                             OUTCMD6:
                            8A EO
BO 01
E6 68
E4 61
E4 61
86 C4
EB OE
                                                                                                            AH,AL
AL,SBUFR
RDCMD2,AL
AL,ERROR
AL,ERROR
AL,ERROR
AL,AH
SHORT DUTCMD5
                 0150
0170
0172
0174
0175
0178
017C
017E
0180
0182
0188
0188
                                                                                             MOV
                                                                                                                                                           ; SAVE MAIN ERROR STATUS
; STROBE BUFFER READY
                                                                                             MO V
                                                                                             IN
IN
IN
XCHG
JMP
                                                                                                                                                           ; READ RD ERROR STATUS
; (THIS IS NOT BUG!!, WAIT UNTIL
; ERROR REGISTER IS VALID)
; FORM ERROR STATUS
                                                                             DUTCMD4:
                                                                                                            AL,RDSTAT
AL,DWRTF
OUTCMD3
AL,RDSTAT
AL,ERRF
OUTCMD6
                            E4 67
24 20
75 E8
E4 67
24 01
75 E4
33 C0
                                                                                             IN
AND
JNZ
IN
AND
                                                                                                                                                          ; GET STATUS
; CHECK WRITE FAULT
                                                                                                                                                          ; GET STATUS AGAIN
; CHECK ERROR
; ERROR
; NO ERROR
                                                                                             JNZ
                  018A
                                                                                             XOR
                                                                                                             AX,AX
498
499
500
501
                  018C
018C
018D
018E
                                                                             DUTCMD5:
                             59
58
C3
                                                                                             POP
POP
Ret
                                                                                                                                                         ; RESTORE REGISTERS
```

```
PAGE 1-13
The Microsoft MACRO Assembler Hard Disk Driver
                                                                                                       02-20-84
                                                                                                                   PAGE
502
503
                                                                                                 :******
504
505
506
507
508
509
510
511
512
513
514
515
517
518
519
520
                                                                                                                    ADVINS - advance to next sector
                                                                                                                                      BH = surface #
BL = sector #
DX = cylinder #
(RDXLT) = translate table
(XLT_F) = 0 if NOT USE translate table
<> 0 if USE
                                                                                                                   USED: AX

    This routine is 0S dependent
    (KLT_F) must be set before enter this
routine. (This flag will fix the logical
not logical and physical not physical problem.)
Actually only routines HREAD, HWRITE, and
HWRITEV calls this routine.

                                                                                                                   *** NOTE:
521
522
523
524
525
526
527
                     018F
018F
0194
0196
0188
019B
019D
                                  F6 O6 OOOA R FF
75 OB
FE C3
80 FB 10
7E 2A
B3 O1
EB 1C
                                                                                                                    TEST
                                                                                                                                       BYTE PTR XLT_F,OFFH ADVINS3
                                                                                                                                                                                                 : check if using translate table
                                                                                                                                                                                                ; oneca if using translate table; yes
; just set to next sector #;
; pass boundary?
; no
; yes, reset sector #;
; and update surface/cylinder #
                                                                                                                    JNZ
INC
CMP
                                                                                                                                       BL , SECPTRK ADVT1
527
528
529
530
531
532
                                                                                                                                        SHORT ADVINT
                                                                                                                     JMP
                      01A1
01A1
01A2
01A3
533
534
535
536
537
538
540
541
542
543
                                                                                                 ADVINS3:
                                   5 1
5 7
0 6
                                                                                                                    PUSH
PUSH
PUSH
                                                                                                                                       C X
D I
ES
                                                                                                                                                                                                 ; save registers
                     01A4 OE
01A5 O7
01A6 BF 0000 E
01A9 B9 0010
01AC 8A C3
01AE F2/ AE
                                                                                                                    PUSH
POP
MOV
MOV
                                                                                                                                       CS
ES
DI,OFFSET RDXLT
CX,SECPTRK
                                                                                                                                                                                                 ; ES = CS
                                                                                                                                                                                                ; get translate table
; get sectors per track
; get sector #
; check in XLT
                                                                                                                     MOV
                                                                                                                                        AL, BL
                                                                                                                     REPNZ
                                                                                                                                       SCASB
544
545
546
547
548
559
551
552
553
                      0180
0182
0183
0184
0186
0187
0188
                                   8A 1D
07
5F
E3 02
                                                                                                                    MOV
POP
POP
                                                                                                                                        BL, BYTE PTR [DI]
                                                                                                                                                                                               ; get next sector #
; restore registers
                                                                                                                                        ES
Di
                                                                                                                                       ADVT2
                                                                                                                     JEXZ
                                                                                                                                                                                                 ; if not in XLT
                                  5 9
C 3
                                                                                                                    POP
                                                                                                                    POP
                                  59
8A 1E 0000 E
                                                                                                                                                                                                 ; get first sector #
                      0189
                                                                                                                     MDV
                                                                                                                                        BL, RDXLT
                                                                                                                     JMP
                                                                                                                                        ADVINI
```

```
The Microsoft MACRO Assembler Hard Disk Driver
                                                      02-20-84
                                                                       PAGE
ADVINT - advance to next track
                                                             ENTRY: BH = surface #
DX = cylinder #
                                                   ·
           018D
018D
018F
01C2
01C4
01C6
01C7
                                                   ADVTNT:
                  FE C7
80 FF 04
7C 03
32 FF
                                                             I N C
                                                                       BH, NUMHEAD
ADVT1
BH, BH
DX
                                                                                                     ; bump surface #
; check pass limit
                                                             JL
XOR
INC
                                                                                                     ; set surface # =0
; bump cylinder #
                   С3
                                                             RET
```

```
The Microsoft MACRO Assembler
Hard Disk Driver
                                                                  02-20-84
                                                                                   PAGE
                                                                                                1 - 15
                                                               CLTPN - Convert Logical Track # to Physical surface/cylinder Number
                                                                          ENTRY: DX = logical track #
EXIT: BH = Surface #
DX = cylinder #
USE: NONE
                                                               ************************
             01C8
01C8
01CA
01CD
01CF
01D1
                                                               CLTPN:
                                                                                                  ; get low track byte
; keep surface only
; get rid of surface bits
                      8A FA
80 E7 03
D1 EA
D1 EA
C3
                                                                           MOV
AND
SHR
SHR
RET
                                                                                       BH, DL
BH, O3H
DX, 1
DX, 1
                                                                          CPTLN - Convert Physical surface/cylinder Number to Logical Track Number
                                                                          ENTRY: BH = Surface #
DX = cylinder #
EXIT: DX = logical track #
USE: AX
              01D2
01D2
01D4
01D6
01D8
01DA
01DC
                                                               CPTLN:
                       D1 E2
D1 E2
33 C0
8A C7
O3 D0
C3
                                                                                       DX,1
DX,1
AX,AX
AL,BH
DX,AX
                                                                                                                ; make room for surface #
                                                                           SHL
XOR
MOV
ADD
RET
                                                                                                                ; get surface #
; add surface #
```

```
The Microsoft MACRO Assembler Hard Disk Driver
                                            02-20-84
                                                          PAGE
                                                                  1-16
                                         INTHOL - INTERRUPT HANDLER
                                                         ********************************
                                         ************
         O1DD
O1DD 50
O1DE 2E: FE 06 01E7 R
O1E3 E4 67
                                                          AX
BYTE PTR CS:IRQDRQ
AL,RDSTAT
                                                                                  ; ACKOWNLEDGE INT
; CLEAR INT
                                                 INC
IN
                                             ENDIF
                                                          AX
         01E5 58
01E6 CF
                                                 POP
IRET
                                                                                  ; INTERRUPT FLAG !!!
; THIS BYTE BETTER GO WITH CODE_SEG
; SINCE, DS IS UNKNOWN WHEN INTERRUPT
                                         IRODRO LABEL
DB
         01E7
01E7 00
                                          DELAY - DELAY ABOUT 0.5 MS
                                                 ENTRY:
                                                          NONE
                                                          NONE
                                         O1E8
O1E8 51
O1E9 B9 OO94
O1EC E2 FE
O1EE 59
O1EF C3
                                         DELAY:
                                         DELAY:
PUSH
MOV
DELAY1: LOOP
POP
RET
                                                          CX
CX,148
DELAY1
CX
                                                                                  ; LOOP COUNT
; 17 CLOCKS * 0.2 US
```

```
PAGE
                                                                                                                                            1 - 17
                                                                                                02-20-84
The Microsoft MACRO Assembler Hard Disk Driver
                                                                                          RETRY - error recovery
                                                                                                            ENTRY: AX = original error status
BX = drive/surface/sector
DX = cylinder #
EXIT: AX = original error status
(RETNS) updates
                                                                                           01F0
01F0
01F3
01F7
01FD
01FD
01FF
0201
0203
0205
0208
0210
                                                                                          RETRY:
                                 A3 0007 R
FE 06 0005 R
BE 0006 R
C6 04 00
                                                                                                                                                                                   ; SAVE THE ORIGINAL ERROR
; update retry #
; reset local retry count
                                                                                                            MOV
                                                                                                                              ERRCODE, AX
BYTE PTR RETNS
SI, OFFSET RETRIES
BYTE PTR [SI], O
                                                                                                            I N C
MOV
MOV
                                 8A 04
24 03
75 10
8B 2C
81 E5 OOFC
D1 FD
2E: FF 96 0224 R
E8 01E8 R
                                                                                                                                                                                   ; GET RETRY COUNT;
TIME TO DO NEW TRICKS? (EVERY 4);
SKIP THE TRICK IF NO;
GET THE RETRY COUNT AGAIN;
ONLY KEEP THE BITS WE WANT;
SHIFT TO WORD OFFSET;
PERFORM THE TRICK;
WAIT FOR HEAD TO SETTLE
                                                                                                                              AL,[SI]
AL,0000011B
RETRY2
BP,[SI]
BP,11111100B
BP,1
CS:MOVEHR[BP]
                                                                                                            MOV
                                                                                                            MOV
AND
JNZ
MOV
AND
SAR
CALL
CALL
                                                                                                                              DELAY
                                                                                                             READ SECTOR RETRY
                    0213
0213
0216
0218
0218
021D
021F
                                                                                          RETRY2:
                                 E8 0105 R
B0 20
E8 014F R
OA CO
74 04
FE 04
EB DA
                                                                                                            CALL
MOV
CALL
OR
JZ
INC
                                                                                                                                                                                    ; RELOAD REGISTERS
; ISSUE THE READ COMMAND
                                                                                                                              LDTSKF
AL, RDREAD+ATRETRY
OUTCMD
AL, AL
RETRY3
BYTE PTR [SI]
SHORT RETRY1
                                                                                                                                                                                    ; ERROR?
; NO ERROR, FINALLY
; 1 MORE RETRY
; TRY MORE
                     0221 EB
0223
0223 C3
                                                                                                             JMP
                                                                                          RETRY3:
                                                                                                            RET
```

```
The Microsoft MACRO Assembler 
Hard Disk Driver
                                                                                02-20-84
                                                                                                        PAGE
                                                                                                                       1-18
                                                                           PAGE
MOVEHR - MOVE HEAD ROUTINES
                                                                                                        WORD
SAME
MOVEO4
MOVEI4
HOMET
MOVEMX
GIVEUP
                 0224
0224
0226
0228
022A
022C
022C
                                                                           MOVEHR
                                                                                                                                                    ; FIRST ROUND STAY ON SAME cylinder; OUT 4 cylinder; IN 4 cylinders; DO A RESTORE; MOVE TO INNER cylinder; TIME TO GIVE UP
                         0264 R
0243 R
0233 R
0258 R
0230 R
0260 R
                                                                                         DW
DW
DW
DW
DW
                                                                                         MOVE TO MINER cylinder
                 0230
0230 52
0231 EB OA
                                                                           MOVEMX:
                                                                                                                                                     ; SAVE cylinder # : GO TO LOAD MAX cylinder # & MOVE HEAD
                                                                                          PUSH
                                                                                                        DX
SHORT MVI41
                                                                                          MOVE IN 4 cylinderS
                                                                           :
                 0233
0233
0234
0237
023B
023D
                                                                           MOVEI4:
                           52
83 C2 O4
38 16 OOOO R
72 OE
                                                                                                        DX
DX,4
DX,MAXTRK
MOVEH
                                                                                                                                                     ; SAVE cylinder #
; ADD 4 cylinderS
; CHECK PASS MAX cylinder #
; IF < MAX, THEN PERFORM MOVE HEAD
                                                                                          PUSH
                                                                                          ADD
CMP
JC
                                                                           MV I 41:
                           8B 15 0000 R
EB 08
                                                                                                        DX,MAXTRK
Short Moveh
                                                                                          MOV
                                                                                                                                                     ; LOAD THE MAX cylinder # ; PERFORM MOVE HEAD
                                                                                          JMP
                                                                                          MOVE OUT 4 cylinderS
                                                                           :
                 0243
                                                                           MOVEO4:
                                                                                                        DX
DX,4
MOVEH
DX,DX
MOVEH
                                                                                                                                                     ; SAVE cylinder #
; SUB 4 cylinderS
; ( o ?
; JUST USE 0
; MERGE TO MOVE HEAD
                 0243
0244
0247
0249
                           52
83 EA 04
79 02
33 D2
                                                                                          PUSH
                                                                                          SUB
JNS
XOR
                                                                                          JMP
                                                                                          MOVEH - MOVE HEAD
                                                                           ;
                 024B E8 0105 R
024E A0 0002 R
0251 04 70
0253 E8 014F R
0256 5A
0257 C3
                                                                           MOVEH:
                                                                                                        LDTSKF
AL,STEPRATE
AL,SEEK
OUTCMD
DX
                                                                                          CALL
                                                                                                                                                     ; LOAD REGISTERS (cylinder #); GET STEP RATE and issue seek
                                                                                          MOV
ADD
CALL
POP
RET
                                                                                                                                                     ; RESTORE OLD cylinder #
 The Microsoft MACRO Assembler 
Hard Disk Driver
                                                                                 02-20-84
                                                                                                         PAGE
                                                                                                                        1 - 19
74901275557755877558776656776677667
                                                                                          PERFORM RESTORE
                 0258
0258 AO 0002 R
025B O4 10
025D E9 014F R
                                                                           HOMET:
                                                                                                         AL,STEPRATE
AL,RESTOR
GUTCMD
                                                                                          MO V
                                                                                                                                                     ; GET STEP RATE
; ISSUE RESTORE COMMAND
                                                                                          ADD
JMP
                                                                                          GIVE UP AFTER SO MANY RETRIES
                 0260
0260
0261
0264
0264
                                                                           GIVEUP:
                                                                                                         AX
AX, ERRCODE
                          58
A1 0007 R
                                                                                          POP
MOV
                                                                                                                                                     ; GET RID OF RETURN ADDR
; RETURN THE ORIGINAL ERROR
                            СЗ
                                                                                          RET
                  0265
                                                                                          ENDS
END
                                                                           CODE
```

e Microsoft MACRO Assembler d Disk Driver	02-2	0-84	PAGE	Symbols-1
ments and groups:				
N a m e	Size	align	combine	class
OUP	GROUP 0265	BYTE	PUBLIC	'CDDE'
bols:	0100	5	, , ,	2002
N a m e	Type	Value	Attr	
TC	Number	0004		
/T1	L NEAR L NEAR L NEAR	01C7 01B8 018F	CODE CODE CODE	
'TNS3	L NEAR L NEAR	01A1	CODE	
ETRY	Number	0026	CODE	
)BD	Number	0080		
DAST	Number	0005 0002		
	Number Number	0008 0003		
_HOM	Number Number	0001		
PAS	Number Number	0006 0007		
UKN	Number Number Number	0000 0001 0002		
15	Number	8000		
2	Number Number Number	0004		
5	Number Number Number	0010 0020 0040		
T	Number Number	0080		
LBN OFF	Number Number	0003		
T_0FF	Number Number	0021		
SY2	Number Number	0001		
TPN	Number L NEAR	0008 01C8	CODE	Global
OIP	Number	0002		
[LN	L NEAR Number	01D2 000D	CODE	
ER	Number Number	0040 0011		
RL_S	Number	0013		
L_S	Number	0013	PAGE	Symbols-2
RL_S	Number		PAGE	Symbols-2
ne Microsoft MACRO Assembler ed Disk Driver RL_Z	Number 02-2	0 - 84 0 0 1 A	PAGE	Symbols-2
e Microsoft MACRO Assembler d Disk Driver L_Z DH	Number 02-2 Number Number Number Number Number	001A 0065 0064 0001 0060		Symbols-2
ne Microsoft MACRD Assembler d Disk Driver kL_Z	Number O2-2 Number Number Number Number Number L NEAR	001A 0065 0064 0001 0060 0008 01E8	PAGE CODE CODE	Symbols-2
LL_S DE MICROSOFT MACRO Assembler DISK Driver LL_Z DH. DL. INF. A A	Number O2-2 Number Number Number Number L NEAR Number Number	001A 0065 0064 0001 0060 0008 01EC 0005	CODE	Symbols-2
e Microsoft MACRO Assembler d Disk Driver L Z	Number O2-2 Number Number Number Number L NEAR L NEAR Number Number Number Number Number Number Number	00 1A 0064 0064 0001 0080 0080 01E8 01EC 0000	CODE	Symbols-2
e Microsoft MACRO Assembler d Disk Driver L_Z	Number Number Number Number Number L NEAR L NEAR Number Number Number Number Number Number Number Number Number	001A 0065 0064 0001 0080 01EE 01EE 0005 0000 0000	CODE	Symbols-2
L_S de Microsoft MACRD Assembler d Disk Driver L_Z DH	Number Number Number Number Number Number L NEAR L NEAR Number	00-84 00-65 00-64 00-61 00-60 00 00-60 00-	CODE	Symbols-2
RL_S IN MICROSOFT MACRO Assembler IN DISK Driver RL_Z DH	Number Number Number Number Number Number L NEAR Number	001A 0064 00064 0000 01EE 0005 0000 000C 000C 000C	CODE	Symbols-2
TO MICROSOFT MACRO Assembler d Disk Driver tL_Z DH. DL. HARO ARO ARO API ARO BE-FLAG BE-FTN BE-LUN BE-UN B	Number Number Number Number Number Number L NEAR L NEAR Number	001A 0085 0084 0006 0080 0080 01EE 0005 0000 0000 0000 0000 0000 000	CODE	Symbols-2
IL_S IN MICROSOFT MACRO ASSEMBLER IL_Z DH DH DL IN MF IAA IAA IAA IAA IAY IAY IAY IA	Number Number Number Number Number Number L NEAR L NEAR Number	001A 0065 0066 0000 01E8 01E8 0000 0000 0000 0000 0000	CODE	Symbols-2
ll_S ie Microsoft MACRO Assembler od Disk Driver kL_Z DH DL NF A A A A A A	Number	00-84 00-18	CODE	Symbols-2
RL_S IN MICROSOFT MACRO Assembler IN DISK Driver RL_Z DH	Number	001A 0085 0084 0080 0080 0080 01EE 0005 0000 0000 0000 0000 0001 0002 0002	CODE	Symbols-2
TIL_S TI	Number	00 1A 00 05 5 00 06 4 00 06 0 00 06 0 00 06 0 00 06 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00	CODE	Symbols·2
TIL_S THE MICROSOFT MACRD ASSEMBLE TO DISK Driver RL_Z DH	Number	00-84 00-1A 00-65 00-06 00-08 01-1E 00-05 00-00	CODE	Symbols-2
TIL_S THE MICROSOFT MACRD ASSEMBLE TO DISK Driver RL_Z DH	Number	00-1A 0064 00064 0001 0050 0008 01EC 0005 000C 000C 000C 000C 000C 000C 00	CODE	Symbols-2
TILE MICROSOFT MACRO ASSEMBLER TO DISK DRIVER TO DISK DRIVER	Number	00-1A 0064 0001 0050 0008 01E2 0005 000C 000C 000C 000C 000C 000C 000	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLER TO DISK DRIVER RL_Z DH. DL. HARD ANT ANT ANT ANT ANT BE_LAG BE_FLAG BE_FTN BE_LTN BE_LTN BE_LTN BE_LU BE_OFF BE_OFF BE_OFF BE_ON DOFF DY YEA VEE VEE VEE VEE VEE VEE VEE VEE VEE V	Number	00-84 00-1A 00654 0001 0068 01EC 0005 000C 000E 000E 0001 0020 000B 0002 000A 0017 0020 000B 0002 000B 0001	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLER TO DISK DRIVER TAL_Z	Number	00-84 00-1A 00015 0008 01E8 01EC 0005 0000 000C 000C 000C 000C 000C 00	CODE	Symbols-2
IL_S IN MICROSOFT MACRO Assembler IN DISK Driver RL_Z DH	Number	00-84 00-1A 0065 0008 01EE 0000 000E 000E 000E 000E 000E	CODE	Symbols-2
TILES THE MICROSOFT MACRO ASSEMBLET TO DISK Driver TO DISK	Number	00-84 00-1A 0065 0068 0008 01EE 0000 000E 000E 0001 0020 000E 0001 0020 0008 0017 0020 0008 0010 0018 0000 0018 0000 0018 0000 0018 0000 0018 0000 0018 0000 0018 0000 0018 0000 0018 0000 0008 0000 0008 00000 0008 00000 0008 00000 0008	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLER TO DISK Driver RL_Z DH. DH. DL. ANF. AA . AA . AA . AAY . AAY . AAY . E_FLAG. E_FTN E_LTN EE_LTN EE_LTN EE_LTN EE_LTN EE_OFF EE_OFF EE_OFF EE_OPF START_OFF VY2 . VYEO	Number	00-84 00-1A 0065 0064 0001 0080 0188 0188 0000 0000 0000 000	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLER TO DISK Driver TL_Z DH. DH. DL. HANF TARQ TARQ TARQ TARQ TARY	Number	00-84 00-1A 0065 0008 01EE 0000 000E 000E 000E 000E 000E	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLET TO DISK Driver TL_Z DH DE DH DL MARA ARQ AY AY AY AY BE FLAG BE FLAG BE FTN BE LIN BE LIN BE LIN BE	Number	00-84 00-1A 0065 0068 01EE 0005 0006 0006 0007 0001 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 0020 0008 0017 00000 0008 0018 00000 0008 00000 0001 0008 00000 00000 00000 00000 00000 00000 0000	CODE	Symbols-2
TO MICROSOFT MACRO ASSEMBLET TO DISK Driver TL_Z DH DH DL MARCO AAY AAY AAY EARO AAY EFIAG BE_FIAG BE_FIAG BE_FIAG BE_FIN BE_LIN BE_LIN BE_LIN BE_LIN BE_LIN BE_LOFF BE_PN BE_PON DOFF START_OFF VY2 VY2 VY2 VY2 VY2 VY2 VY3 VY2 VY3 VY4 VY4 VY5 VY5 VY6 VY6 VY7 VY7 VY7 VY8 VY8 VY8 VY8 VY8	Number	00-84 00-1A 0005 00064 0001 0008 01ES 01000 000E 0000 000E 0000 000E 0000 0001 0018 0000 0010 001	CODE	Symbols-2 Globel
TO MICROSOFT MACRO ASSEMBLET TO DISK Driver TL_Z DH. DH. NNF. A. A. A. A. A. A. B. B. B. B	Number	00-84 00-1A 00654 0001 0068 01EE 00000 000E 000E 000E 0001 0020 000E 0001 0020 0008 0010 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 0008 00001 00001 00002 00001 00002 00004 00003 00001 00002 00004 00003 00004 00003 00004 00003 00006 00006 00007	CODE	

ficrosoft MACRO Assembler Fisk Driver	02-20-84	PAGE		
<u>,</u>	L NEAR 0129	CODE	Global	
A	Number 0114 Number 0294			
	L NEAR OOFE L NEAR 0258	CODE	Global	
	L NEAR OOOB L NEAR OO2E	CODE	Global	
0	L NEAR 001B	CODE		
	L NEAR 004D L NEAR 0026	CODE		
	L NEAR 0011 L NEAR 0102	CODE	Clabel	
	L NEAR 0058	CODE	Global Global	
:1	L NEAR OOSD L NEAR OOAA	CODE		
12	L NEAR OOB1 L NEAR OO79	CODE		
15	L NEAR OOBO	CODE		
: V	L NEAR 0051 Number 00E0	CODE	Global	
	Number 00A0 Number 0010			
	L NEAR 01DD	CODE	Global	
'	L BYTE 01E7 L NEAR 0105	CODE		
(1	L NEAR 0126 L NEAR 00B1	CODE		
	Number 000A	****		
	Number 0002 Number 0004			
	L WORD 0000 L NEAR 024B	CODE	Global	
	L WORD 0224	CODE		
	L NEAR 0233 L NEAR 0230	CODE		
	L NEAR 0243 Number 0004	CODE		
	Number 0002 L NEAR 023D	CODE		
OFF.	Number 0040	CODE		
OFF	Number 002D Number 0004			
	Number 001C L NEAR 014F	CODE		
11	L NEAR 0157	CODE		
)11	L NEAR 015E L NEAR 015B	CODE		
13	L NEAR 016C L NEAR 017E	CODE		
95	L NEAR 018C	CODE		
16	L NEAR 015E Number 0000	CODE		
TN	Number 0002 Number 0004			
FF	Number 0005			
ficrosoft MACRO Assembler	02-20-84	PAGE	Symbols-4	
ticrosoft MACRO Assembler Disk Driver	02-20-84	PAGE	Symbols-4	
lisk Driver IN	Number 0004 Number 0003	PAGE	Symbols-4	
insk Driver	Number 0004	PAGE	Symbols-4	
in	Number 0004 Number 0003 Number 0002 Number 0000 L WORD 0003	CODE	Symbols-4 Global	
N	Number 0004 Number 0003 Number 0000 L WDRD 0003 A 1 ias FALSE Number 0060	CODE		
insk Driver IN	Number 0004 Number 0003 Number 0000 Number 0000 L WORD 0003 Alias FALSE Number 0060 Number 0067	CODE		
N	Number 0004 Number 0003 Number 0002 Number 0000 L WDRD 0003 Alias FALSE Number 0060	CODE		-
insk Driver	Number 0004 Number 0003 Number 0000 L WDRD 0003 A 1 i as FALSE Number 0060 Number 0066 Number 0068 Number 0008 Number 0008	CODE		•
insk Driver	Number 0004 Number 0003 Number 0000 L WORD 0003 A 1 i as FALSE Number 0060 Number 0066 Number 0068 Number 0008 Number 0000 Number 0000 Number 0007 Number 0007 Number 0007 Number 0007 Number 0007	CODE		
insk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0008 Number 0020 Number 0020 Number 0030	CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0067 Number 0067 Number 0068 Number 0020 Number 0020 Number 0030 V BYTE 0000 L BYTE 0006	CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 A 1 ias FALSE Number 0060 Number 0066 Number 0068 Number 0008 Number 0008 Number 0000 Number 0010 L BYTE 0006 L BYTE 0006 L NEAR 01F0	CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0060 Number 0067 Number 0068 Number 0020 Number 0030 V BYTE 0000 L BYTE 0005 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213	CODE CODE CODE CODE CODE CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0000 L WDRD 0003 V BYTE 0000 L BYTE 0005 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 02213 L NEAR 02264	CODE CODE CODE CODE CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0060 Number 0068 Number 0068 Number 0067 Number 0007 Number 0007 Number 0000 L BYTE 0000 L BYTE 0000 L BYTE 0000 L BYTE 0010 L BYTE	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0068 Number 0068 Number 0068 Number 0020 Number 0020 Number 0030 V BYTE 0000 L BYTE 0000 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0223 L NEAR 0223 L NEAR 0223 L NEAR 0200 Number 0000 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0060 Number 0068 Number 0008 Number 0000 L WORD 0003 V BYTE 0000 L BYTE 0005 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0213 L NEAR 0223 L NEAR 02264 Number 0001 Number 0004 Number 0008 Number 0008	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0060 Number 0067 Number 0068 Number 0000 Number 0000 Number 0000 L BYTE 0000 L BYTE 0006 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0264 Number 0040 Number 0040 Number 0040	CODE CODE CODE CODE CODE CODE	Global	-
isk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias ALIS Number 0068 Number 0068 Number 0068 Number 0000 L WSYTE 0000 Number 0010 L BYTE 0000 L BYTE 0000 L BYTE 0000 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0223 L NEAR 0223 L NEAR 0223 L NEAR 0223 L NEAR 0200 Number 0000 Number 0000 Number 0000 Number 0000 Number 00066 Number 0086 Number 0086 Number 0086 Number 0086 Number 0000 Number 00200 Number 00200 Number 00066 Number 00000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias	CODE CODE CODE CODE CODE CODE	Global	
isk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0000 L WDRD 0003 V BYTE 0000 L BYTE 0006 L NEAR 0150 L NEAR 0150 L NEAR 0213 L NEAR 0213 L NEAR 0223 L NEAR 0226 Number 0040 Number 0040 Number 0040 Number 0040 Number 0040 Number 0050 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0062 Number 0062	CODE CODE CODE CODE CODE CODE	Global	
isk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0067 Number 0067 Number 0068 Number 0000 L WORD 0003 L WORD 0003 L WORD 0005 L WEAR 01F0 L NEAR 0213 L NEAR 0213 L NEAR 0213 L NEAR 0213 L NEAR 0254 Number 0040 Number 0040 Number 0040 Number 0056 Number 0056 Number 0050 Number 0060	CODE CODE CODE CODE CODE CODE	Global	
isk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0050 Number 0057 Number 0067 Number 0000 Number 0010 L BYTE 0000 L BYTE 0000 L BYTE 0010 L BYTE 0010 L BYTE 0010 L BYTE 005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0254 Number 0000 Number 0010	CODE CODE CODE CODE CODE CODE	Global	
isk Driver IN	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0066 Number 0067 Number 0067 Number 0070 Number 0010 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0213 L NEAR 025 L NEAR 0264 Number 0000 Number 00050 Number 00050 Number 0050 Number 0050 Number 0070 Number 0010	CODE CODE CODE CODE CODE CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0000 L WDRD 0003 Alias FALSE Number 0068 Number 0020 Number 0050 L WSTE 0000 L NEAR 01F0 L NEAR 0213 L NEAR 0254 Number 0000 Number 0010 Number 0010 Number 0000 Number 0010	CODE CODE CODE CODE CODE CODE	Global	
isk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0067 Number 0067 Number 0068 Number 0020 Number 0030 V BYTE 0000 L BYTE 0006 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0254 Number 0000 Number 0040 Number 0040 Number 0050 Number 0060 Number 0060 Number 0060 Number 0060 Number 0060 Number 0070 Number 0060 Number 0010 Number 0000 Number 00000 Number 00000 Number 00000 Number 00000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0066 Number 0067 Number 0067 Number 0070 Number 0010 L BYTE 0005 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 025 L NEAR 0264 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver IN	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0070 Number 0070 Number 0070 Number 0070 L BYTE 0000 L BYTE 0000 L BYTE 0000 L BYTE 0070 L BYTE 0070 L BYTE 0070 L NEAR 0170 L NEAR 0170 L NEAR 0213 L NEAR 0254 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0068 Number 0067 Number 0060 Number 0010 L BYTE 0000 L BYTE 0000 L BYTE 0006 L NEAR 01F0 L NEAR 0213 L NEAR 0213 L NEAR 0223 L NEAR 0223 L NEAR 0200 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
insk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0060 Number 0060 Number 0060 Number 0070 L BYTE 0000 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 025 L NEAR 0260 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
isk Driver IN	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0066 Number 0068 Number 0060 Number 0070 Number 0070 L BYTE 0000 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 025 L NEAR 0264 Number 0000	CODE CODE CODE CODE CODE CODE	Global	
isk Driver N	Number 0004 Number 0003 Number 0000 L WDRD 0003 Alias FALSE Number 0066 Number 0067 Number 0067 Number 0070 Number 0010 L BYTE 0006 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0213 L NEAR 025 L NEAR 0264 Number 0000	CODE CODE CODE CODE CODE CODE CODE	External	
insk Driver N	Number 0004 Number 0003 Number 0000 L WORD 0003 Alias FALSE Number 0067 Number 0067 Number 0068 Number 0020 Number 0030 V BYTE 0000 L BYTE 0006 L NEAR 01F0 L NEAR 01F0 L NEAR 0213 L NEAR 0253 L NEAR 0264 Number 0000	CODE CODE CODE CODE CODE CODE	Global	

	02-20-84	PAGE	Symbols-5
EPRD	Number 000D		
EPRE	Number 000E		
PRF	Number COOF		
PR OFF	Number 004C		
AST OFF	Number 0028		
o2. T	Number 0080		
OE	Number 0002		
	Number - 000	1	
TE	Number 0010		
I OFFSET	Number 0094		
I_SEG	Number 0096		
OFF	Number 004A		
CMP	Number 0061		
INT	Number 0001		
WF	L BYTE COOS	CODE	
RR	Number 000D		
BD	Number 0008		
BS	Number 000A		
CN	Number 0004		
DISKF	Number 0000		
DSN	Number 0003		
ERRC	Number 000C		
LDN	Number 0001		
SC	Number 0006		
SN	Number 0002		
SNT	Number 0002		
.F	L BYTE OOOA	CODE	Global

Symbol Cross Reference		# is d	efiniti	on)	Cref-1	
ABRTC	23#					
ADVT1	529	569	572#			
ADVT2	548	551#				
ADVTNS	150 526	260 533#	524#			
ADVINT	349	531	566#			
AST OFF	23#	•••				
ATRETRY	23#	123	215	250	686	
BADBD	23#					
BAT_OFF	23#					
BID_AST	23#					
BID_BAT	23# 23#					
BID_DPD	23#					
BID HOM	23#					
BID_OSN	23#					
BID_PAS	23#					
BID_PBT	23#					
BID_UKN	23# 23#					
BIT1	23#					
BIT15	23#					
BIT2	23#					
BIT3	23#					
BIT4	23# 23#					
BIT6	23#					
BIT7	23#					
BK_EC_OFF	23#					
BK_LBN_OFF	23#					
BK_MEC_OFF	23# 23#					
BOOT_OFF	23#					
CBSY	23#					
CBSY2	23#	_	_	_	_	
CGROUP	7 23#	9	9	9	9	
CLRSPL	23# 23#					
CLTPN	21	587#				
CMDIP	23#					
CODE	7	8#	8	766		
CPM8680	23#					
CPTLN	607# 23#					
CR	23#					
CTRL_0	23#					
CTRL'S	23#					
CTRL_z ."	23#					
CYLDH	23#	404				
CYLDL	23#	402				
DAMNF	23#					
DATA	23#	134	222	326		
DATARQ	23#					
DELAY	436	648#	680			
DELAY1	651#	651				

ymbol Cross Reference		(# is de	finiti	on)	Cref-	2					
KDF	. 23#	379									
[]FTN	. 23# . 23#										
E_LTN											
[_OFF											
E_PN	23#										
	. 23#										
	. 23#										
VEO	. 23#	441									
VE1	. 23#										
VE3		271 480									
YEM	. 23#										
TATO	. 23#					-					
RTF	. 23#	492									
RCODE											
RF	. 23#	668 495	762								
ROR		485	486	487							
ISE		23 319									
EUP	. 707	760#									
R		•									
SK	. 23#	441									
NDO2	. 23#										
ND12	. 23#										
n 2 2	274										
D3	. 23#										
ADM	. 23#	_									
DRMAT	. 331#	303# 336									
RMAT2	. 315	322 350	347	353#							
/IT	. 17	429#									
VEC_B	. 23#	365#									
wnn	. 18										
MET	. 705	752#									
MET	. 705 . 13		finiti	on)	Cref-	3					
MET	. 136# . 125#	752# 111# [# is de: 142	finitio	on) 159#	Cref-	3				 	
MET	. 136# . 125# . 122 . 127	752# 111# (# is de: 142 130 131# 151		•	Cref-	3					
MET	. 138# . 138# . 125# . 122 . 127 . 120# . 19	752# 111# (# is de: 142 130 131# 151 378# 201#		•	Cref-	3					
MET. GATO Disk Driver ymbol Cross Reference EAD10. EAD2	. 705 13 . 136# . 125# . 122 . 127 . 120# . 19 . 14 . 199 . 248	752# 111# [# is de: 142 130 131# 151 378#		•	Cref-	3			***************************************		
MET. EAD. erd Disk Driver ymbol Cross Reference EAD1 EAD10. EAD2 EAD2 EAD3 EAD5 EAD5 EAD5 EAD5 EAT5 EAT5 EAT5 EAT5 EAT5 EAT5 EAT5 EAT	. 136# . 136# . 125# . 122 . 127 . 120# . 19 . 14 . 199 . 248	752# 111# (# is det 142 130 131# 151 378# 201# 203#		•	Cref- 254#	3					
MET. ACAD: ACA	. 138# . 138# . 125# . 122 . 127 . 120# . 19 . 14 . 199 . 248 . 214 . 227#	752# 111# (# is de: 142 130 131# 151 378# 201# 203# 218 232 261	157	159#		3					
### ##################################	. 138# . 138# . 125# . 122 . 127 . 120# . 19 . 14 . 199 . 248 . 214 . 227# . 212# . 15	752# 111# (# is def 142 130 131# 151 378# 201# 203# 259# 218 232	157	159#		3					
ET	. 705 13 138# 125# 122 127 120# 19 14 199 248 214 227# 212# 15	752# 111# (# is de: 142 130 131# 151 378# 201# 203# 218 232 261	157	159#		3					
### ### ### ### ### ### ### ### ### ##	136# 125# 125# 1220# 120# 19 14 199 246 212# 212# 15 23# 23# 23#	752# 111# 142 130 131# 151 378# 203# 203# 218 232 261 197#	157 243	159#	264#						
MET. SANDA DISK Driver Imbol Cross Reference EAD1 EAD2 EAD3 EAD3 EAD3 EAD5 EAD5 EAD5 EAD1 EAD1 EAD2 EAD1 EAD2 EAD3 EAD3 EAD3 EAD5 EAD5 EAD6 EAD7 EAD7 EAD7 EAD7 EAD8 EAT7	136# 125# 125# 122 127 120# 19 14 199 246 214 227# 212# 15	752# 111# 142 130 131# 151 378# 203# 203# 218 232 261 197#	157 243	159#	264#	635#					
### ### ### ### ### ### ### ### ### ##	136# 125# 125# 122	752# 111# (# is det 142 130 131# 151 378# 201# 203# 259# 218 232 261 197#	157 243 464 314	159# 255 474 397#	264#						
### ### ### ### ### ### ### ### ### ##	136# 125# 125# 122	752# 111# (# is de: 142 130 131# 151 378# 203# 203# 218 232 261 197#	157 243	159#	264#	635#					
MET. BAD. AND Cross Reference EAD1 CAD10 CAD2 CAD2 CAD3 CAD5 CAD5 CAD5 CAD6 CAD7 CA	136# 125# 125# 122	752# 111# (# is det 142 130 131# 151 378# 201# 203# 259# 218 232 261 197#	157 243 464 314	159# 255 474 397#	264#	635#					
MET	136# 138# 125# 125# 127 120# 19 14 199 248 214 227# 223# 23# 23# 20 238 121 400 161 23# 23# 23#	752# 111# 142 130 131# 151 378# 203# 203# 218 232 261 197# 624# 342 213 416# 269#	157 243 464 314 354	159# 255 474 387# 451	264# 626 685	635#					
MET. EAD. ard Disk Driver ymbol Cross Reference EAD1 EAD1 EAD2 EAD3 EAD3 EAD5 EAD5 EAD5 EAD5 EAD5 EAD5 EAD6 EAD7 EAD7 EAD7 EAD7 EAD7 EAD7 EAD7 EAD7	136# 125# 125# 127 120# 19 14 199 246 214 227# 212# 15 23# 23# 20 238 121 400 161 23# 23# 23# 23# 23# 23# 20 238	752# 111# 142 130 131# 151 378# 203# 203# 218 232 261 197# 624# 342 213 416# 269#	157 243 464 314	159# 255 474 397#	264#	635#					
MET. EAD. ard Disk Driver ymbol Cross Reference EAD1 EAD2 EAD3 EAD5 DISK RITE RITE1 RITE2 RITE1 RITE2 RITE5 RITET RITES RITET RITES RITET RITES RITET RITES RITET RITES RITET RITET	138# 128# 125# 122 127 120# 19 14 199 246 214 227# 212# 15 23# 23# 20 238 121 400 161 23# 23# 23# 23# 23# 23# 20 238 1723#	752# 111# (# is def 142 130 131# 2013# 2013# 2013# 218 232 261 197# 624# 342 213 416# 269#	157 243 464 314 354	159# 255 474 397# 451	264# 626 685	635#					
### ### ### ### ### ### ### ### ### ##	705 13 138# 125# 122 127 120# 19 14 199 248 214 227# 215 23# 23# 23 20 238 23 121 400 161 23# 23# 23# 23 23# 20 23# 23# 20 23# 23# 20 23# 23# 23# 23# 20 23# 23# 23# 23# 24 25 24 25 25 25 25 25 25 25 25 25 25 25 25 25 2	752# 111# (# is de: 142 130 131# 151 378# 203# 203# 218 232 218 231 218 232 248 259# 259# 259# 259# 259# 259# 269# 279#	157 243 464 314 354	159# 255 474 397# 451	264# 626 685	635#					
MET. BEAD. ard Disk Driver ymbol Cross Reference EAD1 EAD10 EAD2 EAD3 EAD5 DISK RITE RITE1 RITE1 RITE1 RITE2 RITE2 RITE2 RITE2 RITE2 RITEY BIT. BIT. BIT. BIT. BIT. BIT. WFF. FODRO TSKF. TSKF. D_OFF. WERN XTRK WEH. WEHR WEHR WEHA WEHA WEMA WEMA	705 13 138# 125# 127 120# 19 14 189 246 2274 2212# 15 23# 23# 20 238 121 400 161 23# 23# 21 723 679 704 706 446 23# 23#	752# 111# (# is de: 142 130 131# 151 378# 201# 203# 218 232 251 197# 624# 342 213 416# 269# 726 701# 719# 712# 703	157 243 464 314 354 399 734	159# 255 474 397# 451	264# 626 685	635#					
### ### ### ### ### ### ### ### ### ##	705 13 138# 128# 128# 129 127 120# 19 14 199 248 227# 212# 23# 23# 23# 23# 23# 23# 23# 23# 23# 2	752# 111# 142 130 131# 151 378# 203# 203# 259# 218 232 261 197# 624# 342 213 416# 726# 726# 726# 726# 726# 726#	157 243 464 314 354 399 734	159# 255 474 397# 451	264# 626 685	635#					
### ### ### ### ### ### ### ### ### ##	705 13 136# 125# 1229 127 120# 19 14 199 248 212# 15 23# 23# 23# 23# 20 238 121 400 151 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# (# is de: 142 130 131# 151 378# 201# 203# 218 232 251 197# 624# 342 213 416# 269# 726 701# 719# 712# 703	157 243 464 314 354 399 734	159# 255 474 397# 451	264# 626 685	635#					
MET. SEAD. ard Disk Driver ymbol Cross Reference SEAD! SAD10. SAD2 SAD3 SAD5 SISK SITE SITE1 SITE1 SITE1 SITE2 SITE2 SITE2 SITE5 SITEV SITE SITEV SITE SITEV SITE SITEV SITE SITEV SITE SITEV SITE SITEV S	138# 138# 125# 122 127 120# 19 14 199 248 221# 23# 23# 23# 23# 23# 23# 23# 23# 23# 23	752# 111# (# is de: 142 130 131# 151 378# 201# 203# 218 232 251 197# 624# 342 213 416# 269# 726 701# 719# 712# 703	157 243 464 314 354 399 734	159# 255 474 397# 451	264# 626 685	635#					
MET	705 13 138# 125# 122# 127 14 199 246 214 227# 212# 15 23# 23# 20 238 121 400 161 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# (# is def 142 130 131# 151 378# 201# 203# 2259 232 2651 197# 624# 269# 726 701# 712# 703 724#	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742			75-		
MET . IEAD . IEAD . IEAD . IEAD 1 . IEAD 1 . IEAD 1 . IEAD 2 . IEAD 3 . IEAD 3 . IEAD 5 . IEAD 6 . IEAD 7 . IEAD 8 . IEAD 9 . IEAD	705 13 138# 128# 128# 129 120# 19 14 199 248 227# 15 23# 23# 23# 20 238 121 400 161 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# (# is de: 142 130 131# 151 378# 203# 259# 2518 232 218 231 218 242 213 416# 269# 726 719# 710# 710# 710# 703 724# 568 216345	157 243 464 314 354 399 734	159# 255 474 397# 451	264# 626 685	635#	687	745	755		
MET	705 13 138# 128# 125# 120# 19 14 199 248 227# 212# 23# 23# 23# 23# 23# 23# 23# 23# 23# 2	752# 111# (# is de: 142 130 1313 378# 203# 203# 203# 203# 218 232 218 232 218 232 218 232 218 232 218 232 218 234 259# 2567# 2567# 2567 7719# 703 724# 568 2344 568 77019# 703 704 705 705 705 705 705 705 705 705 705 705	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745			
MET	705 13 138# 128# 125# 127 120# 19 14 199 248 227# 23# 23# 23# 23# 23# 23# 23# 23# 23# 23	752# 111# (# is det 142 130 1318 203# 203# 203# 203# 203# 203# 203# 218 232 219 4169# 7261# 727 7192# 724# 568 2145 4768 4990#	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745	755		
MET	705 13 136# 125# 122# 127 120# 19 14 199 246 2214 222# 15 23# 23# 20 238 121 400 161 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# 142 130 1318# 1513701# 2003# 218 237014 218 2261 197# 6242 213 416# 2261 70019# 7192# 726 # 726 # 727 * 727 * 728 * 729 *	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745			
MET	705 13 136# 128# 128# 129 127 120# 19 14 199 248 214 212# 215 23# 23# 20 238 121 400 161 23# 23# 23# 21 400 23# 23# 23# 23# 21 400 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# 142 130 137013# 1518# 2003# 218 2261 197# 6242 2169 4261 197# 6242 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 7261# 72	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745			
MET	705 13 136# 128# 128# 129 14 199 14 199 248 214 212# 15 23# 23# 20 238 121 400 161 233# 237 714 23# 23# 23# 21 400 23# 23# 21 400 23# 23# 23# 21 400 23# 23# 23# 23# 23# 23# 23# 23# 23# 23#	752# 111# 142 130 137013# 1518# 2003# 218 2261 197# 6242 2169 4261 197# 6242 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 7261# 72	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745			
### ### ### ### ### ### ### ### ### ##	705 13 138# 125# 125# 125# 127 120# 19 14 199 248 227# 23# 23# 23# 23# 23# 23# 23# 23# 23# 23	752# 111# 142 130 137013# 1518# 2003# 218 2261 197# 6242 2169 4261 197# 6242 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 568 2169# 70019# 7103 724# 7261# 72	157 243 464 314 354 399 734	159# 255 474 397# 451 722 741#	264# 626 685 725	635# 742	687	745			

Γ

Symbol Cross Reference		(# is d	efiniti	on)	Cref	- 4								
PAS_BSN	. 23#													
PAS_BTN														
PRECOMP		27#	438					_						
PRE_READ	. 23#	37	115	153	160	206	306	432	629					
RDBASE	. 23#	23	23	23	23	23	23	23	23	23	23	23	23	
RDCMD	. 23#	465										23	23	
RDCMD2		148	240	258	344	435	484							
RDINTF	. 23#													
RDREAD		123	250	686										
RDSTAT		491	494	627										
RDWRITE		215												
RDXLT		540	553											
RESTOR		449 113	754											
RETRIES		670	156	669										
RETRY		667#												
RETRY1		691												
RETRY2		684#												
RETRY3	. 689	692#												
SAME	. 702 . 23#	763# 147	239	257										
SCANID		147	239	257	343	483								
SCIEXT														
SDH		272	408	442										
SECPTRK		316	327	528	541									
SECSIZE		135	223		• • • • • • • • • • • • • • • • • • • •									
SECTC		317	414											
SECTN	. 23#	411												
SECTSZ														
SEEK		744												
SEEKC														
SERR		158												
SINIT		434												
SSCTF.		737												
SSZ128	23#													
\$\$21K				-										
SSZ256														
\$\$2512		271	406	441										
STEPRO														
STEPR1														
STEPR2														
STEPR3														
STEPR4														
STEPRS		26												
STEPRO		26												
STEPRS														
STEPRS														
STEPRA	. 23#													
STEPRATE		26#	448	743	753									
STEPRB	. 23#													
STEPRC														
STEPRD														
STEPRE	. 23#													

Symbol	C	ro	SS		e f	f e i		100	•				(# is d	efiniti	on)	Cref-5
STEPRF												23#				
STEPR_C	FF											23#				
ST_AST_	OF.	F	٠									23#				
TRKO2.												23#				
TRKOE.												23#				
TRUE .			٠	٠		٠	٠			٠		23#	23			
WGATE.												23#				
WINI_OF	FS	EΤ										23#				
WINI_SE												23#				
WPC_OFF	٠.											23#				
WPREMP												23#	439			
WPRSNT												23#				
WRTWVF												33#	198	202	247	
WTVERR	•		٠				٠					23#	254			
X88_80												23#				
XBB BS												23#				
XBB_CN												23#				
x B B _ D I S	KF											23#				
XBB DSN	١.											23#				
XBB_ERR												23#				
XBB_LDN	١.											23#				
XBB SC												23#				
XBB SN												23#				
XCPRSNT												23#				
XLT F.											•	21	35#	525		

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                                                          02-20-84
                                                                                                                                                               PAGE
                                                                                                                                                                                      1 - 1
                                                                                                                                        PAGE 60,132
TITLE Hard Disk Initialization
NAME HINIT
1 2 3 4 5 6 7 8 9 1 1 1 2 3 4 4 5 6 7 8 9 1 1 1 2 3 4 4 5 6 7 8 9 1 1 1 2 3 4 5 6 7 8 9 2 1 2 2 3 4 5 6 7 8 9 3 1 2
                                                                                                                                        COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
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                                                                                                                                         09/22/83
                                                                                                                  ;
                                                                                                                  CGROUP GROUP CODE
CODE SEGMENT BYTE PUBLIC 'CODE'
ASSUME CS:CGROUP, DS:CGROUP, ES:CGROUP, SS:CGROUP
                          0000
                                                                                                                                        EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
EXTRN
                                                                                                                                                              HREAD: NEAR, HWRITE: NEAR, HWRITEY: NEAR
HFORMAT: NEAR, HINIT: NEAR, HMCHK: NEAR
HVDISK: NEAR, INTHOL: NEAR, PSTR: NEAR
INI_TAB: WORD, PTRSAVE: DWDRD, HDSKO: BYTE
PARTITION: WORD
GETFTN: NEAR, GETPEA: NEAR, CLTPN: NEAR
XLT_F: BYTE, MAXTRK: WORD, STEPRATE: BYTE, PRECOMP: WORD
BUFFER: WORD, PART_SIZE: ABS
XOPTION: BYTE
                                                                                                                                         PUBLIC DSK_INIT, RHOME, NDRY, HNFMT
PUBLIC UP_BAT, TEMP_TRK, TEMP_SEC, HD_INIT
                                                                                                                                         LIST
                          = 0004
                                                                                                                  MNPARTS EQU
```

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The Microsoft MACRO Assembler 
Hard Disk Initialization
                                                                                        02-20-84
                                                                                                                  PAGE
                                                                                                                                1 - 3
                                                                Common Drive parameter block dafinitions
345678901234567890123456
                                                                                  DBP
                                                                                  ;----- Start of Drive Parameter Block.
                  0000 7777
0002 77
0003 7777
0005 77
0006 7777
0008 7777
                                                                                                                                                    ;Sector size in bytes.;
;Number of sectors per alloc. block.;Reserved sectors.;Number of FAT's.;Number of FAT's.;Number of root directory entries.;Number of sector.;Number of sector.;Number of FAT Sectors.
                                                                                                                                                                                                                                       (dpb)
(dpb)
(dpb)
                                                                                  SECS2 DW
                                                                                  SECSZ DW
ALLOC DB
RESSEC DB
FATS DB
MAXDIR DW
SECTORS DW
MEDIAID DB
FATSEC DW
                                                                                                                                                                                                                                        (dpb)
(dpb)
                                                                                                                                                                                                                                       (dpb)
(dpb)
(dpb)
                                                                                  ;----- End of Drive Parameter Block.
                                                                                 SECTRK DW
HEADS DW
HIDDEN DW
                  000D ????
000F ????
0011 ????
                                                                                                                                                    ;Number of Sectors per track.;Number of heads per cylinder.,Number of hidden sectors.
                                                                                  DBP ENDS
                  0013
                  = 0013
                                                                                  DPB_DFF EQU
                                                                                                                 19
                                                                                                                                                   ;Size of DPB
                                                                                                  SUBTIL Hard Disk Drive initalization routine.
```

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                        02-20-84
                                                                                             PAGE
                                                                                                       1 - 4
                                                      Hard Disk Drive initalization routine.
PAGE
                                                                               PUBLIC HD_INIT
                                                                                                                                     ;Entry from BIOSINIT
                                                                  HD_INIT:
MOV
CALL
                        C6 06 00E2 R 00 90
E8 0000 E
                                                                                              NDRV,O
HINIT
                                                                                                                                      ; Initially no drives
; init hard disk
               0000
                        OE
07
BF 0000 E
E8 00E3 R
22 C0
74 05
               0009
000A
000B
000E
                                                                                PUSH
POP
MOV
CALL
                                                                                              CS
                                                                                                                                      ; set buffer addr for read home track
                                                                                              ES
DI,OFFSET BUFFER
RHOME
                                                                                                                                     ; read home
; check any error
               0011
                                                                                AND
                                                                                              AL,AL
DINIT2
               0013
                                                                                JZ
               0015
0018
                        BB 0042 R
EB 0D
                                                                                MD V
JMP
                                                                                              BX,OFFSET HNFMT
SHORT DINITX
                                                                                                                                      ; print error message
                        E8 0000 E
A0 00E2 R
OA CO
75 08
BB 0082 R
E8 0000 E
33 CO
               00 1 A
                                                                  DINITE: CALL
                                                                                              HINIT
                                                                                                                                     ; re_init hard disk
; get # of drives present
; see if any msdos partitions are here
               001A
001D
0020
0022
0024
0027
                                                                  DINITZ: CALL
MOV
OR
JNZ
MOV
DINITX: CALL
XOR
                                                                                             HINIT
AL, NDRY
AL, AL
DINITY
BX, OFFSET NOMSDOS
PSTR
AX, AX
                                                                                                                                     ; IF NOT, TELL THE USER
               0025
                        C3
                                                                   DINITY: RET
                                                                                                                                      ; RETURN # OF DRIVES
                                                                  ; DEVICE INIT ENTRY (HARD INIT ALREADY DONE, JUST RETURN INFO)
                                                                   DSK_INIT:
               002D
                                                                                             AH, NDRY
BX,CS:[PTRSAVE]
BYTE PTR [BX.MEDIA],AH ; # of drives for MSDDS
                       8A 26 OOE2 R
2E: C5 1E OOOO E
88 67 OD
                                                                                MOV
               0031
                                                                                LDS
               0039 C7 47 12 0000 E
003E 8C 4F 14
                                                                                             WORD PTR [BX.COUNT], OFFSET INI_TAB WORD PTR [BX.COUNT+2], CS
                                                                                MOV
                                                                                MOV
               0041 C3
                                                                                RET
              OO42 OD OA 57 41 52 4E
49 4E 47 3A 20 54
68 65 20 68 61 72
64 20 64 69 73 68
20 69 73 20 6E 6F
74 20 66 6F 72 6D
61 74 74 65 64 20
66 72 20 6E 6F
40 70 61 72 74 69
74 69 6F 8E 65 84
                                                                  HNFMT
                                                                                DB
                                                                                              CR.LF. 'WARNING: The hard disk is not formatted '
                                                                                DB
                                                                                             'or not partitioned!'
               21
007F OD OA OO
                                                                                DB
                                                                                              CR.LF.O
128
129
130
131
               0082 OD 0A 54 68 65 72
65 20 61 72 85 20
6E 6F 20 4D 53 2D
                                                                   NOMSDOS DB
                                                                                              CR, LF, 'There are no MS-DOS partitions on '
```

	Microsoft Disk Init	MACRO Assembler	02-20-84	PAGE	1 - 6	
	513K 1		Winchester HOME block	reader & D	PE/DPD builder	
40			PAGE			
41			; must be the	same order	as the equates	
42			`			
143	0087	48 4F 4D 00	BM_HOM DB	'HDM',0		
44	0088	42 41 54 00	BM_BAT DB	'BAT',0		
45	OOBF	44 50 44 00	BM_DPD DB	'DPD',0		
46	0003	4F 53 4E 00	BM_OSN DB	'DSN',0		
47	0007	41 53 54 00	BM_AST DB	'AST',O		
48	OOCB	50 41 53 00	BM_PAS DB	'PAS',O		
49			_			
50	OOCF		HOMEER	LABEL	WORD	; home track error word
51	OOCF	00	HT ERR C	DB	•	; home track error code
52	0000	00	HT_ERR_ID	DB	•	; home track error block II
53						
54	00D1	0000	ST AST	DW	0	: start track # for AST
55	0003	00	NT AST	DB	0	# of tracks for AST
5.6	0004	0000	NT_AST Bat_tn	DW	0	BAT track #
57	0006	00	BAT_SN	DB	0	: BAT sector #
58	0007	0000	BAT_LEN	DW	•	; BAT length
59	0009	00	BK_LBN	DB	•	; logical block #
60	OODA	0000	BK_MEC	DW	o .	; max entry count
61	0000	0000	BK_EC	DW	ò	; entry count
162	*****				-	
163	OODE	0000	TEMP	DW	٥	
164	OOEO	0000	SAVE SEC	DW	ŏ	
165	00E2	00	NDRV T	DB	ŏ	: # of drivers
	0022	••	HURT	0.0	•	, " " " " " " " "

```
The Microsoft MACRO Assembler 
Hard Disk Initialization
                                                                                      02-20-84
                                                                                                                 PAGE
                                                                                                                             1 - 7
                                                                Winchester HOME block reader & DPE/DPD builder
166
                                                                                 RHOME - Read 'HDM' block and other necessary blocks to build
the logical partition and alternate sectors.
More information can be obtained from V2 spec. DDS.MEM
                                                                                                This routine assumes that the RD controller board is installed.
                                                                                                ENTRY: ES:DI = sector buffer start address.

It must contain one sector memory (512 bytes).

Since there are more than 1 sector read in this routine, the sector buffer start address, DI, will be saved in (TEMP).
                                                                                                EXIT:
                                                                                                                AX = home track error word

(AL = error code, and AH = error block)

AL = 0, no error

AL = 1, sector read error (hard error)

AL = 2, block ID not found

AL = 3, block checksum error

AL = 4, no partitions
                                                                                                                            and
                                                                                                                                 d
AH = 0, unknown area
AH = 1, 'HOM' block
AH = 2, 'BAT' block
AH = 3, 'DPD' block
AH = 4, 'OSN' block
AH = 5, 'AST' block
AH = 6, 'PAS' block
AH = 8, BODT block
AH = 8, BODT block
                                                                                                                WORD HOMEER : AX, home track error word.
which can be examined later.
WORD ST_AST : start track # for alternate sector area
BYTE NT_AST : # of tracks reserved for alternate sector area
                                                                                 00E3
00E3
00E4
00E8
                                                                                 RHOME :
                              FC
88 3E OODE R
33 CO
A3 OOCF R
                                                                                                 C L D
Mov
                                                                                                                                                                ; set direction forward ; save buffer start addr
                                                                                                                  TEMP, DI
209
210
211
212
213
214
215
216
                                                                                                 X O R
M D V
                                                                                                                  AX,AX
Homeer,ax
                  OOEA
                                                                                                                                                               ; init error word
                              B3 02
33 D2
B4 01
E8 017B R
75 42
                                                                                                 MOV
XOR
MOV
Call
Jnz
                  00ED
00EF
00F1
00F3
00F6
                                                                                                                 BL,2
DX,DX
AH,BID_HOM
READINB
                                                                                                                                                                ; sector 2
; track 0
; HOM ID
                                                                                                                                                                  ; read HOM block
; abort if error
                                                                                                                  RHOM4
217
218
219
220
                  OOF8 26: 88 44 40
OOFC 48
OOFD A3 0000 E
                                                                                                 MO V
DEC
MO V
                                                                                                                  AX,ES:NCYLD_DFF[SI]
                                                                                                                                                                ; get # of cylinders
                                                                                                                  AX
Maxtrk,ax
                                                                                                                                                                 ; save max track #
```

```
The Microsoft MACRO Assembler
Hard Disk Initialization
                                                                                                  02-20-84
                                                                                                                                 PAGE 1-8
                                                                          Winchester HOME block reader & DPE/DPD builder
                                                                                                                                 AX,ES:WPC_OFF[SI] ; get write pre_comp value PRECOMP,AX ; save pre_comp AL,ES:STEPR_OFF[SI] ; get step rate value STEPRATE,AL ; save step rate
                   0100 26: 8B 44 4A
0104 A3 0000 E
0107 26: 8A 44 4C
010B A2 0000 E
                                                                                                               MOV
221
                                                                                                               MO V
MO V
MO V
223
224
225
226
227
                    010E 80 3E 00D9 R 00
0113 88 0104
0116 75 22
                                                                                                               CMP
                                                                                                                                 BK_LBN,O
AX.104H
                                                                                                                                                                                      ; check partitioned
; assume no partitions
                                                                                                               MOV
228
228
229
230
231
232
233
                                                                                                                                  RHOM4
                   0118 26: 8B 54 17
011C 26: 8A 5C 19
0120 26: 8B 4C 1A
0124 23 C9
0126 B8 0104
0129 74 0F
                                                                                                                                 DX,WORD PTR ES:DPD_OFF[SI] ; get DPD track number BL,BYTE PTR ES:DPD_OFF+2[SI] ; get sector # CX,WORD PTR ES:DPD_OFF+3[SI] ; get length CX,CX ; Check any entry? AX,104H ; assume no partitions RHOM4
                                                                                                               MDV
                                                                                                               MD V
                                                                                                               AND
MOV
JZ
234
236
237
238
239
                    012B B4 03
012D E8 017B R
0130 75 08
                                                                                                              MOV
Call
Jnz
                                                                                                                                 AH, BID_DPD
READINB
                                                                                                                                                                                        ; set block ID
; read DPD block
; abort if error
                                                                                                                                  RHOM4
241
242
243
244
245
246
247
                    0132 E8 013E R
0135 E8 01F0 R
0138 33 CO
013A
013A A3 00CF R
013D C3
                                                                                                                                                                                      ; build hard disk partition
; build hard disk drive DPE & DPB
; no error
                                                                                                               CALL
                                                                                                                                  BHPAR
                                                                                                             CALL
XOR
                                                                                                                                  BHDRV
AX,AX
                                                                                           RHOM4:
                                                                                                                                 HOMEER, AX
                                                                                                               MO V
                                                                                                                                                                                      ; save home track error word
```

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                               02-20-84
                                                                                                                             PAGE
                                                                        Winchester HOME block reader & DPE/DPD builder
248
249
250
251
252
253
254
255
256
257
258
259
260
261
                                                                                                            BHPAR - Build Hard disk PARtition first/last track #
                                                                                          ;
                    013E
013E 8B 36 00DE R
                                                                                          BHPAR:
                                                                                                           MOV
                                                                                                                             SI, TEMP
                                                                                                                                                                                  ; get buffer addr
                                                                                                                             CX,BK_EC ; get entry count CX,CX ; any entry? BHPAR4 ; done if no entry S1,0FFSET DPD_START_OFF ; point to entry start addr
                     0142 88 OE OODC R
                                                                                                            MOV
                    0142
0146
0148
014A
014D
014D
0151
0153
                                23 C9
74 30
83 C6 20
                                                                                                          AND
JZ
ADD
263456672667267727756727789012283
                                                                                          BHPAR1:
                                                                                                                             BYTE PTR ES:DPDE_FLAG[SI],DPDE_INIT ; check initialized? BHPARS ; skip if not BYTE PTR ES:DPDE_OST[SI],MSDDS ; check this OS type? ; skip this if wrong OS
                                  26: 80 3C FO
75 22
26: 80 7C 0B 02
75 1B
                                                                                                            CMP
                                                                                                             JN7
                                 AO OOE2 R
3C O4
74 19
FE O6 OOE2 R
E8 OOOO E
                                                                                                                                                                                                    ; get # of drives so far
; check max
; done if max drives reached
; update # of drives
; get partition entry addr
                                                                                                                             AL, NDRV
AL, MNPARTS
BHPAR4
                     015A
                                                                                                            моч
                     015D
015F
0161
0165
                                                                                                            CMP
JZ
INC
CALL
                                                                                                                             BITAR4
BYTE PTR NDRV
GETPEA
                                                                                                                                                                                                   ; get first track #
; save to partition table
; get last track #
; save to partition table
; set to next DPD entry
; loop for all entries
                                  26: 8B 44 OC
89 O7
26: 8B 44 OE
89 47 O2
83 C6 20
E2 D3
C3
                    0168
016C
016E
0172
0175
0178
                                                                                                                             AX, WORD PTR ES:DPDE_FTN[SI]
WORD PTR PART FTN[BX], AX
AX, WORD PTR ES:DPDE_LTN[SI]
WORD PTR PART LTN[BX], AX
SI, OFFSET DPDE_OFF
BHPARI
                                                                                                            MOV
                                                                                                            MOV
MOV
MOV
ADD
                                                                                          BHPAR4:
```

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                                           02-20-84
                                                                                                                                            PAGE 1-10
                                                                                Winchester HOME block reader & DPE/DPD builder
                                                                                                       PAGE
284
285
286
287
288
289
290
291
292
293
294
295
                                                                                                                        READIN - This routine read in a Wini sector into the buffer area.
It also checks the correct block ID and checksum.
Some useful data are also being colected.
                                                                                                                        ENTRY:
                                                                                                                                           AH : block ID
BL : sector number
DX : logical track number
ES:(TEMP) : sector buffer start addr
296
297
298
299
300
301
302
303
304
305
306
                                                                                                                        EXIT:
                                                                                                                                            AX : error word defined in RHOME routine SI : (TEMP)
Z flag is set if error
BYTE BK_LBN : logical block number, byte 3
WORD BK_MEC : max entry count, byte 6-7
WORD BK_EC : entry count, byte 8-9
                                                                                                     READINB:
                       017B
                       0178
017B 53
017C 52
017D E8 0187 R
0180 5A
0181 5B
0182 75 01
0184 C3
308
308
309
310
311
312
                                                                                                                        PUSH
PUSH
CALL
POP
POP
                                                                                                                                                                                                         ; save sector #; save track #
                                                                                                                                             DX
READIN
DX
BX
                                                                                                                                             READINB 1
                                                                                                                         JNZ
RET
                       0185
0185
0186
                                                                                                                                                                                                        ; try to read the alternate block
                                                                                                                         INC
                                                                                                                                             READIN
                       0187
0187
018B
018E
                                                                                                     READIN:
                                                                                                                                                                                                        ; save block ID
; convert to physical #
; get buffer start addr
; set sector count
; set physical flag
; read sector
; assume hard error
                                     88 26 OODO R
E8 OOOD E
88 3E OODE R
B9 OOOI
C6 O6 OOOO E OO
E8 OOO E
BO O1
75 4A
                                                                                                                                             HT_ERR_ID,AH
CLTPN
DI,TEMP
CX,1
BYTE PTR XLT_F,O
HREAD
                                                                                                                         MOV
                                                                                                                         MOV
MOV
MOV
CALL
                       018E
0192
0195
019A
019D
019F
                                                                                                                                             AL,1
READIN10
                                                                                                                         JNZ
                                                                                                                         check
                                                                                                                                        correct block ID
                       01A1 88 3E OODE R
01A5 AO OODO R
01A8 FE C8
01AA 98
01AB D1 E0
01AD D1 E0
01AF BE 0087 R
                                                                                                                                             DI,TEMP
AL,HT_ERR_ID
AL
                                                                                                                         MOV
                                                                                                                                                                                                        ; get buffer addr
; get block ID
                                                                                                                         MOV
DEC
CBW
SHL
SHL
MOV
                                                                                                                                             AX,1
SI,OFFSET BM_HOM
                                                                                                                                                                                                        : get string start addr
```

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                                                                                    1-11
                                                                                                      02-20-84
                                                                                                                                     PAGE
                                                                            Winchester HOME block reader & DPE/DPD builder
                     0182 03 F0
0184 E8 0251 R
0187 B0 02
0189 75 30
                                                                                                                   ADD
Call
                                                                                                                                      SI,AX
COMPS
                                                                                                                                                                                               ; assume ID not found
341
342
343
344
345
346
347
348
350
351
352
353
354
355
                                                                                                                   MOV
                                                                                                                                      AL,2
READIN10
                                                                                                                   JNZ
                                                                                                                  check correct check sum
                                                                                                :
                     01BB 8B 36 00DE R
01BF 89 0100
01C2 33 DB
01C4 26: AD
                                                                                                                                      SI,TEMP
CX,512/2
BX,BX
                                                                                                                                                                                               ; get buffer start addr
; get sector count
; init check sum
                                                                                                                   MOV
                                                                                                                   MOV
XOR
                                                                                                READINS:
                                   26: AD
03 D8
E2 FA
23 DB
B0 03
75 1B
                                                                                                                   LODS
                                                                                                                                      ES:WORD PTR [SI]
                                                                                                                                                                                             ; get data
; add to check sum
; loop for whole sector
; checksum=0?
                      01C4
01C5
01C8
01CA
01CC
                                                                                                                   ADD
                                                                                                                   LOOP
AND
MDV
                                                                                                                                      READINS
BX,BX
AL,3
READIN10
                                                                                                                                                                                                ; assume check sum error
                                                                                                                   JNZ
356
357
358
359
360
361
                                                                                                                   save some useful information
                    01D0 88 36 OODE R
01D4 26: 8A 44 03
01D8 A2 OOD9 R
01DB 25: 88 44 06
01DF A3 OODA R
01E2 26: 8B 44 08
01E6 A3 OODC R
01E9 32 CO
01EB
01EB 8A 26 OODO R
01EF C3
                                                                                                                                     SI,TEMP ; get buffer addr
AL,BYTE PTR ES:BK_LBN_OFF[SI] ; get logical block number
BK_LBN,AL ; save it
AX,WORD PTR ES:BK_MEC_OFF[SI] ; get max entry count
BK_MEC,AX ; save it
AX,WORD PTR ES:BK_EC_OFF[SI] ; get entry count
BK_EC,AX ; save it
AL,AL ; no error
                                                                                                                   MDV
MDV
MDV
                                                                                                                   MOV
MOV
MOV
362
363
364
365
366
367
368
368
                                                                                                                    XOR
                                                                                                READIN10
                                                                                                                   MOV
RET
                                                                                                                                      AH, HT_ERR_ID
                                                                                                                                                                                              ; restore ID
```

```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                          Winchester HOME block reader & DPE/DPD builder
BHDRV - build hard disk logical drives
                                                                                       ENTRY: (PARTITION) should contain first & lest track
number for all drives, if any one of them
is not present, the first track number should
contain a 0.
                                                                                                  DPB are built
ALL
                 01F0
01F3
01F5
01F9
01F9
01FA
01FG
01FF
                                                                        BHDRV:
                           BF 0000 E
BE 0000 E
B9 0004
                                                                                                     DI,OFFSET PARTITION
SI,OFFSET HDSKO
CX,MNPARTS
                                                                                       MDV
                                                                                                                                               ; do all partitions
                         51
8B 1D
8B 4D 02
23 DB
74 05
                                                                                       PUSH
                                                                                                     BX, PART_FTN[DI]
CX, PART_LTN[DI]
BX,BX
BHDRV1
                                                                                                                                                ; get first track #
; get last track #
; check this partition present
; Skip if not
                                                                                       MOV
MOV
                0203 56
0204 E8 0213 R
0207 5E
0208 0208 81 C7 0000 E
020C 83 C6 13
020F 59
0210 E2 E7
                                                                                        PUSH
                                                                                                                                                ; save DPE
; build this DPE & DPB
; restore DPE
                                                                                        CALL
                                                                                                      BDPB
                                                                                       POP
                                                                         BHDRV1:
                                                                                       ADD
                                                                                                     DI,PART_SIZE
SI,DPB_OFF
                                                                                                                                               ; set to next partition
                                                                                        ADD
                                                                                                                                                ; restore partition count ; loop until all partitions
                                                                                       POP
                                                                                                     BHDRV2
                                                                                       LOOP
                 0212 C3
                                                                                       RET
                                                                                       BDPB - Build Disk Parameter Block Fields
for Winchester logical disk drive
                                                                                       ETNRY: BX = first track #
CX = last track #
SI = DPE addr
                                                                         ·
                 0213
0213
0215
0216
0218
021A
021C
418
419
420
421
422
423
424
                                                                         BDPB:
                         28 CB
4.1
D1 E1
D1 E1
D1 E1
D1 E1
                                                                                       SUB
INC
SHL
SHL
                                                                                                     CX, BX
CX
CX, 1
CX, 1
CX, 1
                                                                                                                                 ; total tracks
                                                                                       SHL
                                                                                                                                  ; total sectors
```

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```
The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                                                   02-20-84
                                                                                                                                                      PAGE
                                                                                    Winchester HOME block reader & DPE/DPD builder
                       021E 89 4C 08
0221 8D 0002
0224 8B C1
0226 2D 0030
0229 2B C5
022B D1 E8
022D D1 E8
022T 8B B B
0231 D1 E8
0231 D1 E8
0233 03 C3
0235 05 0004
0238 33 D2
0238 77 F3
0237 25 00FF
0242 40
0243 3B C5
0247 8B E8
0249 7C D9
0248 D1 E8
0249 7C D9
0248 D1 E8
                                                                                                                                                     WORD PTR SECTORS[SI],CX
BP,2; assum
BDPB1:
                                                                                                                                                                                               ; get total sectors
; 2 reserved tracks+16 sectors for DIR
; - FAT = total sectors for data
; / 4 sectors per cluster
                                                                                                                                MOV
                                                                                                                                                      AX,CX
                                                                                                                                                     AX, CX
AX, 32+16
AX, BP
AX, 1
BX, AX
AX, 1
AX, 1
AX, 8X
AX, 4
BX, 512
DX, DX
BX
                                                                                                                                SUB
SUB
SHR
SHR
MOV
                                                                                                                                                                                              ; *1.5
; /2
                                                                                                                                SHR
ADD
ADD
MOV
XOR
DIV
                                                                                                                                                                                               ; +3 and 1 may be overflow
                                                                                                                                                                                                 THIS MAY FIX A BUG PIS
                                                                                                                                                     DX, DX ; THIS MA

BX

AX, OFFH ; don't

AX

AX AX ; 2 cop-

AX, BP ; enough

BP, AX

BDPB1

AX, 1

WORD PTR FATSEC[SI], AX
                                                                                                                                 AND
INC
ADD
CMP
MOV
                                                                                                                                                                                                 ; don't remove this instruction
                                                                                                                                                                                                ; 2 copies of FAT
; enough sector for FAT
                                                                                                                                 JL
SHR
MOV
RET
```

```
The Microsoft MACRO Assembler 
Hard Disk Initialization
                                                                                                                02-20-84
                                                                                                                                                 PAGE
                                                                                                                                                                  1-15
                                                                                  Winchester HOME block reader & DPE/DPD builder
                                                                                                       PAGE
UP_BAT:
                    025A 025A 8C C8 025C 8E D8 025E 8E C0 0280 8F 0000 E 0283 88 16 02F2 R 0287 8A 1E 02F4 R
471
472
474
475
476
475
477
480
481
482
484
485
487
488
489
489
489
489
489
489
489
489
500
501
502
                                                                                                                                                 AX,CS
DS,AX
ES,AX
DI,OFFSET BUFFER
DX,TEMP_TRK
BL,TEMP_SEC
WT_BAD_B
                                                                                                                             MO V
MO V
MO V
                                                                                                                                                                                                              ;SETUP ES:DS:
                                                                                                                                                                                                        ; set up working buffer ES:DI
; get bad track #
; and sector #
                                                                                                                             MOV
                                                                                                                            WT_BAD_B - write bad bit into BAT
                                                                                                                            ENTRY:
                                                                                                                       DX = track #
BL = sector #
ES:DI = buffer
NOTE: all BAT must stay in the same track
                       0288 89 1E 00E0 R
0268 89 3E 00DE R
0273 88 C2
0275 88 18 00D4 R
0279 8A 1E 00D6 R
0279 8A 0E 00D7 R
0281 0281 3D 00FA
0284 72 08
0286 FE C3
                                                                                                       WT_BAD_B:
MOV
MOV
MOV
                                                                                                                                                                                                           ; save sector #
; save buffer addr
; get bad track #
; get BAT track #
; get BAT sector #
; get length
                                                                                                                                                 SAVE_SEC, BX
TEMP, DI
AX, DX
DX, BAT_TN
BL, BAT_SN
CX, BAT_LEN
                                                                                                                             MO V
MO V
                                                                                                       WT_BADB1:
                                                                                                                                                 AX,250
WT_BADB4
BL
                                    3D OOFA
72 O8
FE C3
2D OOFA
E2 F4
C3
                                                                                                                                                                                                            ; check in this block
; by compare with entry size
; set to next sector
                                                                                                                             JB
Inc
                        0284
0286
0288
028B
028D
028E
 503
                                                                                                                                                  AX,250
WT_BADB1
504
505
506
507
508
                                                                                                                             LOOP
                                                                                                                                                                                                               ; can't find it, don't update BAT
                                                                                                       RET
WT_BADB4:
PUSH
PUSH
PUSH
GOV
CALL
POP
                       ; save track #
; save sector #
; save track offset in BAT
; set ID
; read the sector
; restore offset
; if error don't update BAT
                                                                                                                                                 DХ
                                                                                                                                                 BX
AX
AH,BID_BAT
READINB
509
510
511
512
513
514
515
516
517
518
518
                                                                                                                                                 AX
WT_BADB5
                                                                                                                             JNZ
                       O299 88 3E OODE R
O29D 88 F7
O29F D1 EO
O2A1 03 F0
O2A3 8B 0E OOEO R
O2A7 49
O2A8 88 OOO1
O2AB D3 EO
O2AD 26: O9 44 OC
O2B1 26: C7 45 04 OOOO
                                                                                                                                                                                                          ; get buffer addr
; get buffer addr
; word offset
                                                                                                                             MOV
MOV
SHL
ADD
                                                                                                                                                 DI,TEMP
SI,DI
AX,1
SI,AX
                                                                                                                                                                                                        ; get sector #
; base 0
; set 1 bit
; shift into position
; set the bit
; clear checksum
 520
                                                                                                                                                   CX,SAVE_SEC
                                                                                                                              MOV
                                                                                                                                                  CX,SAVE_SEC
CX
AX,1
AX,C1
ES:12[SI],AX
ES:WORD PTR 4[DI],O
                                                                                                                             DEC
MOV
SHL
 524
525
                                                                                                                             OR
Mov
 The Microsoft MACRO Assembler Hard Disk Initialization
                                                                                                                 02-20-84
                                                                                                                                                  PAGE 1-16
                                                                              Winchester HOME block reader & DPE/DPD builder
                       0287 33 DB
0289 88 F7
0288 B9 0100
028E 026: AD
02C0 03 D8
02C2 E2 FA
                                                                                                      XOR
MOV
MOV
WT_BCLP:
LODS
                                                                                                                                                                                                 ; init new checksum
; get buffer offset
; # of words in buffer
; get a word
; accumulate result
; do'em all
                                                                                                                                                 BX,BX
                                                                                                                                                  SI,DI
CX,512/2
 ES:WORD PTR [SI]
                                                                                                                                                 BX,AX
WT_BCLP
                                                                                                                             ADD
                                                                                                                             LOOP
                       OZCZ E2 FA

OZC4 F7 DB

OZC5 25: 88 5D O4

OZCA 5B

OZCE 5A

OZCC 53

OZD0 52

OZD1 57

OZD2 51

OZD3 E8 OOOO E

OZD5 E0 OOOO E

OZD8 55

OZD7 55

OZD7 55

OZD7 55

OZD8 54

OZE1 58

OZE2 42
                                                                                                                                                                                                            ; compliment
; store new checksum
; restore sector
; and track
; write | sector
; Save for 2nd write
                                                                                                                             NEG
MOV
POP
POP
                                                                                                                                                  BX
ES:4[DI],BX
                                                                                                                                                  DX
                                                                                                                                                  CX,1
BX
DX
DI
CX
                                                                                                                              MOV PUSH PUSH PUSH CALL CALL POPPOPPOPINC CALL MOV
                                                                                                                                                                                                          ; convert to physical #
; set translate flag
; update the bad bit
; restore write params
                                                                                                                                                  BYTE PTR XLT_F,O
HWRITE
CX
                                                                                                                                                   DX
BX
                                                                                                                                                  BX
DX
DX
CLTPN
BYTE PTR XLT_F,O
HWRITE
                        02E1
02E2
02E3
02E4
02E7
02EC
                                                                                                                                                                                                               ; Step to 2nd copy of BAT
                                        42
E8 0000 E
C8 06 0000 E 00
E9 0000 E
                                                                                                                                                                                                               ; convert to physical #
; set translate flag
; update the bad bit and RETURN
                                                                                                                              JMP
                                                                                                        WT_BAD85:
POP
POP
RET
                         02EF
                         02EF
02F0
02F1
                                                                                                                                                  AX
AX
                                         58
C3
                                                                                                                                                                                                                : clean stack and exit
                         02F2 0000
02F4 00
                                                                                                         TEMP_TRK
TEMP_SEC
                         02F5
                                                                                                         CODE
                                                                                                                             ENDS
```

The Microsoft MACRO Assembler and Disk Initialization	•••	:0-84 P		
tructures and records:				
N a m e	Width Shift	# fields Width M	da sk	Initial
BP	0013	000В		
SECSZ	0000			
RESSEC	0003 0005			
MAXDIR	0006			
MEDIAID	OOOA			
SECTRK	000D 000F			
HIDDEN	0011 0016	0009		
CMDLEN	0000	0003		
CMD	0002			
STATUSW	0003 000D			
TRANS	000E 0012			
START	0014			
gments and groups:				
N a m e	Size	align c	ombine	class
ROUP	GROUP 02F5	BYTE P	PUBLIC	'CODE'
mbols:				
N a m e	Type	Value A	Attr	
RTC	Number	0004		
T_OFF	Number Number	0026		
DBD	Number L WORD		ODE	
T_OFF	Number L BYTE		ODE	
IT_TN	L WORD L NEAR	0213	CODE	
PB1	L NEAR L NEAR		CODE	
DRY1	L NEAR L NEAR	0208	ODE	
PAR		013E C	ODE	
PAR4	L NEAR		ODE	
ID_AST	L NEAR Number	0005	PAGE	Symbols-2
ID_AST	Number 02-2 Number	0005 20-84 P		Symbols·2
ID_AST	Number 02-2 Number Number Number	0005 00-84 P		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT	Number 02-2 Number Number Number Number	0005 0008 0003 0001 0004		Symbols-2
D_AST. he Microsoft MACRO Assembler rd Disk Initialization D_BAT. D_BOT. D_DPD. D_HOM. D_OSN. D_PAS.	Number O2-2 Number Number Number Number Number Number	0005 0002 0008 0003 0001 0004 0006		Symbols-2
D_AST. he Microsoft MACRO Assembler rd Disk Initialization D_BAT. D_BOT. D_DPD. D_HOM. D_OSS. D_PAS. D_PAS. D_UKN. TO	Number O2-2 Number Number Number Number Number Number Number	0005 0002 0008 0003 0001 0004 0006 0007 0000		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_OPD. D_HOM. D_OSN. D_PAS. D_PBT. D_UKN. TO	Number O2-2 Number	0005 0002 0008 0003 0001 0004 0005 0007		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DD. D_HOM. D_OSN. D_PAS. D_PBT. D_UKN. TO T1 T15. T2 T3	Number	0005 0002 0008 0003 0001 0004 0006 0007 0000 0001		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DPD. D_HOM. D_DSS. D_PAS. D_PAS. D_UKN. TO T1 T15. T2 T3 T4	Number	0005 0002 0008 0003 0001 0004 0006 0007 0000 0001 0002 8000		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D-HOM. D-OSN. D-PAS. D_PBT. D_UKN. TO T1 T15. T2 T3 T4 T5 T6 T7	Number	0005 0002 0003 0003 0001 0004 0005 0007 0000 0001 0002 8000 0008 0010		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DPD. D_HOM. D_PAS. D_PBT. D_UKN. TO T1 T15 T2 T3 T4 T5 T7 EEC.	Number Number	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0004 0008 0010 0020 0008		Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DPD. D_HOM. D_PAS. D_PAS. D_UKN. TO T1 T1 T2 T3 T4 T5 T7 T6 T7 T6 T7 EEC. EEC. EEC. EEC. EEC. EEC. EEC. EEC	Number	0005 0002 0008 0001 0004 0000 0001 0004 0008 0010 0020 0040 00000 00000 00000 00000 00000 00000 0000	PAGE	Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_BOT. D_OPD. D_HOM. D_OSN. D_PAS. D_UKN. TO T1 T15 T2 T3 T4 T5 T6 T7 T6 T7 T6 T7 ECC_DEC_DEC_DEC_DEC_DEC_DEC_DEC_DEC_DEC_	Number L WORD	0005 0002 0008 0003 0001 0004 0005 0001 0002 8000 0001 0002 8000 0001 0002 8000 0000 0	PAGE	Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D-DPD. D-HOM. D_PAS. D_PBT. D_UKN. TO T1 T15. T2 T3 T4 T5 T6 T7 EC_OFF. LBN_OFF. LBN_OFF. MEC_OFF. MEC_OFF. MEC_OFF. MEC_OFF. MEC_OFF.	Number L WORD Number L BYTE Number L WORD	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0001 0002 8000 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008	CODE CODE	Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT	Number L WORD Number L BYTE L BYTE L BYTE L BYTE	0005 0002 0008 0003 0001 0004 0006 0007 0000 0001 0002 8000 00010 0020 00000 0010 0020 00000 0010 0020 00000 0010 0020 00000 0010 0020 00000 0010 0020 00000 0010 0020 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000 0010 00000	CODE CODE CODE CODE CODE	Symbols-2
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT	Number L WORD Number L BYTE L BYTE L BYTE L BYTE	0005 0002 0008 0003 0001 0004 0006 0007 0000 0001 0002 8000 0001 0002 8000 0001 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0008 0010	CODE CODE CODE CODE CODE CODE CODE CODE	Symbols-2
D_AST	Number L WORD Number L BYTE L	0005 0002 0008 0001 0004 0006 0007 0000 0001 0002 8000 0001 0002 8000 0001 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0080 008	CODE CODE CODE CODE CODE CODE CODE CODE	Symbols-2
The Microsoft MACRO Assembler and Disk Initialization D_BAT	Number L WORD Number L WORD Number L WORD Number L BYTE Number V WORD Number	0005 0002 0003 0001 0004 0006 0007 0000 0001 0002 8000 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0010 0010	CODE CODE CODE CODE CODE CODE CODE CODE	Symbols-2 External
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DPD. D_DPD. D_DS. D_PAS. D_PAS. D_PAS. D_UKN. TO T1 T15 T2 T3 T4 T5 T4 T5 T6 T7 LEC_OFF. LEN_CFF. LEN_CFF. MEC_OFF.	Number L WORD Number L BYTE L BYTE L BYTE L BYTE L BYTE L BYTE	0005 0002 0008 0003 0001 0004 0006 0007 0002 8000 0001 0002 8000 0001 0008 0010	CODE CODE CODE CODE CODE CODE CODE CODE	
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_BOT. D_DPD. D_DPD. D_DNO. D_PAS. D_UKN. TO TI TI TIS. T2 T3 T4 T5 T2 T4 T5 T6 T7 LBN_DFF LBN_DFF LBN_DFF LMEC_DFF LAST LAST LONG LOSN LOSN LOSN LOSN LOSN LOSN LOSN LOSN	Number L WORD Number L BYTE Number	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0001 0002 8000 0001 0008 0010 0020 0040 0008 0010 0020 0040 0008 0010 0020 0040 0008 0010 0020 0040 0080 0010 0020 0040 0080 0010 0020 0080 0010 0080 0080 008	CODE CODE CODE CODE CODE CODE CODE CODE	External
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DPD. D_DPD. D_DPD. D_DPD. D_UKN. D_PAS. D_PAS. D_PAS. D_UKN. TO. T1 T15 T2 T3 T4 T5 T4 T5 T6 T7 LBN DFF LBN DFF MEC OFF AST DPD MACCOFF JASS DPD MACCOFF JEBN LDPD MACCOFF JEBN LDPD MACCOFF JESN MPASS DTT JTA LOPD MPASS DTT JTA LOPD MPASS DTT JTA LOPD MPASS	Number L WORD Number L BYTE Number	0005 0002 0008 0001 0004 0006 0007 0000 0001 0002 8000 0001 0020 0008 0010 0020 0008 0010 0020 0008 0010 0020 0008 0008	CODE CODE CODE CODE CODE CODE CODE CODE	
D_AST. The Microsoft MACRO Assembler and Disk Initialization D_BAT. D_BOT. D_DOPO. D_OPO. D_HOM. D_PAS. D_PAS. D_PAS. D_UKN. TO T1 T15. T2 T3 T4 T5 T4 T5 T6 T7LBN. DFFLBN. DFFMEC. MEC. MEC. MEC. MEC. MEC. MEC. MEC.	Number L WORD Number L BYTE L	0005 0002 0008 0001 0004 0006 0007 0000 0001 0002 8000 0001 0002 8000 0008 0010 0020 0008 0010 0020 0008 0008	CODE CODE CODE CODE CODE CODE CODE CODE	External
The Microsoft MACRO Assembler and Disk Initialization D_BAT	Number L WORD Number L NEAR Number	0005 0002 0003 0001 0004 0006 0007 0000 0001 0002 8000 0001 0002 8000 0008 0010 0020 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001 0008 0001	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST. The Microsoft MACRD Assembler and Disk Initialization ID_BAT. ID_DED. ID_BOT. ID_DED. ID_HOM. ID_OSN. ID_PAS. ID_UKN. ITO. ITI ITIS. I	Number L WORD Number L BYTE L	0005 0002 0008 0003 0001 0004 0006 0007 0000 0000 0000 0000 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST The Microsoft MACRO Assembler and Disk Initialization ID_BAT. ID_DED. ID_DED. ID_DED. ID_DED. ID_OPD. ID_HOM. ID_PAS. ID_UKN. ITO. ITI ITIS.	Number L WORD Number L BYTE L	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0004 0008 0010 0008 0009 0008 0008 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST The Microsoft MACRO Assembler and Disk Initialization ID_BAT. ID_DPD. ID_BOT. ID_DPD. ID_HOM. ID_PAS. ID_UKN. ITO. ITI ITIS. ITIS	Number L WORD Number L WORD Number L BYTE L	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0004 0008 0010 0020 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0010 0008 0001	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST The Microsoft MACRO Assembler and Disk Initialization ID_BAT. ID_DBAT. ID_DPD. ID_HOM. ID_OPD. ID_HOM. ID_PAS. ID_UKN. ITO. ITI ITIS. I	Number L BYTE Number	0005 0002 0008 0003 0001 0004 0006 0007 0000 0001 0002 8000 0004 0008 0010 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009 0008 0009	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST The Microsoft MACRD Assembler and Disk Initialization ID_BAT. ID_DBOT. ID_DPD. ID_OPD. ID_OPD. ID_OPS. ID_UKN. ITO. ITI ITIS. ITI	Number L WORD Number L BYTE NUMBER NU	0005 0002 0008 0003 0001 0006 0007 0000 0001 0002 8000 0001 0002 8000 0001 0008 0010 0020 0040 0008 0010 0020 0040 0008 0010 0020 0040 0008 0000 0000 0000 0000 000	CODE CODE CODE CODE CODE CODE CODE CODE	External
ID_AST. The Microsoft MACRD Assembler and Disk Initialization ID_BAT. ID_DBOT. ID_DBOT. ID_DPD. ID_HOM. ID_PAS. ID_UKN. ITO ITI ITIS. IT	Number L WORD Number	0005 0005 0002 0008 0001 0004 0006 0007 0000 0000 0000 0000 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External
D_AST	Number L WORD Number	0005 0002 0008 0001 0002 8000 0001 0002 8000 0001 0008 0010 0008 0010 0008 0010 0008 0001 0008 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External

Microsoft MACRO Assembler Disk Initialization	02-20-84	PAGE	Symbols-3
DFF	Number 0013 Number 0000		
FTN	Number 000C		
	Number 00F0 Number 000E		
	Number 0001 Number 0020		
OST	Number 000B Number 0002		
PON	Number 000A		
FFF	Number 0017 Number 0020		
, , , , , , , , , , , , , , , , , , ,	Number 0040 Number 0040		
EO	Number 0000 Number 0008		
E2	Number 0010 Number 0018		
EDF	Number 0003		
EM	Number 0018 Number 0001		
INIT	L NEAR 002D Number 0068	CODE	Global
11	Number 0069 Number 0020		
· · · · · · · · · · · · · · · · · · ·	Number 0020 Number 0001		
	Number 0061 Number 0000		
K	Number 0050		
[N	L NEAR 0000 L NEAR 0000	CODE	External External
.	Number 0008 Number 0004		
iτ	V BYTE 0000 L NEAR 0000	CODE	External Global
	Number 0000	CODE	5 · - 2 · 1
)2	Number 0001		
1 2	Number 0002 Number 0002		
22	Number 0004 Number 0003		
32	Number 0006 Number 0007		
M2	Number 000E		
T	L NEAR 0000 L NEAR 0000	CODE	External External
C_A	Number 0114 Number 0294		
K	L NEAR 0000 L BYTE 0042	CODE	External Glopal
ER	L WORD OOCF L NEAR OOOO	CODE	External
RR_C	L BYTE OOCF	CODE	
Microsoft MACRD Assembler Disk Initialization			Symbols-4
Microsoft MACRO Assembler Disk Initialization RR_ID	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO	PAGE CODE	Symbols-4 External
Microsoft MACRO Assembler Disk Initialization RR_ID	02-20-84 L BYTE OODO	PAGE	Symbols-4
Microsoft MACRD Assembler Disk Initialization RR_ID	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO	PAGE CODE CODE CODE CODE	Symbols-4 External External
Microsoft MACRO Assembler Disk Initialization RR_ID	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO Number OOEO Number OOAO Number OO10	PAGE CODE CODE CODE CODE	Symbols-4 External External External
Microsoft MACRO Assembler Disk Initialization RR_ID	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO NUMBER OOEO NUMBER OOOO NUMBER OOIO NUMBER OOIO V WORD OOOO L NEAR OOOO	PAGE CODE CODE CODE CODE	Symbols-4 External External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO NUMBER OOEO NUMBER OOLO NUMBER OOLO Y WORD V WORD NUMBER OOOO NUMBER OOOO NUMBER OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE	Symbols-4 External External External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK. TE. TE. TT. TT. TT. TT. TT. TT. TT. TT	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE	Symbols-4 External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOLO NUMBER OOLO V WORD OOOO L NEAR OOOO L NEAR OOOO NUMBER OOOO NUMBER OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE	External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE	External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOLO V WORD OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE CODE	External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF	PAGE CODE CODE CODE CODE CODE	External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOOO	PAGE CODE CODE CODE CODE CODE	External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO NUMBER OOOOO NUMBER OOOOO NUMBER OOOOO NUMBER OOOOO	PAGE CODE CODE CODE CODE CODE	External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK TE TO TAB ST ST ST ST ST ST Microsoft MACRO Assembler DASSembler	L BYTE OOCF O2-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOLO V WORD OOOO NUMBER OOOOO	PAGE CODE CODE CODE CODE CODE	External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE	PAGE CODE	External External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE	PAGE CODE	External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK TE. TEV. TAB. DL EX PPP RK RTS. S. DOFF. DOS. ST OFF. EAD DOFF. ITION FTN PAS. SSIZE	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOLO NUMBER OOLO NUMBER OOOO NUMBER OOOOO	PAGE CODE	External External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	BYTE OOCF	PAGE CODE	External External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF	PAGE CODE	External External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK. FE. FE. FE. FE. FE. FE. FE. FE. FE. FE	L BYTE	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization R_ID K_E E V AB L X P KK IL X IL X IL X IT IT IT IT IT IT IT IT IT	C BYTE OOCF	PAGE CODE	External External External External External External External External External
Microsoft MACRO Assembler Disk Initialization RR_ID SK TE TE TO	L BYTE OOCF 02-20-84 L BYTE OODO L NEAR OOOO L NEAR OOOO L NEAR OOOO NUMBER OOOO	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK TE TE TE TE TE TE TE TE TE TO TO TAB DO TO TAB TO	BYTE OOCF	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID SK TE TT TO TAB DL EX PPP RK RR SD OFF SS TOFF EAD OFF ST	L BYTE	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID SK SK TE TT TT TAB DL EX PPP RK RK RTS SD_OFF SS ST OFF SS ST OFF SS ST	Company Comp	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID SK TE TT TT TAB DL EX PPP RK RK RRS S. D_OFF D_OFF SS ST	L BYTE	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID SK	L BYTE OOCF C2-20-84	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID. SK TE TE TE TE TE TE TE TE TE TO TO TAB DOL EX PP RK RT ST	Color	PAGE CODE CODE	External
Microsoft MACRO Assembler Disk Initialization RR_ID SK TE TT TT TAB DL EX TAB DL EX TO	Company Comp	PAGE CODE	External
Microsoft MACRO Assembler Disk Initialization R_ID. K_ EE EV. AB. AB. IL XY PP KK ITS. OFF ITION FIF ITION FFF ITION FFF	Committee	PAGE CODE CODE	Symbols-4 External External External External External External External External External External

e Microsoft MACRO Assembler d Disk Initialization	02-2	0-84	PAGE	\$ymbo1s-5
EXT	Number	0080		
TRK	Number	0088		
[2E	Number	0200		
G	Number Number	0062 0063		
:2	Number Number	0080		
	Number	0010		
2	Number Number	0010 000F		
	Number Number	0002		
28	Number	0080		
K	Number	0040		
12	Number Number	0020		
R1	Number	0001		
R3	Number	0002 0003		
R4	Number Number	0004		
R6	Number Number	0006 0007		
R8	Number	0008		
R9	Number Number	8000 A000		
RATE	V BYTE Number	0000 000B	CODE	External
RC	Number	000E		
RD	Number Number	OOOD		
RF	Number Number	000F 004C		
sī	L WORD	00D1	CODE	
ST_OFF	Number L WORD	OO2B OODE	CODE	
_\$EC	L BYTE L WORD	02F4 02F2	CODE	Global Global
2	Number	0080		u robe r
	Number Number	0002		
AT	L NEAR Number	025A 0010	CODE	Global Company of the
_OFFSET	Number	0094		
SEG	Number Number	0096 004A		
MP	Number Number	0061		
RR	Number	000D	C 0 D F	
ADB4	L NEAR L NEAR	0281 028E	CODE	
ADB5	L NEAR L NEAR	02EF 026B	CODE	
d Disk Initialization	L NEAR Number	02BE 0008	CODE	
BS	Number Number	000A 0004		
DISKF	Number Number	0000		
ERRC	Number	000C		
LDN	Number Number			
SN	Number Number			
F	V BYTE	0000	CODE	External
ION	V BYTE	0000	CODE	External
ing Severe ers Errors				
0				

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Symbol Cross Reference		(# is d	lefinition)	Cref-	- 1
MBRTC	29#					
LLOC	58#					
AST_OFF	29# 29#					
BADBD	29# 158#	499				
BAT OFF	29#	***				
BAT_SN	157#	498				
BATTN	156# 396	497				
DPB1	427#	445				
HDRV	243	384#				
SHDRV1	393 388#	398#				
SHPAR	242	402 258#				
SHPAR1	265#	282				
SHPAR4	263	273	283#			
BID_AST	267 29#	269	281#			
BID_BAT	29#	511				
BID_BOT	29# 29#					
BID HOM	29#	238				
BID_OSN						
BID_PAS						
BID_UKN	29#					
ито	29#					
BIT1	29# 29#					
BIT2	29#					
SIT3	29#					
BIT4	29# 29#					
IT6	28#					
BIT7	29#					
BK_EC	161#	261 364	365			
BK <u></u>	159#	227	361			
1K_LBN_0FF	29#	360				
BK_MEC		363 362				
BM_AST	147#					
BM_BAT	144#					
BM_DPD	145# 143#	338				
BM_OSN	146#	330				
BM_PAS						
BOOT OFF		87	476			
		٠.				
BSY	29#					
GROUP	29# 12	14	1.4	14	14	
:LRIL	29#	٠-		. •	. •	
CLRSPL	29#					

Symbol	Cross	Re	fer	en	c e					(#isd	efiniti	on)	Cref-2
LTPN									21#	322	543	552	
CMD									39#				
CMDIP									29#				
CMDLEN .									37#				
CODE									12	13#	13	564	
COMPS, .									340	461#			
COMPS1 .									465	468#			
COMPS2 .									462#	467			
COUNT									47#	111	112		
CPM8680.									29#				
CR Crcer									29# 29#	116	127	129	137
TRL Q .									29#				
TRL S .									29#				
TRL_Z									29#				
YLDH									29#				
CYLDL .									29#				
DAMNF									29#				
DATA									29#				
DATARQ .									29#				
DBP									53#	72			
DINIT2 . Dinitx .									9 O	95#			
DINITX . Dinity .									9.3 9.8	100#			
DISKOF .									29#	102#			
DPB_DFF.									74#	400			
DPDE_FLA									29#	266			
DPDE FTN									29#	277			
DPDE_INI									29#	266			
DPDE_LTN									29#	279			
DPDE_LU.									29#				
DPDE_OFF									29#	281			
DPDE_OST									29#	268			
DPDE_PN.									29#				
PPDE_PON									29#				
DPD_OFF.									29#	231	232	233	
DPD_STAR									29#	264			
DRDŸ									29#				
DRDY2 Driveo .									29#				
									29#				
DRIVE1 . Drive2 .									29# 29#				
DRIVEZ .									29#		`		
DRIVEDF.									29#				
DRIVEM .									29#				
DRVSEL .		:	:			•		:	29#				
SK INIT									26	106#			
STATO .									29#	100#			
STAT1 .		:		:		:	:	:	29#				
DWRTF									29#				
DWRTF2 .									29#				
ERRF									29#				
ERROR									29#				
FALSE									29#	29			

ymbol Cross Reference	1	# is de	finiti	on)	Cref	· 3								
TS	50# 54#	447												
TTRK	29# 21#													
PEA	21#	275												
R	29# 29#													
KO	19# 27 29#	386 79	81#											
DO	29# 29#													
ND 12	29# 29#													
AD22	29# 29#													
AD32	29# 29# 29#													
IDS	69# 17#													
IT	70# 17# 29#	83	95											
/EC_A	29# 29# 17#													
'MT	26 150#	92 210	116# 246											
ERR_C	16# 151#	326												
ERR_ID	152# 18# 16#	321 545	333 554	368										
LITEV	16#													
BIT	29# 29#													
NF	29# 19# 18#	111												
PAT	36#	49												
IDEX	29# 29# 29#	116	127	129	137									
EPP	61#													
(TRK	22# 45#	220 109												
DIAID	63# 31# 29#	272	387											
008	29#	268												
YLD_0FF	29#	218												
										-				
		# is de	efiniti	on)	Cref	- 4								
vmbol Cross Reference	26	82	efiniti 96	on) 107	Cref 165#	- 4 271	274					***************************************		
/mbol Cross Reference RY	26 99 155# 29#			•		•	274							
vmbol Cross Reference RV	26 99 155# 29# 29#	82		•		•	274							
/mbol Cross Reference RY	26 99 155# 29# 29#	82 129#		•		•	274							
VMDO1 Cross Reference RV	26 99 155# 29# 29#	82		•		•	274							
VMDO1 Cross Reference RY MSDOS	26 99 155# 29# 29# 29# 29# 29# 29# 29#	82 129# 385 278	96	•		•	274							
VMDO1 Cross Reference RY ASDOS AST AST ASTOPF MHEAD RTITION RTIFTN RT_FTN RT_PAS	26 99 155# 29# 29# 29# 29# 29# 29# 29# 29# 29#	82 129# 385 278 280	96	•		•	274							
VMDO1 Cross Reference RY ASDOS AST AST OFF WHEAD RTITION R	26 99 1559# 29# 29# 29# 29# 29# 29# 29# 29# 29# 2	82 129# 385 278 280	96	•		•	274							
Mbol Cross Reference RY MSDOS AST AST MFEAD MOFF RTITION R	299 # 299 #	82 129# 385 278 280 399	96	•		•	274							
VMDO1 Cross Reference RY ISDOS AST AST AST OFF WHEAD IOFF RTITION RT FTN RT PAS RT SIZE EGOFF ASN BSN BSN BSN BSN BSN BSN BSN	26 99 155# 29# 29# 29# 29# 29# 29# 29# 29# 29# 29	82 129# 385 278 280 399	96	•		•	274							
VMDO1 Cross Reference RY MSDOS AST AST AST MHEAD N_OFF RTITION RTI_TION RTI_TIN RTI_LTN RT_LTN RT_SIZE SE_OFF S_ASN S_ASN S_BSN	26 99 1559# 29# 29# 29# 29# 29# 29# 29# 29# 29# 2	82 129# 385 278 280 399	96	•		•	274	29	29	29	29	25	29	28
VMDO1 Cross Reference RV ASDOS AST AST OFF WHEAD N_OFF RTITION R	299 # 299 #	82 129# 385 278 280 399	96 390 391	107	165#	271			29	29	29	29	29	21
ymbol Cross Reference RY MSDDS	295599 # 299### 299### 299### 299### 299### 299### 299### 299### 299### 299#### 299#### 299#### 299##### 299##### 299###### 299########	82 129# 385 278 280 399	96 390 391	107	165#	271			29	29	29	29	29	21
MDD1 Cross Reference RY MSD0S AST AST MSD0S AST MSD0F MSD0FF MILITION MILITI	299 # 299 #	82 129# 385 278 280 399 222 100 108 29	390 391 29	107	165#	271			29	29	29	29	29	21
VMDO1 Cross Reference RY AST	299 # # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299 # # 299	82 129# 385 278 280 399 222 100 108 29	96 390 391	107	165#	271			29	29	29	29	29	21
VMDO1 Cross Reference RY MSDOS _AST _AST_OFF MHEAD N_OFF RTITION RT_FIN RT_FIN RT_FIN RT_FIN RT_SIZE SE_OFF S_ASN S_ASN S_ASN S_ASN S_BSN S_SSBSN S_SSBSN S_SSSBSN S_SSSSBSN S_SSSSSSSSSS	295599 # ###############################	82 129# 385 278 280 399 222 100 108 29	96 390 391 29	107 29	165#	271			29	29	29	25	29	21
/mbol Cross Reference RY	295599 # ###############################	82 129# 385 278 280 399 222 100 108 29	390 391 29 355 306#	107 29	165#	271			29	29	29	29	29	2 (
VMDO1 Cross Reference RY MSDOS _AST	295 *** * * *** * * * * * * * * * * * * *	82 129# 385 278 280 399 222 100 108 29 320# 342 352 239 315#	390 391 29 355 306#	29 367# 512	29	271			29	29	29	29	29	28
ard Disk Initialization ymbol Cross Reference RY MSDDSAST_OFF _AST_OFF _MHEAD. N_OFF. RIJIION RT_FIN RT_LIN RT_FIN RT_LIN RT_FIN RT_SIZE SS_ASN SS_OFF S_ASN SS_ATN S_BSN S_BSN S_BSN S_BTN ECOMP ECOMP ERAD TR RSAVE BAN SSEC FREAD TR RSAVE BAN SSET SASN SSET SSET SSET SSET SSET SSET SSET SS	295599 9 0999############################	82 129# 385 278 280 399 222 100 108 29 342 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	28
ymbol Cross Reference RY MSDOS	295599 9 09993888888888888888888888888888888	82 129# 385 278 280 399 222 100 108 29 342 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	25
ymbol Cross Reference RY MSDOSASTAST_OFF MHEAD. N_OFF. RTITION	295599 9 09993888888888888888888888888888888	82 129# 385 278 280 399 222 100 108 29 342 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	28
ymbol Cross Reference RY MSDOSASTAST_OFF MHEAD. N_OFF. RTITION RT_FIN	295399 9 09994##############################	82 129# 385 278 280 399 222 100 108 29 342 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	21
VMDO1 Cross Reference RY MSDOS _AST _AST_OFF WHEAD N_OFF RTITION RTI_TION RTI_TION RTI_TIN RT_PAS RTI_SIZE SE_OFF S_ASN S_BSN S	295599 9 099933988992211 222239999999999999999999999999999	82 129# 385 278 280 399 222 100 108 29 342 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	21
ymbol Cross Reference RY MSDDS _AST _AST _AST_OFF MHEAD N_OFF RTITION RT_ITION RT_ITION RT_ITN RT_ITN RT_ITN RT_AS RT_SIZE SE_OFF _ASN SBSN	295399 9 0999##############################	82 129# 385 278 280 399 222 100 108 29 342 352 239 315# 229 88	390 391 29 355 306# 236 206#	29 367# 512	29	271			29	29	29	29	29	21

RHIT	Symbol Cross Reference	1# 15 /	efinition)	Cref-5				
SCYF. 28			,					
121K. 20# 1228	SCTF	29#						
ZEST	7128							
ANTISM	2256							
TATUSW. 400 FERRO 2394 FERRI	2512							
	TART. ,							
TERRI 22	TEPRO							
TEPRA 294 TEPRA 295 TEPRA 295 TEPRA 295 TEPRA 296 TEPRA	TEPRI							
TEPRA 28	TEPR3							
TEPRR 29		29#						
TEPRS 29# TASI 1544 T_ASI 1544 T_ASI 5544 T_BADBS 5514 5544 T_B	TEPRS							
TEPRR 29# TEPRR 29# TEPRA 29# TEPRA 29# TEPRA 29# TEPRA 29# TEPRC 29# TEPRC 29# TEPRC 29# TEPRC 29# TEPRR 29# TEPRR 29# TEPR 29# TEPR 29# TEPR 29# TEPR 29# TEPR 29# TEPR 29# TASI 184# T_ASI 184# T_ASI_OFF 29# TASI_OFF 29# TASI	TEPR7							
TEPRA 22# TAST 154# T_AST	TEPR8							
TEPRATE TEPRA TEPR	TEPRA							
TEPRC	TEPRATE	22# 224						
TEPRD	TEPRB							
TEPR	TEPRD							
TEPR OFF.								
T_AST_OFF 29# EMP								
EMP	T_AST	154#						
RANS	T_AST_OFF	29#						
RANS	EMP	163# 208	259 323	332 346	359	495	516	
RANS	EMP_SEC		562#					
RKO2. 29# RKOE. 29# RUE . 29# 29 NIT . 38# P_BAT . 27 472# GATE 29# INI_OFFSET 29# INI_OFFSET 29# INI_SEG . 29# PC_OFF . 29# 221 PRCMP . 29# PRSNT . 29# T_BADB1 . 500# 505 T_BADB4 . 502 507# T_BADB5 . 514 558# T_BCLP . 529# T_BCLP . 532 BB_BD . 29# BB_BD . 29# BB_BC . 29#	RANS		561#					
NIT	RK02	29#						
NIT	RKOE							
P_BAT 27 472# GATE 29# INI_OFFSET 29# INI_SEG 29# PC_OFF 29# 221 PRCMP 29# PRSNT 29# T_BADB1 500# 505 T_BADB4 500 505 T_BADB5 514 558# T_BADB 504 505# T_BADB 505		29# 29						
GATE. 29# INI_OFFSET 29# INI_SEC 29# PRECMF 29# PRESMF 29# TVERR 29# T_BADB1 500# 505 T_BADB5 514 556# T_BADB5 514 556# T_BADB 493# T_BCLP 529# 532 BB_BC 29# BB_BC 29# BB_BC 29# BB_BC 29# BB_BC 29#	NIT							
INI_OFFSET. 29# INI_SEG . 29# PC_OFF . 29# 221 PRCMP . 29# PNSNT . 29# TVERR . 29# T_BADB1 . 500# 505 T_BADB4 . 502 507# T_BADB5 . 514 556# T_BADB5 . 514 556# T_BADB . 493# T_BCLP . 529# 532 BB_BC . 29# BB_BC . 29# BB_CN . 29#		21 412#						
INI_SEG	GATE							
PC_OFF. 29# 221 PREMF	INI SEG							
PRSNT 29# TUERR	PC_0FF	29# 221						
TYERR . 29# T BADB1	PRCMF							
T BADB1	TVERR							
T_BADBS	T BADB1							
T_BAD_B	T_BADB4							
T_BCLP	T_BAD_B	493#						
BB_BS	T_BCLP	529# 532						
BB_BS	BB BO	29#						
	BB_BS							
	BB_DISKF	29#						
	BB_CN	29#						
	lard Disk Initialization Symbol Cross Reference	[# is a	efinition)	Cref-B				
	R DSN		· · · · · · · · · · · · · · · · · · ·	, -				
Symbol Cross Reference (# is definition) Cref-6	BB_ERRC	29# 29#						
Symbol Cross Reference (# is definition) Cref-6	BB_LDN	29#						
Symbol Cross Reference (# is definition) Cref-6	B_SC	29#						
Symbol Cross Reference (# is definition) Cref-6 BB_DSN	CPRSNT							
symbol Cross Reference (# is definition) Cref-6								

```
The Microsoft MACRO Assembler Character ID for MSDDS 2.90
                                                                                                                                                                              02-20-84
                                                                                                                                                                                                                                   PAGE
                                                                                                                                                                                                                                                                   8 - 2
                                                                                                                                                                                                  PAGE
:
                                                                                                                                                                                                  Device command dispatch tables.
                                                       OOBS R
OOBS R
OOBS R
OOAC R
OOCO R
OOCO R
OOFA R
OOFA R
OOBS R
                                     0000
                                                                                                                                                                 CONTBL: DW
                                                                                                                                                                                                                                   RETURN
                                                                                                                                                                                                                                                                                                  ;0 not used
;1 not used
;2 not used
;3 reserved
;4 read
;5 non-d. read
;6 input status
;7 flush buff
;8 write
;9 write/ver
;10 write stat
;11 not used
;12 not used
                                                                                                                                                                                                                                                                                                                    not used
                                                                                                                                                                                                                                  RETURN
RETURN
CMDERR
CONIN
CONINXDR
CONISTAT
LCONFLUSH
CONOUT
CONOUT
                                     0002
0004
0008
0008
000A
                                                                                                                                                                                                   OOOE
                                                            0114 R
00FA R
00FA R
00B5 R
00B5 R
                                     0010
0012
0014
0016
                                                                                                                                                                                                                                    RETURN
                                     0018
                                                                                                                                                                                                   DW
                                                                                                                                                                                                                                   RETURN
;0 not used
;1 not used
;2 not used
;3 reserved
;4 read
;5 read non-d
;6 not used
                                    001A
001C
001E
0020
                                                            00B5
00B5
00B5
                                                                                                                                                                 CLKTBL:
                                                                                                                                                                                                   DW
DW
                                                                                                                                                                                                                                    RETURN
RETURN
                                                           OOB5 R
OOAG R
OOB5 R
                                                                                                                                                                                                                                  RETURN
CMDERR
CMDERN
BUSY
RETURN
RETURN
SETTIME
SETTIME
RETURN
RETURN
RETURN
RETURN
                                                                                                                                                                                                   DW DW DW DW DW DW DW
                                     0022
                                     0024
0026
0028
002A
                                                                                                                                                                                                                                                                                                   ;6 not used
;7 not used
;8 write
;9 write/ver
;10 not used
;11 not used
;12 not used
                                     002C
002E
0030
0032
0034
0036
0038
003A
003C
003E
                                                                                                                                                                                                   DW
                                                                                                                                                                                                                                                                                                  ;12 not used

;0 - Initialize Driver.
;1 - Return current media code.
;2 - Get Bios Parameter Block.
;3 - IOCTL
;4 - read.
;5 - Non-d input
;6 - Return status. (Not used)
;7 - Flush input buffer. (Not used.)
;8 - write.
;9 - Block write with verify.
;10 - Return output status.
;11 - Flush output buffer. (Not used.)
;12 - IOCTL
                                                                                                                                                                 XCOM_TBL:
68977777777788
689777777778901
                                                          004E R
0085 R
0085 R
0193 R
0146 R
                                                                                                                                                                                                   DW
                                                                                                                                                                                                                                   CINIT
RETURN
XC_IOCTL
XC_IN
XC_IN
NETURN
RETURN
XC_OUT
XC_OUT
XC_OSTAT
RETURN
                                                                                                                                                                                                                                   CINIT
                                                            004E
0085
0085
0193
0146
016F
                                                                                                                                                                                                   ************
                                                             00B5
                                     0042
0044
0046
0048
004A
                                                            0085
0159
0159
                                                            00B5
                                                                                                                                                                                                                                    XC_IOCTL
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                    02-20-84
                                                                                                             PAGE
                                                                                                                             8 - 3
PAGE
                 OO4E
OO4E 80 3E 0137 R 01
OO53 75 03
OO55 E9 0000 E
OO58 C3
                                                                              CINIT:
                                                                                              CMP
JNZ
JMP
                                                                                                             BYTE PTR XC_DEV, 1
CINIT1
INITCOM
                                                                                                                                                            ; check first time
                                                                                                                                                            ; only init once
                                                                              CINIT1: RET
                                                                                              IO device BIOS interface
                 0059
0059
005F
                                                                              AUX2:
                             2E: C6 O6 O137 R O3
EB OE
                                                                                              MOV
                                                                                                             BYTE PTR CS:XC_DEV,3
SHORT XCOMM
                                                                                                                                                            ; device extended comm port
                                                                                              JMP
                  0061
                                                                              PRN:
                  0051
0057
0059
0059
                            2E: C6 O6 O137 R O2
EB O6
                                                                                              MD V
JMP
                                                                                                             BYTE PTR CS:XC_DEV,2
SHORT XCOMM
                                                                                                                                                            ; device printer port
                                                                              AUX:
                             2E: C6 06 0137 R 01
                                                                                              MOV
                                                                                                             BYTE PTR CS:XC_DEV,1
                                                                                                                                                            ; device comm port
                                                                              X C D MM ·
                 008F
0070
0073
                             56
BE 0034 R
EB 0A
                                                                                                             SI
SI,OFFSET XCOM_TBL
SHORT ENTER
                                                                                              PUSH
                                                                                              MOV
JMP
                 0075
0076
0078
                           56
BE 001A R
EB 04
                                                                                                             SI
SI,OFFSET CLKTBL
SHORT ENTER
                                                                              CLK:
                                                                                              PUSH
                                                                                              MO V
JMP
                 007B 56
007C BE 0000 R
                                                                              CON:
                                                                                              PUSH
                                                                                                             SI, OFFSET CONTBL
SHORT ENTER
                                                                                              MOV
                                                                              ;
                                                                                              JMP
                                                                                              Common command dispatch. Pick up useful things and dispatch to the right routine within the table.
                                                                           ENTER:
DISPATCH PROC FAR
PUSH A
PUSH B
PUSH C
PUSH D
PUSH D
PUSH C
PUSH C
                  007F
                 007F
007F
0080
                           50
53
51
52
57
1E
06
                                                                                                             AX
BX
CX
DX
DI
DS
ES
                  0081
                 0083
0084
0085
                OO86 2E: C5 1E 0000 E
008B C7 47 03 0100
0090 8B 4F 12
0093 C4 7F 0E
0096 8A 47 02
0098 98
009A 03 F0
009C 03 F0
                                                                                                             BX,CS:PTRSAVE ; get data pkt,
WORD PTR [BX.STATUS],100H ; set done bit
CX,[BX.COUNT] ; CX = count
DI,[BX.TRANS] ; ES:DI = buffer addr
AL,[BX.CMD] ; AL = command
; make it word
SI,AX ; add offset
SI AY
                                                                                             LDS
MOV
MOV
130
131
132
                                                                                              LES
                                                                                              MOV
CBW
ADD
ADD
133
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                             02-20-84
                                                                                                                          PAGE
                                                                                                                                           8 - 4
                   009E
009F
00A0
1378
1398
140
141
144
144
144
144
147
155
155
155
155
156
166
162
166
                               OE
1F
FC
FF 14
O7
5F
5A
59
5B
5B
5E
CB
                                                                                                        PUSH
POP
CLD
CALL
POP
POP
POP
POP
POP
POP
                                                                                                                          CS
DS
                                                                                                                                                                              ; DS = CS
                                                                                                                                                                              ; set forward direction ; perform func
                   00A1
00A3
00A4
00A5
                                                                                                                          WORD PTR [SI]
ES
DS
                                                                                                                          DI
                                                                                                                          DX
CX
BX
AX
SI
                   00A6
00A7
00A8
00A9
00AA
                   OOAC
                                                                                       DISPATCH ENDP
                                                                                       :
                                                                                                        set command error.
                  OOAC C5 1E OOOO E
OOBO 81 4F 03 8003
OOB5 C3
                                                                                       CMDERR: LDS
OR
RETURN: RET
                                                                                                                          BX,PTRSAVE
WORD PTR [BX.STATUS],8003H
                                                                                       ;
                                                                                                        Set BUSY.
                   OOB6 C5 1E OOOO E
OOBA 81 4F O3 0200
OOBF C3
                                                                                                        LDS
OR
Ret
                                                                                       BUSY:
                                                                                                                          BX,PTRSAVE
WORD PTR [BX.STATUS],200H
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                                          02-20-84
                                                                                                                                           PAGE
                                                                                                                                                              8 - 5
Non destructive console read. If char is available, return it, else return busy set.
                                                                                                    ;
                                                                                                  CONINXDR:

MOV
INT
AND
JZ
MOV
MOV
MOV
CBW
ADD
MOV
LDS
MOV
RET
                      0000
                                    BF 0004
CD 18
22 CB 9
74 ED 88 EE00
8E EO0
BE 0FB8
26: 8A 44 14
98
03 F0
25: 8A 04
CS 3E 0000 E
88 45 0D
C3
                                                                                                                                         DI.4 ; Level 2 co....

ROM
CI.CL ; Check any char
BUSY ; None - return busy
AX, DFFSET SYSRAM ; Set SYSRAM Segment
ES.AX
SI.OFFSET ESCSTR+1 ; String addr
AL.ES:BYTE PTR ESCOFF[SI] ; get string index
; make it word
                      00C0
00C3
00C5
00C7
00C8
00CC
00CE
00D1
00D5
00D8
00D8
                                                                                                                                          SI,AX
AL,ES:[SI]
DI,PTRSAVE
[DI.MEDIA],AL
                                                                                                                                                                                                      ; get char from buffer
; get pkt ptr,
; return it
                                                                                                   :
                                                                                                                       console input status
                     00E3
00E3
00E6
00E8
00EA
                                                                                                   CONISTAT:
MOV
INT
                                    BF 0004
CD 18
22 C9
74 CA
C3
                                                                                                                                           DI,4
ROM
CL,CL
BUSY
                                                                                                                                                                                                      ; Level 2 console status
                                                                                                                                                                                                      ; Check any char
; None - return busy
                                                                                                                       JZ
RET
                                                                                                                       console input
                                                                                                   ;
                     00ED
00EE
00EF
00F0
00F3
00F4
00F5
00F6
                                  05
57
51
E8 010A R
58
5F
07
AA
E2 F4
                                                                                                   CONIN:
                                                                                                                       PUSH
PUSH
CALL
POP
POP
STOSB
LOOP
RET
                                                                                                                                           ES
                                                                                                                                           DI
CX
LCONIN
                                                                                                                                           CX
                                                                                                                                           CONIN
                                                                                                                       Write
                      OOFA
OOFB
OOFC
OOFE
OOFE
OOFF
O1OO
                                                                                                    CONOUT:
                                  06
1F
8B F7
                                                                                                                       PUSH
                                                                                                                                           ES
                                                                                                                                           DS
SI,DI
                                                                                                                       MOV
                                                                                                                                                                                   ; get pointer into DS:DI
                                                                                                    CONDUT1:
                                    51
56
AC
33 FF
                                                                                                                       PUSH
PUSH
                                                                                                                                           C X
S I
                                                                                                                       LODSB
                                                                                                                                                                                   ; get output char
; do console output
                                                                                                                                           DI.DI
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                                                                                                                                                                                                                02-20-84
                                                                                                                                                                                                                                                                                                                                                                      PAGE
                                                                                                                                                                                                                                                                                                                                                                                                                       8 - 6
                                                         0103 CD 18
0105 5E
0106 59
0107 E2 F5
0109 C3
                                                                                                                                                                                                                                                                                                                   INT
POP
POP
LOOP
RET
                                                                                                                                                                                                                                                                                                                                                                      RDM
SI
CX
CONOUT 1
                                                                                                                                                                                                                                                                                                                   CONSOLE input, wait if not any
                                                                                                                                                                                                                                                                                                                                                                      AL = character
ALL
                                                          010A
010A
010D
010F
0111
                                                                                                                                                                                                                                                                LCONIN:
                                                                                             8F 0002
CD 18
22 C9
74 F7
C3
                                                                                                                                                                                                                                                                                                                  MOV
INT
AND
JZ
RET
                                                                                                                                                                                                                                                                                                                                                                    DI,2
ROM
CL,CL
LCONIN
                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ; check status
; wait
                                                                                                                                                                                                                                                                                                                   Flush the console input buffer.
                                                          0114
0114
0117
0119
0118
011D
                                                                                                                                                                                                                                                               LCONFLUSH:
MOV
INT
AND
JNZ
RET
                                                                                            BF 0002
CD 18
22 C9
75 F7
C3
                                                                                                                                                                                                                                                                                                                                                                    DI,2
ROM
CL,CL
                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ; level 2 console in
                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ; any character?
; keep flush if still have character
                                                                                                                                                                                                                                                                                                                                                                      LCONFLUSH
                                                                                                                                                                                                                                                                                                                  Output a character from AL to the console.
                                                                                                                                                                                                                                                                                                                  USE: none
                                                         011E

011E

011F

012D

012D

012D

0121

0121

0122

0123

0123

0125

0125

0126

0126

0128

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0120

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                                                                                                                                                                                                                                                                LCONOUT:
                                                                                                                                                                                                                                                                                                                  AX
BX
CX
DX
SI
DI
BP
ES
DI, DI
ROM
ES
BP
DI
SI
                                                                                                                                                                                                                                                                                                                                                                      DX
CX
BX
AX
                                                                                                                                                                                                                                                                                                                   fast console output interrupt service routine
```

```
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                                                                                                              PAGE
                                                                                                                              8 - 8
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                              PAGE
ï
                                                                                              AUX, PRN, & AUX2 common routines
                  0137 00
                                                                               XC_DEV DB
                                                                                                            ۰
                                                                               :
                                                                                              comm output status
                  0138
0138 8A 2E 0137 R
013C E8 0000 E
013F 22 C0
0141 75 15
0143 E9 0086 R
                                                                               XC_OSTAT:
                                                                                              MOV
CALL
AND
JNZ
                                                                                                              CH,XC_DEV
OUTSTAT
AL,AL
XC_IN2
BUSY
                                                                                                                                                             ; get device #
                                                                                              JMP
                                                                                              comm input character
                                                                              :
                  0146
0146
0147
0148
0149
0140
0150
                           57
51
08
8A 2E 0137 R
E8 0000 E
8A C1
07
                                                                               XC_IN:
                                                                                              PUSH
                                                                                                              DI
                                                                                              PUSH
PUSH
PUSH
MOSY
CALL
MOY
POP
POP
STOSB
                                                                                                              DI
CX
ES
CH,XC_DEV
GETCHAR
AL,CL
                                                                                                                                                             ; get device #
; go to get character
                  0152 07
0153 07
0154 5F
0155 AA
0156 E2 EE
0158
0158 C3
                                                                                                                                                              ; save in buffer
                                                                                                              XC_IN
                                                                               XC_IN2:
                                                                                              RET
                                                                                               comm output character
                  0159
0159
28: 8A 05
015C 47
015D 57
015E 06
015F 51
0160 8A D0
0182 8A 2E 0137 R
0186 E8 0000 E
0189 59
016A 07
018B 5F
018C C3
                                                                               XC_OUT:
                                                                                              MOV
                                                                                                               AL . ES : [ D I ]
                                                                                                                                                             : get character
                                                                                                              AL,ES:[DI
DI
ES
CX
DL,AL
CH,XC_DEV
PUTCHAR
                                                                                               INC
PUSH
PUSH
PUSH
MOV
                                                                                               CALL
POP
POP
LOOP
                                                                                                              CX
ES
DI
XC_OUT
                                                                                               RET
                                                                              ;
XC_NDIN:
MDV
CALL
JNZ
                                                                                              comm non destructive input
                  O18F
O18F 8A 2E 0137 R
O173 E8 0184 R
O176 75 09
                                                                                                              CH, XC_DEV
COM_NDIN
XC_NDIN1
                                                                                                                                                           ; get device #
; get it
; check if any input
 The Microsoft MACRO Assembler Character ID for MSDOS 2.00
                                                                                     02-20-84
                                                                                                               PAGE
                  LES
MOV
Ret
                                                                                                               DI,PTRSAVE ; get packet pointer
BYTE PTR ES:[DI.MEDIA],CL ; save character in packet
 334
335
336
337
338
349
341
342
343
344
345
346
347
348
350
                                                                               XC_NDIN1:
JMP
                                                                                                               BUSY
```

Non-destructed input

INSTAT

AL, OFFH XC_NDIN1 SI, ES: RBOUT[BX] CX, ES: O[SI]

; check input status ; any character? ; return if no ; get buffer address ; get character and status

COM_NDIN: CALL CMP JNZ MOV MOV

COM_NDIN1:

0184 0184 E8 0000 E 0187 3C FF 0188 75 F6 0188 26: 88 77 06 018F 26: 88 0C 0192 0192 C3

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                                                   02-20-84
                                                                                                                                                       PAGE
                                                                                                           PAGE
XCIOCP STRUC
351
XC_FUN DB
XC_RC DB
XC_CHAR DB
XC_STAT DB
XC_BUF DB
                         0000 00
                                                                                                                                                      0
                                                                                                                                                                                                 ; function #
                                                                                                                                                                                                  ; return code
; character
; character status
; CCB or buffer
                         0001
                                         00
                         0003
                        0005
                                                                                                           XCIOCP ENDS
                                                                                                                                 XC_IOCTL
                                                                                                                                 ENTRY: ES:DI = packet pointer
                                                                                                                                 byte 0 = function #
byte 1 = return code
byte 2 = character
byte 3 = character status
byte 4 to byte n = buffer
                        O193
O193 8A 2E O137 R
O197 BB OOO4
O19A O3 DF
O19C 8C C2
                                                                                                           XC_IOCTL:
                                                                                                                                                                                                                      ; CH = device #
; BX = buffer address
; add packet offset
; DX = segment
                                                                                                                                  MOV
MOV
373
374
375
376
377
378
379
380
381
382
383
                                                                                                                                                      CH,XC_DEV
BX,OFFSET XC_BUF
BX,DI
DX,ES
                                                                                                                                  ADD
                                                                                                                                 MOV
                        019E
01A1
01A2
01A5
01A7
                                       26: 8A 05
98
BE 01AB R
03 F0
03 F0
FF 24
                                                                                                                                 MOV
CBW
MOV
                                                                                                                                                                                                                      ; get function #
; make it word
; get offset
; add word offset
                                                                                                                                                       AL,ES:[DI]
                                                                                                                                                      SI,OFFSET XIOC_TBL
SI,AX
SI,AX
                                                                                                                                  ADD
                                                                                                                                  ADD
                                                                                                                                                       WORD PTR [SI]
                                                                                                                                                                                                                       ; perform function
384
385
386
387
388
                       O1AB
O1AB O1DF R O1EC R O1EF R
O1F2 R
O183 O1F8 R O1FE R O201 R
O189 O208 R O216 R O221 R
O1C5 O243 R O235 R O235 R
O1C5 O243 R O244 R O251 R
O1C5 O254 R O257 R O260 R
O1D1 O263 R O26D R O270 R
O1D7 O278 R O278 R O280 R
O1DD O283 R
                                                                                                           XIOC_TBL:
                                                                                                                                                       FUNO.FUN1.FUN2.FUN3
                                                                                                                                                      FUN4, FUN5, FUN6
FUN7, FUN8, FUN9
FUN10, FUN11, FUN12
FUN13, FUN14, FUN15
FUN16, FUN17, FUN18
FUN19, FUN20, FUN21
FUN22, FUN23, FUN24
FUN25
                                                                                                                                 DW
389
390
391
392
393
394
395
                                                                                                                                  DW
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                      02-20-84
                                                                              PAGE
               01DF
01DF
01E1
01E2
01E3
01E6
01E6
01E7
FUNO:
                       8B CB
06
57
E8 0000 E
                                                                                           CX,BX
ES
DI
                                                                               MOV
                                                                               PUSH
                                                                               PUSH
                                                                              CALL
                                                                                           PRG7201
                                                                 FUN_RC:
                      5F
1F
88 45 01
C3
                                                                              POP
POP
                                                                                           [DI.XC_RC],AL
                                                                               MOV
                                                                                                                                 : return function code
                                                                               RET
                                                                 FUN1:
               01EC
01EF
01EF
01F2
01F2
01F4
01F5
01F6
01F8
                        E9 0000 E
                                                                               JMP
                                                                                           DFLT7201
                                                                 FUN2:
                        E9 0000 E
                                                                               JMP
                                                                                           DFLTBUF
                                                                 FUN3:
                        88 CB
06
57
E8 0000 E
EB EB
                                                                                           CX, BX
ES
DI
                                                                              MOV
                                                                              PUSH
PUSH
Call
JMP
                                                                                            RDSETUP
                                                                                           SHORT FUN_RC
                                                                 FUN4:
                        E9 0000 E
                                                                               JMP
                                                                                           RCVENA
                                                                 FUNS:
                        E9 0000 E
                                                                              JMP
                                                                                           RCVDIS
               0201
                                                                 FUN6:
               0201
                        0.6
                                                                              PUSH
                                                                                           ES
               0202
0203
0205
0208
0208
0208
                        57
E8 0000 E
EB DE
                                                                              PUSH
CALL
JMP
                                                                                           DI
INSTAT
SHORT FUN_RC
                                                                 FUN7:
                                                                              PUSH
                                                                                           ES
                                                                                           DI
INCHAR
               0208
020A
020D
020D
020E
020F
                         E8 0000 E
                                                                               CALL
                                                                 FUN_CHAR
                                                                              POP
POP
MOV
                        5 F
1 F
                                                                                           DΙ
                                                                                           DS
[DI.XC_RC],AL
WORD PTR [DI.XC_CHAR],CX
                        88 45 01
89 4D 02
C3
                                                                                                                                              ; return function code
; return char and status
              020F
0212
0215
0216
0216
                                                                              MOV
RET
                                                                 FUNS:
                                                                              PUSH
                                                                                           ES
                                                                               PUSH
              0217
0218
0218
0218
021C
021D
                        E8 0000 E
                                                                              CALL
                                                                                           GETCHAR
                                                                 FUN_AX:
                                                                              POP
                                                                                           DI
                                                                               POP
                                                                                           DS
                         89 45 02
                                                                              MOV
RET
                                                                                           WORD PTR [DI.XC_CHAR],AX
                                                                                                                                           ; return char and status
                        C3
                                                                              PUSH
                                                                                           ES
DI
               0222
```

	icrosoft MACRO Assembler er IO for MSDOS 2.00	02-20-84	PAGE 8-12	
5 1 5 2	0223 E8 0000 E 0226 EB BE	CALL JMP	OUTSTAT Short fun_rc	
5 3	0228	FUN10:	-	
5 4	0228 26: 8A 55 02	MOV	DL,ES:[DI.XC_CHAR]	; get char
5	022C 06 022D 57	PUSH Push	ES DI	
5 7	022E E8 0000 E	CALL	OUTCHAR	
5 8	0231 8A C1	MOY	AL,CL	; get return code
5 9 6 0	0233 EB B1 0235	JMP FUN11:	SHORT FUN_RC	
6 1	0235 26: 8A 55 02	MOV	DL,ES:[DI.XC_CHAR]	
62	0239 E9 0000 E	JMP	PUTCHAR	
63 64	023C 023C 26: 8A 55 02	FUN12:	DL,ES:[DI.XC_CHAR]	
65	0240 E9 0000 E	JMP	OUTNOW	
6 6	0243	FUN13:		
67 68	0243 06 0244 57	PUSH PUSH	ES D I	
69	0245 E8 0000 E	CALL	RDMODEM	
70	0248 EB D1	JMP	FUN_AX	
71	024A	FUN14:		
72 73	024A 06 024B 57	PUSH PUSH	ES Di	
74	024C E8 0000 E	CALL	SETMODEM	
75	024F EB 95	JMP	SHORT FUN_RC	
76 77	0251 0251 E9 0000 E	FUN15: Jmp	BRKON	
78	0251 E9 0000 E	FUN16:	BRKUN	
79	0254 E9 0000 E	JMP	BRKOFF	
80	0257	FUN17:	-v -v	
8 1 8 2	0257 8B CB 0259 06	MOV Push	CX,BX ES	
83	025A 57	PUSH	DI	
8 4	025B E8 0000 E	CALL	RCVINT	
85 86	025E EB 86 0260	JMP Fun18:	SHORT FUN_RC	
87	0260 E9 0000 E	JMP	RCVCANCEL	
8.8	0263	FUN19:		
89	0263 8B CB 0265 06	MOV Push	CX, BX ES	•
91	0266 57	PUSH		
92	0267 E8 0000 E	CALL	XMTMTY	
93	026A E9 01E6 R	JMP	FUN_RC	
9 4 9 5	026D 026D E9 0000 E	FUN20: JMP	XMTCANCEL	
96	0270	FUN21:		
97	0270 8B CB	MOV	CX,BX	
9 8 9 9	0272 E8 0000 E 0275 E9 01E6 R	CALL JMP	HACK7201 Fun_rc	
00	0275 ES 01ES K	FUN22:	, 5%_KC	
01	0278 E9 0000 E	JMP	VECT7201	
02 03	027B 027B 8B CB	FUN23:	CX,BX	
04	027D E9 0000 E	JMP	STATCHG	
05	0280	FUN24:		
The M	icrosoft MACRO Assembler ter IO for MSDOS 2.00	02-20-84	PAGE 8-13	
o 6	0280 E9 0000 E	JMP	STCANCEL	
06 07	0280 E9 0000 E	FUN25:	SILAMUEL	
08	0283 06	PUSH		
10	0284 57 0285 E8 0184 R	PUSH		

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The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                      02-20-84
                                                                                                                 PAGE
512
513
514
515
516
517
518
519
520
                                                                                                PAGE
                                                                                                Time and Date Data Area.
Initialized time and date.
                                                                                ;***** don't change the order
DAYS DW 1096+31+28+31+30+31+30+31+31+30 ; Dct 1, 1983
MINUTES DB 0
HOURS DB 0
                  028A 0559
028C 00
028D 00
                                                                                MINUTES DS O
TICKS_H DB O
SECONDS DB O
TICKS_L DB O
TICKS_L DB O
;****** don't change the order
                              00
521
522
523
524
525
                  028E
028F
                                                                                                                                                 ; high byte for 0.01 sec
                                                                                                                DB
DB
DW
DB
                                                                                                                                 60
60
426
-3
526
527
528
529
530
                  0291 3C
0292 3C
0293 01AA
0295 FD
                                                                                TICKER
CLK_60_50
CLK_16_20
CLK_ADJ
                                                                                                                                                 ;1/80 or 1/50 counter
;80/50 Hz constant set by ioinit
;1/60.0502*258*100:426 or 1/49.9485*256*100:512
;(60-80.052)*80:-3 or (50-49.9465)*80:+3
531
532
533
534
535
536
                                                                                                SETTIME - set time
                                                                                                ENTRY: ES:DI = time source buffer
                  0296
                                                                                SETTIME:
                  0296
0299
029A
029B
029C
                            BE 028A R
1E
06
537
538
540
541
542
544
544
544
547
548
555
553
553
                                                                                                 MOV
                                                                                                                 SI, OFFSET DAYS
                                                                                                                 DS
ES
DS
                                                                                                 PUSH
                                                                                                                                                : XCHG DS.ES
                                                                                                 PUSH
POP
POP
                              07
87 F7
                                                                                                 XCHG
                                                                                                                 SI.DI
                  029 D
029 F
029 F
02A 2
02A 4
02A 5
                                                                                SETTIME 1
                              B9 0003
F3/ A5
OE
1F
                                                                                                  :
Mnv
                                                                                                                 CX,3
MDVSW
CS
DS
                                                                                                 REP
PUSH
                                                                                                                                                 ; RESTORE DS:
                                                                                                  POP
                             8A OE 0292 R
88 OE 0291 R
C3
                                                                                                  MOV
                                                                                                                  CL, CLK_60_50
                                                                                                                                               ; RESET COUNTER/TIMER
                  OZAA
OZAE
                                                                                                 MOV
                                                                                                 RET
                                                                                                 GETTIME - get time
                                                                                                ENTRY: ES:DI = time destination buffer
555
556
557
558
559
560
561
                  02AF
02AF
02B2
02B5
                                                                                 GETTIME:
                             BE 028A R
B9 0003
F3/ A5
C3
                                                                                                                 SI, OFFSET DAYS
                                                                                                 MOV
                                                                                                                 CX,3
MOVSW
                                                                                                 REP
```

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                                                                                                             02-20-84
                                                                                                                                             PAGE
                                                                                                                          PAGE
562
563
564
565
566
567
568
570
571
                                                                                                                          clock interrupt service. We have a 16.66 ms clock.
                                                                                                      ;
                      0288
0288 1E
0289 0E
028A 1F
                                                                                                      CLKISR:
                                                                                                                           PUSH
                                                                                                                                               DS
                                                                                                                                                                                      ;DS = CS
                                                                                                                          PUSH
                                                                                                                          NOTE: the 60/50 ticks/second constant(CLK_16_20) and adjustment value (CLK_ADJ) must be set by ioinit, default is 60 Hz.
0288 50
028C 53
028D 8A 26 028E R
02C1 AO 0290 R
02C4 03 06 0293 R
02C6 88 26 028E R
02CC A2 0290 R
                                                                                                                                              AX
BX
AH, TICKS_H
AL, TICKS_L
AX, CLK_16_20
TICKS_H, AH
TICKS_L, AL
                                                                                                                          PUSH
PUSH
MOV
MOV
                                                                                                                                                                                       ; get ticks high byte
; get ticks low byte
; add constant for 0.01 sec
                                                                                                                           ADD
                                                                                                                           MOV
                                                                                                                           MOV
                                                                                                                                              TICKER
CLKRET
AL, DSKTMR
AL
                      02CF
02D3
02D5
                                                                                                                          DEC
JNZ
MDV
                                                                                                                                                                                      ; count down ticker
; time yet?
; do disk timeout processing
                                   FE OE 0291 R
75 4E
                      O2D3 75 4E
02D5 AO OOOO E
02D8 FE C8
02DA 78 03
02DC A2 0000 E
02DF AO 0292 R
02E2 A2 0291 R
02E3 B8 003C
02E8 88 26 028E R
                                                                                                                          MOV
MOV
MOV
                                                                                                                                                                                       ; jump if timer already expired
; set new value
; get 60/50 Hz constant
; reset the ticker
; AH=0, AL=60 used as constant
; reset 0.01 sec count to 0
                                                                                                                                               DSKTMR, AL
AL, CLK_60_50
TICKER, AL
                                                                                                                                               AX, 60
TICKS_H, AH
                                                                                                                           MOV
                                                                                                                           MOV
                                   FE 05 028F R
38 06 028F R
72 2D
88 26 028F R
8A 1E 0295 R
28 1E 0291 R
FE 06 028C R
38 06 028C R
72 17
88 26 028C R
80 3E 028D R
80 3E 028D R
80 3E 028D R
80 3E 028D R
88 26 028D R
88 26 028D R
                      02EC
02F0
02F4
                                                                                                                          I N C
                                                                                                                                               SECONDS
SECONDS, AL
                                                                                                                                                                                       ; AL=60
                                                                                                                           JB
                                                                                                                                               CLKRET
                                                                                                                          MOV
MOV
SUB
INC
CMP
                                                                                                                                               SECONDS, AH
BL, CLK_ADJ
TICKER, BL
MINUTES
                       02F6
02FA
02FE
0302
0306
                                                                                                                                                                                       ; SECONDS = O
                                                                                                                                                                                       ; get adjust value
; adjust ticker every minute
                                                                                                                                                MINUTES, AL
                                                                                                                                                                                       ; AL=60
                                                                                                                                               CLKRET
MINUTES, AH
HOURS
HOURS, 24
CLKRET
                                                                                                                          JB
MOV
INC
CMP
                       AOEO
                      030A
030C
0310
0314
0319
031B
031F
0323
0323
                                                                                                                                                                                       ; MINUTES = O
                                                                                                                            JB
                                                                                                                           MDV
                                                                                                                                                                                       ; HOURS=0
                                                                                                                                               HOURS, AH
                                    CD 64
58
                                                                                                                           INT
                                                                                                                                               PROFILE
                                                                                                                                                                                       ; for user interface
610
611
612
613
                                                                                                                                               BX
AX
DS
                                                                                                                           POP
                                    5 8
1 F
C F
                                                                                                                            POP
                       0327
                                                                                                      POP
INTRET: IRET
```

```
The Microsoft MACRO Assembler Character IO for MSDOS 2.00
                                                                                              02-20-84
                                                                                                                           PAGE
                                                                                                                                         8-16
                                                                                                         PAGE
type bx as hex, followed by a space.
                   0329 8A C7
0328 E8 0339 R
032E 8A C3
0330 E8 0339 R
0333 B0 20
0335 E8 011E R
0338 C3
                                                                                                                           al,bh
out2
al,bl
out2
al,',
lconout
                                                                                        outhex: mov
call
mov
call
mov
call
ret
                                                                                                                                                              ; most first
                                                                                                                                                               ; do least
                                                                                                          type al as hex.
                   0339 50
033A 51
033B B1 04
033D D2 E8
033F 59
0340 E8 0344 R
0343 58
                                                                                                                           ax
cx
c1,4
a1,c1
cx
out1h
ax
                                                                                                         push
push
mov
shr
pop
call
pop
                                                                                         out 2 :
                                                                                        ;
                                                                                                          type isnybble of al.
                                                                                                         and
or
cmp
jb
add
call
ret
                                                                                                                           al, ofh
al, '0'
al, '9'+1
out1
al, 'a'-'9'-1
lconout
                   O344 24 OF
O346 OC 30
O348 3C 3A
O34A 72 O2
O34C O4 27
O34E E8 O11E R
O351 C3
                                                                                                                                                              ;only 1s 4 bits
; make ascii
                                                                                         out 1h:
                                                                                                                                                              ; 0-9,a1-f
                                                                                         out 1:
                                                                                        ;
                                                                                                          Type a null terminated string.
                   0352 2E: 8A 07
0355 43
0356 3C 00
0358 74 F7
035A E8 011E R
035D E8 F3
                                                                                                         mov
inc
cmp
je
call
jmp
                                                                                                                            a1,cs:[bx]
                                                                                        pstr:
                                                                                                                           al,cs:[i
bx
al,0
ohr
lconout
pstr
                                                                                                          ENDS
END
                    035F
                                                                                        CODE
```

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T1	Number 0002 Number 0004		
T3	Number 0008 Number 0010		
T5	Number 0020 Number 0040		
T7	Number 0080 Number 00FF		
KERR	Alias BIT7 L NEAR 0000	CODE	External
KON	L NEAR 0000 Number 0043	CODE	External
SY	L NEAR OOBS Number 0003	CODE	
LEN	Number 0011 Number 0001		
PRTY	Number 0000 Number 0004		
RCVB	Number 000D Number 0005 Number 0009		
RXOF	Number 000B L BYTE 0000	CODE	
STPB	Number 0002 Number 0006		
TXOF	Number 000A Number 0008		
XDN	Number 0007 L NEAR 004E	CODE	
NIT1	L NEAR 0058 L NEAR 0075	CODE	Global
K16X	Number 0040 L NEAR 0288	CODE	Global
KRET	L NEAR 0323 L NEAR 001A	CODE	
K_16_20	L WORD 0293 L BYTE 0292	CODE	Global Global
K_ADJ	L BYTE 0295 Number 002C L NEAR 00AC	CODE	Global
MRMA	Number 0043 L NEAR 0184	CODE	
M_NDIN	L NEAR 0192 L NEAR 007B	CODE	G 1 o b 4 1
NINXDR	L NEAR OOED L NEAR OOCO	CODE	
NISTAT	L NEAR OOE3 L NEAR OOFA	CODE	
NOUT1	L NEAR OOFE L NEAR OOOO	CODE	
17201	Number 0001 Number 0015		
1TXBE	Number 0002 Number 0002		
27201	Number 0010		
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he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3	02-20-84 Number 0003 Number 0005 Alias BIT3 Number 0006 L WORD 028A Number 0011 Number 0013 Number 0003 Number 0003 Number 0003 Number 00003	CODE	
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he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3. 4	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0006 L WORD 028A Number 0011 Number 0013 Number 0003 Number 0003 Number 0003 L NEAR 0000 L NEAR 0000 F PROC 0075	CODE CODE CODE CODE	External External Length = 002D
he Microsoft MACRO Assembler aracter ID for MSDOS 2.00 3. 4	02-20-84 Number 0003 Number 0004 Number 0005 A1\as B1T3 Number 0006 L WORD 028A Number 0011 Number 0011 Number 0001 Number 0001 Number 0003 L NEAR 0000 L NEAR 0000 F PROC 0075 V BYTE 0000 A1\as B1T2	CODE	External External
he Microsoft MACRO Assembler aracter ID for MSDOS 2.00 3	02-20-84 Number 0003 Number 0004 Number 0005 A1 1 as 81 T3 Number 0006 L WORD 028A Number 0011 Number 0011 Number 0001 Number 0002 Number 0002 Number 0002 Number 0005 L NEAR 0000 F PROC 007F Number 0055 V BYTE 0000 A1 ias BIT2 A1 ias BIT2 Number 0028	CODE CODE CODE CODE	External External Length = 002D
he Microsoft MACRO Assembler aracter ID for MSDOS 2.00 3.	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0016 L WORD 028A Number 0013 Number 0001 Number 0003 Number 0003 L NEAR 0000 Alias BIT2 Alias BIT2 Number 0028 L NEAR 0078	CODE CODE CODE CODE	External External Length = 002D
he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3. 4	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0016 L WORD 028A Number 0013 Number 0003 Number 0003 L NEAR 0000 Alias BIT2 Alias BIT2 Alias BIT2 Number 0028 L NEAR 0078	CODE CODE CODE CODE	External External Length = 002D
he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3.	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0016 L WORD 028A Number 0013 Number 0013 Number 0003 Number 0003 L MEAR 0000 L MEAR 0007 Number 0005 Number 0008 Number 0038 Number 0014 Number 0014 Number 0045	CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler aracter ID for MSDOS 2.00 3.	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0018 Number 0011 Number 0013 Number 0003 Number 0003 L MEAR 0000 Alias BIT2 Alias BIT2 Number 0028 L MEAR 007F Number 0038 Number 0038 Number 0014 Number 0045	CODE CODE CODE CODE	External External Length =002D
he Microsoft MACRO Assembler aracter ID for MSDOS 2.00 3.	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0013 Number 0013 Number 0003 Number 0003 Number 0003 L NEAR 0000 L NEAR 0000 L NEAR 0000 Alias BIT2 Alias BIT2 Alias BIT2 Number 0028 L NEAR 007 Number 0050 Number 0050 L NEAR 0000 Alias BIT2 Alias BIT2 Alias BIT2 Number 0028 L NEAR 007 Number 0038 Number 0038 Number 0038 Number 0014 Number 0045 L NEAR 0133 Alias BIT5 L NEAR 01DF L NEAR 01DF L NEAR 01DF L NEAR 01DF L NEAR 01EC	CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3.	Number 0003 Number 0004 Number 0005 Alias BIT3 Number 0013 Number 0013 Number 0003 Number 0003 Number 0003 L MEAR 0000 L NEAR 0014 Number 0014 Number 0014 Number 0014 Number 0015 L NEAR 0133 Alias BIT6 L NEAR 01DF L NEAR 01DF L NEAR 0128 L NEAR 0236 L NEAR 0236 L NEAR 0236	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler Aracter IO for MSDOS 2.00 3.	Number 0003 Number 0005 Alias BIT3 Number 0006 L WORD 028A Number 0011 Number 0013 Number 0003 Number 0003 L NEAR 0000 L NEAR 0001 L NEAR 0105 L NEAR 0133 Alias BIT2 Number 0038 Number 0038 Number 0045 L NEAR 015 L NEAR 015 L NEAR 015 L NEAR 015 L NEAR 0235 L NEAR 0244	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler Aracter IO for MSDOS 2.00 3.	Number 0003 Number 0005 Alias BIT3 Number 0006 L WORD 028A Number 0001 Number 0003 Number 0003 Number 0003 L NEAR 0000 L NEAR 0000 F PRDC 007F Number 0085 V BYTE 0000 Alias BIT2 Number 0028 Alias BIT2 Number 007F Number 0016 L NEAR 0116 L NEAR 0246 L NEAR 0241 L NEAR 0241 L NEAR 0241 L NEAR 0251	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler Aracter IO for MSDOS 2.00 3.	Number 0003 Number 0005 Alias BIT3 Number 0006 L WORD 028A Number 00013 Number 0003 Number 0003 Number 0003 L NEAR 0000 L NEAR 0000 F PROC 007F Number 0078 V BYTE 0000 Alias BIT2 Number 0028 L NEAR 0000 F PROC 007F Number 0075 Number 0075 Number 0075 L NEAR 0075 Number 0085 L NEAR 0107 L NEAR 0110 L NEAR 013 Alias BIT2 Number 0045 L NEAR 015 L NEAR 015 L NEAR 015 L NEAR 015 L NEAR 025 L NEAR 024 L NEAR 024 L NEAR 0251 L NEAR 0255 L NEAR 0255 L NEAR 0255 L NEAR 0255	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler aracter IO for MSDOS 2.00 3.	Number 0004 Number 0005 A11as BIT3 Number 0006 L WORD 028A Number 0013 Number 0001 Number 0003 Number 0003 L NEAR 0000 L NEAR 0000 F PROU 0075 V BYTE 0000 A11as BIT2 A11as BIT2 Number 0075 Number 0075 Number 0075 Number 0075 Number 0075 Number 0075 L NEAR 0076 Number 0075 L NEAR 0076 Number 0014 L NEAR 0116 L NEAR 015 L NEAR 015 L NEAR 025 L NEAR 0235 L NEAR 0244 L NEAR 0251 L NEAR 0250	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
he Microsoft MACRO Assembler Aracter IO for MSDOS 2.00 3.	02-20-84 Number 0003 Number 0005 A11485 B173 Number 0011 Number 0011 Number 0001 Number 0003 Number 0002 Number 0003 Number 0005 L NEAR 0000 F PROL 0005 V BYTE 0000 A1148 BIT2 A1148 BIT6 L NEAR 0075 Number 0030 Number 0030 Number 0014 Number 0015 L NEAR 015 L NEAR 025 L NEAR 025 L NEAR 0251 L NEAR 0252	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
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he Microsoft MACRD Assembler aracter ID for MSDOS 2.00 3. 4. 5. 5. 5. 5. 7MMAX YS 1 3. VYCOM YMAX YYRT YYRT YYRT YYRT YYRT YYRT YYRT YYR	Number 0003 Number 0005 Alias BIT3 Number 0001 Number 0001 Number 0001 Number 0003 Number 0003 Number 0003 Number 0003 L NEAR 0000 I NEAR 0000 L NEAR 0000 L NEAR 0007 Number 0075 Number 0075 Number 0075 Number 0075 L NEAR 017 Number 0030 Number 0014 Number 0013 Alias BIT2 Alias BIT2 Alias BIT2 L NEAR 0075 Number 0030 Number 0014 Number 0015 L NEAR 0250 L NEAR 0250 L NEAR 0250 L NEAR 0251 L NEAR 0251 L NEAR 0251 L NEAR 0251 L NEAR 0250 L NEAR 0250 L NEAR 0250 L NEAR 0270 L NEAR 01F2 L NEAR 01F5 L NEAR 01F6 L NEAR 01F7 L NEAR 0270	CODE CODE CODE CODE CODE CODE CODE CODE	External External Length = co2D External
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	L NEAR 0000	CODE	External External	
	L NEAR 0328 Number 0010	CODE	Global	
	Number 0011 L NEAR 0114	CODE		*
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10 for MSDOS 2.00	Number 0022 Number 0021 Number 0026 Number 0030 Number 001D Number 001C Number 002E Number 002D L NEAR 00061 Number 0041 Number 0041 Number 0043 Number 0064 Number 0064 Number 0063 L NEAR 0352 V DWD 0000 L NEAR 0300 Number 0000 Number 0000	CODE	External Global Global External	
IO for MSDOS 2.00	Number 0022 Number 0021 Number 0026 Number 0030 Number 001D Number 001C Number 002E Number 002D L NEAR 0000 L NEAR 0061 Number 0041 Number 0043 Number 0054 Number 0054 Number 0050 L NEAR 0352 V DWORD 0000 L NEAR 0000 Number 0000	CODE	External Global Global External	
0 for MSDOS 2.00	Number 0022 Number 0021 Number 0026 Number 0030 Number 0010 Number 0010 Number 002E Number 002E L NEAR 0000 L NEAR 0061 Number 0041 Number 0041 Number 0041 Number 0054 Number 0054 Number 0054 Number 0054 Number 0054 Number 0050 L NEAR 0000 L NEAR 0000 Number 0000 Number 0020 Number 0020 Number 0020	CODE	External Global Global External	
0 for MSDOS 2.00	Number 0022 Number 0021 Number 0026 Number 0030 Number 0010 Number 0010 Number 0020 L NEAR 0000 L NEAR 0061 Number 0041 Number 0041 Number 0043 Number 0054 Number 0050 L NEAR 0352 V DWORD 0000 L NEAR 0000 Number 0010 Number 0000 Number 0000	CODE CODE CODE CODE	External Global Global External	
IO for MSD0S 2.00	Number 0022 Number 0021 Number 0026 Number 0030 Number 001D Number 001C Number 002E Number 002D L NEAR 0006 L NEAR 00061 Number 0041 Number 0043 Number 0064 Number 0064 Number 0063 L NEAR 0352 V DWD 0000 L NEAR 0300 Number 0000 Number 0010 Number 0000	CODE	External Global Global External	
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ID for MSDOS 2.00	Number 0022 Number 0021 Number 0021 Number 0030 Number 0010 Number 0010 Number 0010 Number 0020 L NEAR 0061 Number 0041 Number 0041 Number 0043 Number 0043 Number 0061 L NEAR 0352 V DWORD 0000 L NEAR 0000 L NEAR 0000 Number 0000	CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
IO for MSDOS 2.00	Number 0022 Number 0026 Number 0030 Number 0010 Number 0010 Number 0026 Number 0020 L NEAR 0000 L NEAR 0001 Number 0041 Number 0043 Number 0043 Number 0043 Number 0043 Number 0054 Number 0000 L NEAR 0352 V DWD 0000 L NEAR 0352 V DWD 0000 Number 0000 L NEAR 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
ID for MSDOS 2.00	Number 0022 Number 0026 Number 0030 Number 0010 Number 0010 Number 0020 L NEAR 0000 L NEAR 0061 Number 0041 Number 0043 Number 0043 Number 0043 Number 0064 Number 0064 Number 0060 L NEAR 0352 V DWORD 0000 Number 0000 L NEAR 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
IO for MSDOS 2.00	Number 0022 Number 0026 Number 0030 Number 0010 Number 0010 Number 0010 Number 0020 L NEAR 0000 L NEAR 0061 Number 0043 Number 0043 Number 0043 Number 0043 Number 0000 L NEAR 0352 V DWORD 0000 L NEAR 0000 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
IO for MSDOS 2.00	Number 0022 Number 0021 Number 0021 Number 0020 Number 0010 Number 0010 Number 0010 Number 0020 L NEAR 0000 Number 0020 Number 0020 Number 0000 Number 0000 L NEAR 0000 Number 0000 L NEAR 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
O for MSDOS 2.00	Number 0022 Number 0021 Number 0021 Number 0030 Number 001D Number 001C Number 002C Number 002D L NEAR 0061 Number 0041 Number 0043 Number 0043 Number 0064 Number 0064 Number 0060 L NEAR 0352 V DWORD 0000 L NEAR 0000 L NEAR 0000 Number 0000 Numbe	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
O for MSDOS 2.00	Number 0022 Number 0026 Number 0030 Number 0010 Number 0010 Number 0020 L NEAR 0000 L NEAR 0061 Number 0041 Number 0044 Number 0043 Number 0000 L NEAR 0352 V DWAR 0000 Number 0000 L NEAR 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
D for MSDOS 2.00	Number 0022 Number 0026 Number 0030 Number 0010 Number 0010 Number 0020 L NEAR 0000 L NEAR 0052 V DWORD 0000 L NEAR 0352 V DWORD 0000 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	
O for MSDOS 2.00	Number 0022 Number 0021 Number 0021 Number 0030 Number 0010 Number 0010 Number 0010 Number 0020 L NEAR 0061 Number 0043 Number 0043 Number 0041 Number 0043 Number 0064 Number 0064 Number 0060 L NEAR 0352 V DWORD 000 L NEAR 0000 L NEAR 0000 L NEAR 0000 Number 0000 L NEAR 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global External External External External External External	

haracte		ASSem OS 2.0	os	!-20	0-84	PAGE	Symbols-6
R3		 	 . Numbe	er	0003		
RLSD				s	BITI		
RTS		 	 	_	BITI		
					0000	CODE	External
					0000	CODE	External
					0003		
					0000	CODE	External
				er	OFCC		
				er	0010		
				er	EEOO		
ICKER .		 	 . L BY1	ſE	0291	CODE	Global
ICKS H.				ſE	028E	CODE	
ICKSTL.		 	 . L BY1	re	0290	CODE	
JART				er	0046		
ECT7201				AR	0000	CODE	External
ERT				er.	0040		
PRSNT .				er	0001		
A7201 .					0028		
ABAUD .					0021		
					OOFF		
AS7201.					002A		
				er	0029		
				er	0002		
				er	002B		
				AR	OOSF	CODE	
COM TBL					0034	CODE	
CPRSNT.					0002		
KC DEV .					0137	CODE	
CTIN				ΔR	0146	CODE	
KC IN2 .					0158	CODE	
CTIOCTL					0193	CODE	
C NDIN.					016F	CODE	
C NDIN1					0181	CODE	
C OSTAT					0138	CODE	
XC OUT .					0159	CODE	
XIOC TBL					OIAB	CODE	
XMTBRK .					BITS		
XMTCANCE					0000	CODE	External
XMTMTY .					0000	CODE	External
XMTXOF .					BIT2		
KMTXON .					BITI		
280.					0047		
N					0010		
: 14		 	 Numb.	•	00.0		
Warnina	Severe						
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Errors							

Symbol Cross Reference	(# i:	s definiti	on)	Cref-	1								
v	31 3 31 3 31 3	1 31# 1 31 1# 31 1# 31 1# 31 1# 31	31 31 31# 31# 31# 31# 31# 31	31# 31# 31 31 31 31 31 31# 31#	31 31# 31# 31# 31# 31# 31	31# 31# 31 31 31 31 31 31# 31#	31 31# 31# 31# 31# 31# 31	31# 31# 31 31 31 31 31 31# 31#	31 31# 31# 31# 31# 31# 31	31# 31# 31 31 31 31 31 31	31 31# 31# 31# 31# 31# 31#	31# 31# 31 31 31 31 31	31 31 31 31 31 31
7201 SAUD MODEM PPXOF 57201 SCSUB UX UX UX2 UXP UXP SUXP SUXP SUXP SUXP		9 # 3 #											
7201 AKGRND AUDMAX BAUD 170 171 172 173 174 175 177 177	31# 31# 31# 31# 31# 31# 31# 31# 31# 31#	1 31 1 31 1 31 1 31 1 31 1 31	31 31 31	31									
RKERR RKOFF RKOFN STZO1 USY BDATB	31# 24# 47 24# 47 31# 60 16	7	192	292	338								
BLEN. BHODE BPORT BPRTY BRRTY BRRTY BRADR BRCVB BRIZE BSIZE SSTPB BTXB BTXB BTXB BTXB BTXB BXOFF	31# 31# 31# 31# 31# 31# 31# 31# 31# 31#												

mbol Cross Reference	(# is	definiti	on)	Cref-2					
IT	69 84# 86 88# 15 106#			-					
16X	31# 16 586#								
RET	583 595 55# 107 16 528#	601 578	605	608#					
	16 527# 16 529#	548 597	588						
_INT	31# 31# 133 44 58	155#							
LEN	31# 11 12#	12	655						
DMA	31# 332 343# 349#	510							
TN.	15 110# 45 197#	205							
INXDR	46 170# 47 188# 49 50								
OUT1	215# 223 41# 111	211#							
NT	31# 131 31#								
TXBE	31# 31# 31#								
7201	31# 31#								
	31# 31#			•					
MAX	31#								
•	518# 537 31#	557	807						
	31# 31# 31#								
PRT	31# 31#								
T7201	20# 410 20# 412 120# 150								
[O	120# 150 31# 30# 584	587							
	31#								
TXBE	31# 104 108	119#							
7201	31# 31#	.,.,							
DFF	36# 178								
eracter ID for MSDOS 2.00	30# 176				····			 	
bol Cross Reference		definiti 177	on)	Cref-3					erica especialista
Mbol Cross Reference	(# is 34# 36 31# 17 276#		on)	Cref-3				 	
mbol Cross Reference STR	(# is 34# 36 31# 17 276# 31# 386 398#		on)	Cref-3					
mbol Cross Reference STR	34# 36 31# 17 276# 31# 386 398# 386 409# 390 453# 390 453#		on)	Cref-3					
mbol Cross Reference STR	(# is 34# 36 31# 17 276# 386 398# 386 409# 390 453# 390 463# 391 486# 391 486#		on)	Cref-3				 	
mbol Cross Reference STR	(# is 34		on)	Cref-3					
mbol Cross Reference STR	34# 36 31# 36 31# 386 398# 386 399 453# 390 453# 390 453# 391 471# 391 476# 391 476# 391 476# 392 486#		on)	Cref-3					
mbol Cross Reference STR	34# 36 31# 36 31# 386 398# 386 398# 390 453# 390 453# 390 463# 391 471# 391 476# 391 476# 391 476# 392 486# 393 486# 393 486# 393 486#		on)	Cref-3					
mbol Cross Reference STR	[# is 34# 36 31# 36 31# 386 388 398 453# 390 453# 391 476# 391 476# 391 476# 391 476# 391 476# 393 486# 393 486# 393 496# 395 496# 395 496# 395 496# 395 496# 395 496# 395 496# 395 496# 395 496# 395 496# 395 496		on)	Cref-3					
mbol Cross Reference STR	(# is 34 # 36 31 # 36 39 8 # 38 6 39 8 # 39 0 45 3 # 39 0 46 3 # 39 1 47 6 # 39 1 47 6 # 39 1 47 6 # 39 2 48 6 # 39 2 48 6 # 39 3 49 4 # 6 # 39 3 49 4 # 6 # 39 3 49 4 # 6 # 39 3 49 4 # 6 # 39 3 49 4 # 6 # 39 3 49 4 # 6 # 39 4 50 5 #		on)	Cref-3					
mbol Cross Reference STR	(# is 34		on)	Cref-3					
mbol Cross Reference STR	[# is 34# 36 31# 36 31# 386 398# 390 453# 391 476# 391 476# 391 476# 391 476# 392 486# 393 496# 393 496# 393 496# 393 496# 393 496# 393 496# 394 505# 394 50		on)	Cref-3					
mbol Cross Reference STR	# is 34# 36 31# 36 31# 386 398# 390 390 453# 390 463# 391 476# 391 476# 391 476# 391 476# 391 476# 392 486# 393 486# 393 486# 393 496# 393 496# 393 496# 393 496# 393 496# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 505# 394 394 505# 394 394 505# 394 394 505# 394 394 505# 394 394 505# 388 424# 388 424# 388 424# 388 429# 388 429# 388 429# 388 429#		on)	Cref-3					
mbol Cross Reference STR	34 36 31 4 36 31 4 36 31 8 39 8 39 8 39 8 39 8 45 39 8 45 39 8 45 39 8 39 1 47 6 4 5 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8		on)	Cref-3					
mbol Cross Reference STR	[# is 34	177	on)	Cref-3	485	493	499		
mbol Cross Reference STR	(# i S 34# 36 31# 36 31# 386 398# 386 390 453# 390 453# 390 453# 391 476# 391 476# 391 476# 391 476# 391 476# 392 486# 488# 388 41# 393 496# 393 496# 394 505# 393 496# 394 505# 394 505# 395 507# 386 413# 388 424# 388 424# 388 424# 388 424# 388 424# 388 424# 388 424# 388 428# 443# 470 433# 511	177			485	493	499		
AX CHAR RC AX CHAR RC AX CHAR RC AX CHAR RC CT201	34# 36 31# 36 31# 36 319 386 398# 398 390 453# 390 453# 391 476# 391 476# 392 486# 392 486# 392 486# 393 486# 393 486# 393 496# 394 500# 3	177			485	493	499		
TR	1 1 2 7 6 % 3 1 % 3 6 3 1 % 3 6 3 9 8 % 3 8 6 3 9 8 % 3 9 0 4 5 3 % 3 9 0 4 5 3 % 3 9 0 4 5 3 9 0 4 5 3 9 0 4 5 3 9 0 4 5 3 9 0 4 5 3 9 0 4 5 3 9 0 4 5 0 5 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 9 1 4 7 6 % 3 8 9 4 2 9 % 3 8 9 8 4 2 9 % 3 8 9 8 8 9 8 9 8 9 8 9 8 9 8 9 8 9 8 9	177 428 442	452		485	493	499		
MDO1 Cross Reference ITR	# i S 34# 36 31# 36 31# 386 386 388 409# 390 463# 390 463# 391 476# 391 476# 391 476# 391 476# 391 476# 391 476# 391 476# 391 476# 392 486# 393 486# 393 486# 393 496# 394 505# 395 507# 386 413# 389 428# 439# 389 428# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 389 439# 439# 439# 439# 439# 439# 439# 439#	177 428 442	452		485	493	499		
DO1 Cross Reference TR	34# 36 31# 36 31# 36 33 98 33 6 39 8# 38 39 0 45 3# 39 0 45 3# 39 0 45 3# 39 1 47 6# 39 1 47 6# 39 1 47 6# 39 1 47 6# 39 1 47 6# 39 2 48 6# 39 2 48 6# 39 3 5 6 0 7# 39 4 5 0 5 6# 5 5 6 6# 5 5 6	428 442	452		485	493	499		
TR	34# 36 31# 36 31# 36 319 386 398# 398 390 453# 390 463# 391 471# 391 476# 391 476# 391 476# 391 476# 392 486# 393 486# 393 486# 393 486# 393 494# 394 500# 395 500# 388 429# 389 439# 443# 470 433# 511 403# 511 4	428 442	452		485	493	499		
MBD1 Cross Reference STR	34 36 31 4 36 31 7 7 6 # 31 8 39 8 40 9 # 39 0 46 3 8 8 40 9 # 39 1 47 6 # 39 2 48 6 # 39 2 48 6 # 39 2 48 6 # 39 39 2 48 6 # 39 39 2 48 6 # 39 39 4 50 0 # 39 39 4 50 0 # 39 39 4 50 0 # 39 39 4 50 0 # 39 5 50 7 # 39 6 41 3 # 39 7 7 8 # 39 8 7 8 8 7 38 8 7 8 7 38 8 7 8 7 38 8 7 8 7 38 9 7 8	428 442 604 427	452		485	493	499		
AX	34# 36 34# 36 31# 36 3386 398# 386 390 453# 390 390 463# 391 476# 391 476# 391 476# 391 476# 391 476# 391 476# 391 476# 392 486# 392 486# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 493 498 520# 393 498 520# 393 498 520# 393 493 498 520# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 498 520# 393 493 498 520# 393 498 520# 393 493 493 493 493 493 493 493 493 493	428 442 504	452		485	493	499		

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Symbo	1 Cr	oss	Ref	ere	nc	•					(# is c	lefinit	ion)	Cres	-4	
OHR OUT1 . OUT1H. OUT2 . OUTCHA	· ·	: :	:			:	:			644# 641 633 619 22#	851 643# 638# 621 457	528#				
OUTHEX DUTNOW OUTSTA OV OVRERR	T		:			:	:	:		17 23# 22# 586 31#	518# 485 289 588#	451				
PARERR PC7201 PCBAUD			:				:			31# 31# 31#						
PCCR1. PCCR2. PCCR3. PCCR4. PCCR5.	· ·		:				:	:		31# 31# 31# 31# 31#						
PCDATE PCDFLT PCFAIL PCFLAG PCID .	· ·		:				:	:		31# 31# 31# 31# 31#						
PCMASK PCMODE PCMODE							:			31# 31# 31# 31#						
PCPRTY PCRADE PCRATE PCRCVA PCRCVB			:							31# 31# 31# 31# 31#						
PCRCVF PCRXOF PCS720 PCSIZE PCSTAR	1		:	:		:	:		:	31# 31# 31# 31# 31#						
PCSTAT PCSTCA PCSTCA PCSTPB PCTXB.			:			•	:	:	:	31# 31# 31# 31#						
PCTXOF PCXMTA PCXMTF PCXOFF PCXON.								:		31# 31# 31# 31# 31#						
PRG720 PRN PRNDP . PRNP .			•	:			:	:	:	20# 15 31# 31#	402 96#					
PROFIL PRTYMA PSTR . PTRSAV	x		:	:			:		:	31# 31# 17 29#	609 648# 129	653 155	162	182	334	

RI	Symbol Cross Reference		(#isd	lefiniti	on)	Cref	- 5								
REDIT 1 316 RENEAD 3 376 RELEM 316 RELEM 316 RELEM 317 REGUT 316 RELEM 317 REGUT 317 RECUT 317 R	PUTCHAR	23#	321	462											
REDICT 31# REHEAD 31# REVORT 31#	RBCOUNT	31#													
REIN 31/R REVORK 31/R REVORK 31/R REVORK 31/R REVORK 31/R REVORK 31/R REVORK 31/R REVORF 31/R REVORF 31/R RETURN 70 71 75 76 80 157/R RETURN 70 71 75 76 80 157/R RETURN 70 71 75 76 80 157/R RETURN 31/R RESTOR 3															
REIN 31/R REVORT 31/R RETURN 70 71 75 76 80 157/R RETURN 31/R RETU		31#													
RBLEN. 31/8 RBMAX 31/8 RBMAX 31/8 RBOUT 31/8 RBOUT 31/8 RBYOF 31/8 REVOR 31/8		31#													
RBMAX. 31# AROUT. 31# AFT AROUT. 31# AFT AROUT. 31# ARO															
RBOUT. 31# 347 RSTART 31# 31# 7 RSTART 31# 7		31#													
RESTART 31 # REVERT 31 # REVER			347												
RETAIL 31/8 REXOFF 31/8 REXOFF 31/8 REXOFF 31/8 REXORS 31/8 REVCANCEL 22/8 REVCLANCEL 32/8 REVCLANCEL 32/8 REVCLANCEL 32/8 REVLANCEL 31/8 REVLANCEL 31/8 REVLANCE 31/8 REV	RBSTART	31#													
REVORM. 31		31#													
REVORM. 31		31#													
RCVERK 31															
REVICANCEL 25	RCVBRK	31#													
REVEILS 21# 422 REVENA 21# 420 REVEINT 22# 484 REVOOF 31# REVOOF 31# REMANDEM 32# 489 ROSETUPP 20# 417 RETURN 41 42 43 51 52 53 55 58 57 61 62 65 66 6 RI 31# RISD 31# RIST 201 19# SECONOS 52# 583 594 596 SE	RCVCANCEL	25#	487												
REVENA 21 420 REVINT 25 484 REVORF 31 4 REVORF 31 4 REMODEM 22 4 45 483 ROSETUP. 20 417 RETURN 41 42 43 51 52 53 55 56 57 61 62 65 66 6 RETURN 41 42 43 51 52 53 55 56 57 61 62 65 66 6 RETURN 41 42 43 51 52 53 55 56 57 61 62 65 66 6 RETURN 31 6 77 75 76 80 157 6 80															
REVINT 25% 484 REVORF 31% REVORF 31% REVORF 31% REVORF 31% REVORF 31% REVORF 31% RESECUP. 20% 417 75 76 80 157% RI 31% RISD 31% RISD 31% RISD 31% RISD 31% RISD 31% ROM. 31% 172 190 220 234 243 263 RETURN 31%															
RCVYOFF 31# RCVYOFF 31# RCVYOFF 31# ROMODEM 23# 469 ROSETUP 20# 417 RETURN 41 42 43 51 52 53 55 56 57 61 62 65 86 6 RI 31# RISD															
RCYNOF . 31															
RDMODEM. 23# 488 ROSETUP. 20# 417 RETURN															
RETURN			469												
RETURN															
RI		4 1		43	5 1	52	5.3	5.5	56	5.7	6.1	6.2	65	8.6	
RLSD										٠.	٠.	••	• • •	•••	
RLSD															
ROM	RI	31#													
RST7201	RLSD	31#													
RTS	ROM	31#	172	190	220	234	243	263							
RUPT7201	RST7201	31#													
SECONDS.		31#													
SEQ	RUPT7201	19#													
SEQ	CECONDO	F00#													
31 31 31 31 31 31 31 31 31 31 31 31 31 3						21	71		21						
SETMODEM	••••														
SETMODEM															
SETMODEM . 24# 474 SETTIME . 63 64 536# SPARE41 . 31# SPARE42 . 31# SPDI . 31# SPSI . 31# SRI . 31# SRISD . 31# STATUS . 31# 130 156 163 STCANCEL . 27# 506 STPBMAX . 31#															
SETTIME							• •		3.	3.	3.	31	31	31	-
SETTIME															
SETTIME1															
SPARE41. 31# SPARE42. 31# SPARE42. 31# SPDI . 31# SPSEL 31# SPSEL 31# SR1 . 31# SR2 . 31# SR3 . 31# SR1SD . 31# START . 31# ST			64	536#											
SPARE42. 31# SPDI . 31# SPDI . 31# SPSEL . 31# SR1 . 31# SR2 . 31# SR3 . 31# SR3 . 31# SRSD . 31# SRTSD . 31# START . 31# START . 31# STATUS . 31# 130 156 163 STCANCEL . 27# 506 STPBMAX . 31#															
SPDI															
SPSEL 31# SR1 31# SR2 31# SR3 31# SRISD 31# SRTS 31# STAT 31# STATCHG 27# STATUS 31# STCANCEL 27# STPBMAX 31#															
SR1	SPDI														
SR2															
SRISD															
SRLSD															
SRTS		31#													
START. 31# STATCHG. 27# 504 STATUS. 31# 130 156 163 STCANCEL 27# 506 STPBMAX. 31#		31#													
STATCHG	SRTS	31#													
STATUS		31#													
STATUS	STATCHG	27#	504												
STCANCEL				156	163										
STPBMAX					-										
STRATEGY	STPBMAX	31#													
	STRATEGY	29#													

Symbol	Cro	S S	Re	•	. 61	10	•					(# is d	efiniti	on)	Cref	- 6				
TRINX X7201 YSRAM											35# 31# 33#	36 175								
TABLE.											31	31	31							
TICKER .											16	526#	549	582	589	598				
TICKS_H											521#	576	579	591						
TICKS_L. Trans.											523# 31#	677 132	580							
JART .											31#									
NIT .											31#									
CT720	١.										28#	501								
RT .			•	•	٠	•	•	٠	•		31#									
SNT											31#									
201											31#									
BAUD											31#									
MODEM											31#									
57201											31#									
7201 Modem											31#									
MUDEM 57201											31#									
IOCP											352#	360								
DMM .											95	9.8	101#							
OM_TB											68#	103								
PRSNT											31#									
_BUF											358#	374								
CHAR											356#	437	446	454	461	464				
-BEV											85 354#	9 4	97	100	283#	288	300	320	3	3 1
IN.											73	296#	307							
IN2											291	308#	•••							
TOCT											72	8 1	372#							
NDIN											74	330#								
NDIN											333	337#	346							
_OSTA											79	287#								
C_OUT											77	78	313#	325						
RC. STAT											355# 357#	406	436							
OC TB											357#	385#								
TBRK											31#	305#								
TCANC											26#	495								
TMTY											25#	492								
TXOF											31#									
TXON											31#									

The Microsoft MACRO Assembler COMTAB	02-2	0-84	PAGE	1 - 1		
1		PAGE	, 132			
3		TITLE	COMTAB Tables	& CONTROL	BLOCKS FOR CO	MM PORTION OF VERSION 2 BIOS
5	:					
7 8	:	COMPANY			Digital Equip	ment Corporation
9 10		All rigi				
11 12	:	COMBIOS	. A86	version	/٧00-01/	APRIL 11, 1983 DEVELOPMENT
13· 14	:				/V00-02/	
15 16 17	:					: values for optional comm s same as comm port.
17 18 19		SECT	CODE_S	EC 05:		
20 21	; CGROUP	GROUP	CODE_S	EU, KEL		
22 0000	CODE	SEGMENT	BYTE P	OBLIC 'COL	E' S:CGROUP, SS:CO	C D O I I D
24 25	;	NLIST	OF, D3:	cuntur, Es		indor

```
The Microsoft MACRO Assembler COMTAB
                                                                                                                                  02-20-84
                                                                                                                                                                        PAGE
                                                                                                                                                                                               8 - 1
                                                                                                TABLES & CONTROL BLOCKS FOR COMM PORTION OF VERSION 2 BIOS
                                                                                                                                                 .LIST
List
nmlist
26
27
28
29
30
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
45
46
                                                                                                                                                                                                                        : do not list macro expansion
                                                                                                                                                 PUBLIC PCCOM
PUBLIC PCXCOM
PUBLIC PCPRT
                                                                                                                                                                                                                        ; port control block for comm port
; port control block for extended comm
; port control block for printer port
                                                                                                                                                                                                                       ; default comm control block for comm
; default comm control block for printer port
; default comm control block for optional comm
; NYM comm control block for comm
; NYM comm control block for printer port
; NYM comm control block for printer port
; NYM -> CCB values for comm data bits
; NYM -> CCB values for comm PARITY bit
; NYM -> CCB values for printer baud
; NYM -> CCB values for printer baud
; NYM -> CCB values for printer data bits
                                                                                                                                                PUBLIC
PUBLIC
PUBLIC
PUBLIC
PUBLIC
                                                                                                                                                                        DFLTCOM
DFLTPRT
DFLTXCOM
NVMCOM
                                                                                                                                                                         NYMPRT
                                                                                                                                                PUBLIC NYMPRT
PUBLIC NYMXCOM
PUBLIC DATBCOM
PUBLIC PRTYCOM
PUBLIC PRTBAUD
PUBLIC PRTYPRT
PUBLIC DATBPRT
                                                                                                                                                 PUBLIC XBAUD1
PUBLIC XBAUD2
                                                                                                                                                                                                                        ; translation table from Comm control block ; to value to be programmed to generator
                                                                                                                                                                                                                         ; the hack vector table of 4 double word ; pointers that points to user service routines
                                                                                                                                                 PUBLIC HVECTOR
```

```
The Microsoft MACRO Assembler COMTAB
                                                                                                                   PAGE
                                                                                         02-20-84
                                                                                                                                  9 - 1
                                                                  TABLES & CONTROL BLOCKS FOR COMM PORTION OF VERSION 2 BIOS
534556789012334566789012334
                                                                                                                                    COMTAB
                                                                                   This file contains all the tables and control blocks for the Communication part of the BIOS.
                                                                                                                    --- PORT CONTROL BLOCKS -----
                                                                                   ; These blocks contain all the information kept on each port.
                   0000
                                                                                   HVECTOR LABEL
                                                                                                                    WORD
                   0000
                            0000 0000
0000 0000
0000 0000
                                                                                                                    0,0
                                                                                                                                                                     ; port A on the mother board
; port B on the mother board
; port B on the optional comm board
; port A on the optional comm board
                                                                                                    DW
                   0000
                   0010
0010
0013
0014
0016
                                                                                                                   BYTE
                                                                                   PCCOM
                                                                                                   LABEL
                                                                                                                                                                     ; PORT CONTROL BLOCK FOR COMM
                            41 55 58
00
01E1 R
0002
                                                                                                                                                                     ; device name
; status
; address of default device settings
                                                                                                                    OFFSET NVMCOM
                                                                                                                                                                     , address of default device settings; address of default device settings; port address for modem signals; baud rate generator address; $500 baud; image of 5 control registers; 7201 data register address; port address of status/control registansk for databits/parity; receive char interrupt service flag; address of receive character interrupt service routine; transmit buffer empty service flag; address of transmit buffer empty; service routine; status change service flag; address of status change; service routine
                                                                                                    DW
DW
                                                                                                   DW
DB
                                                                                                                    овн
80
                   0018
                               0006
                   0018
001B
001B
0020
0022
0024
0025
                               00
00 00 00 00 00
0040
0042
81
82
83
84
85
86
87
88
90
                                                                                                                    0,0,0,0,0
40H
42H
                                                                                                    DW
DW
DB
                               00
FF00
                                                                                                    DW
DB
DW
DW
DB
                                                                                                                    OFFOOH
                   0025
0027
0028
002A
                               0000
                               0000
                   002C
                               00
                                                                                                    DW
DW
DB
                               0000
91
92
93
94
95
                   002D
                   002F
0031
0032
0034
                               0000
                                                                                                    DW
                               00
                   0036
                                                                                                    DB
                                                                                                                                                                      ; count of # times harware failed
97
98
99
100
                   0037
                                    11 [
                                                                                                    DB
                                                                                                                    CBLEN dup (0)
                                                                                                                                                                     ; length of comm control block
                                                 00
                  0048 004A R
004A 50 [
00
 101
                                                                                                                    +2 ; (last part of PORT CONTROL BLOCK) RBLEN+2*RBDFLT DUP (0) ;
 102
103
104
105
```

The Microsoft MACRO Assembler 02-20-84 PAGE 9-2 COMTAB

TABLES & CONTROL BLOCKS FOR COMM PORTION OF VERSION 2 BIOS

106

```
The Microsoft MACRO Assembler COMTAB
                                                                                                                                               02-20-84
                                                                                                                                                                                          PAGE 10-1
                                                                                                         TABLES & CONTROL BLOCKS FOR COMM PORTION OF VERSION 2 BIOS
108 110 1112 1113 1115 1116 1117 1120 1124 1126 1127 1128 1129 1128 1129 1131 1131 1144 1155 1156 1157 1158 1155 1156
                                                                                                                                                                                                                                                                         ; PORT CONTROL BLOCK FOR PRINTER
; device name
; status
; address of default device settings
; port address for modem signals
; baud rate generator address
; 8800 baud
; image of 5 control registers
; 7201 data register address
; port address of status/control reg
; FLAGS
; mask for databits/parity
; receive char interrupt service flag
; address of receive character
; interrupt service routine
; transmit buffer empty service flag
; address of transmit buffer empty
; service routine
; status change service flag
; address of status change
; service routine
; count of # times harware failed
; length of comm control block
                              LABEL
DB
DB
DW
DW
DW
DB
DB
DW
DB
                                                                                                                                                                                          BYTE
                                                                                                                                     PCPRT
                                                                                                                                                                                          'LST'
O
DFFSET NVMPRT
OFFFFH
OEH
OH
O,0,0,0,0
                                                 0041
0043
00
FF00
00
                              OOAE
OOAE
OOB1
OOB2
OOB4
OOB5
OOB9
OOBB
OOBE
                                                                                                                                                                                           OFFOOH
                                                  0000
                                                  00
                                                  0000
                              0087 0000
0089 0000
008B 00
008C 0000
008E 0000
00C0 00
                               00C1
                                                                                                                                                                DB
                                                                                                                                                                                                                                                                          ; length of comm control block
                                                                               00
                              00D2 00D4 R
00D4 50 [
                                                                                                                                                                                           +2 ; (last part of PORT CONTROL BLOCK) RBLEN+2*RBDFLT DUP (0) ; .
                           0124
0124 55 43 31
0127 00
0128 0203 R
012A 0200
012C 0021
012E EE
012F 05 [
                                                                                                                                      PCXCOM LABEL
                                                                                                                                                                                           BYTE
                                                                                                                                                                                                                                                                           ; PORT CONTROL BLOCK FOR EXTENDED COMM
                                                                                                                                                                DB
DB
DW
DW
DW
                                                                                                                                                                                                                                                                           ; PORT CONTROL BLOCK FOR EXTENDED COP; device name; status; address of default device settings; port address for modem signals; baud rate generator address; 9500 baud; image of 5 control registers
                                                                                                                                                                                            O
DFFSET NVMXCOM
                                                                                                                                                                                           20H
21H
0EEH
                                                                                                                                                                                             5 DUP (0)
                                                                               00
                               0134 0029
0136 002B
0138 00
                                                                                                                                                                DW
DW
DB
                                                                                                                                                                                           29H
28H
0
                                                                                                                                                                                                                                                                          ; 7201 data register address
; port address of status/control reg
; FLAGS
```

The Mi	crosoft	MACRO	Assembler	02-20-84	PAGE 10-2	
				TABLES & CONTROL BLOCKS	FOR COMM PORTION OF	VERSION 2 BIOS
160	0139	FFOO		DW	OFFOOH	; mask for databits/parity
161	0138	00		DB	•	; receive char interrupt service flag
162	0130	0000		DW	0	address of receive character
163	013E	0000		DW	•	; interrupt service routine
164	0140	00		DB	0	; transmit buffer empty service flag
165	0141	0000		DW	•	address of transmit buffer empty
166	0143	0000		DW	•	service routine
167	0145	00		DB	0	status change service flag
168	0146	0000		DW	0	address of status change
169	0148	0000		DW	0	service routine
170	0144	00		DB	0	count of # times harware failed
171						
172	0148	1 1	Į.	DB	CBLEN DUP (O)	; length of comm control block
173			. 00			, •
174			1			
175						
176	0150	015E F	2	DW	\$+2	; (last part of PORT CONTROL BLOCK)
177	015E	50	1	DB	RBLEN+2*RBDFLT DUP	
178			• 00			
179			1			
180			•			

COMTAB	icrosoft	MACRO	Assembler	02-20-	84 PA	GE	12-1	
			TABLE	ES & CONTROL	BLOCKS FO	R COMM	PORTION	OF VERSION 2 BIOS
221								
222								
223	OIAE			DFLTCOM L		TE	;	default table for Comm
224	OIAE	01			B 1		;	port number for comm
225	OIAF	01		D			;	mode - data leads only
226	0180	01		D			;	one stop bit
227	0181	05			8 5		;	7 Space data bits
228	0182	01			8 1		;	no parity
229	01·B3	10		_	B 10		;	9600 receive baud rate
230	0184	10			B 16		;	9600 transmit baud rate
231	0185	11			B D(;	xon character to be used
232	0186	13		D		.3	;	xoff charcter to be used
233	0187	02		D			i	receive auto XDN/XDFF enabled
234	0188	02			B 2		:	transmit auto XDN/XDFF enabled
235 236	0189	0000			w o		;	no newly defined receive buffer
	0188	0000			w o		;	•
237	01BD	0000		D	w 0		;	•
238								
238				_				
240	01BF			DFLTPRT L		TE	;	default table for printer
241	01BF	02			B 2		;	port number for printer
242	0100	01		D			;	mode - data leads only
	0101	01		D			;	one stop bit
244	01C2 01C3	04		D			;	8 data bits
245		03 0E		D			:	No parity
245	0104			D			;	4800 receive baud rate
247	01C5 01C6	0E 11		D			;	4800 transmit baud rate
249	0165				B DC		;	xon character to be used
250	0107	13		D		3	į.	xoff charcter to be used
251	0109	02			B 2		i	receive auto XON/XOFF enabled
252	0169	0000			B 2		i	transmit auto XON/XOFF enabled
252	0100	0000			w o		:	no newly defined receive buffer
254	OICE	0000			w o		;	•
255	OILE	0000		0	w 0		;	•
256	0100							
257	0100	03		DFLTXCOM		TE	;	default table for extended Comm
258	0101	01		D			;	port number for x comm
259	0101	01		ם ם			;	mode - data leads only
260	0103	05					;	one stop bit
261	0104	00		D			:	7 space data bits
262	0105	10		ם			i	no parity change
263	0106	10		0			;	9600 receive baud rate
264	0107	11		D			·	9600 transmit baud rate
265	0108	13		ם			;	xon character to be used
266	0108	02		0		3	•	xoff charcter to be used
	0104	02		D				receive auto XON/XOFF enabled transmit auto XON/XOFF enabled
267					D 2		:	
267	0102	0000		_			•	
267 268 269	01DB 01DD	0000			W O		;	no newly defined receive buffer

The Mi	Crosoft	MACRO Assembler	02-20-84	4 PAGE	13-1	
			TABLES & CONTROL B	LOCKS FOR C	OMM PORTION	OF VERSION 2 BIOS
271						
272						
273	01E1		NVMCOM LABI	EL BYTE		; default table for Comm
274		01	DB	1		port number for comm
275	01E2		DB	1		; mode - data leads only
276		01	DB	1		; one stop bit
277	01E4	05	DB	5		; 7 Space data bits
278	01E5	01	DB	1		; no parity
279		10	DB	16		; 9600 receive baud rate
280 281	01E7	10	DB	16		; 9600 transmit baud rate
281 282	0128	13	DB	DC 1		; xon character to be used
283	OIEA	02	DB	DC3		; xoff charcter to be used
283 284	OIEB	02	DB	2		; receive auto XON/XOFF enabled
285		0000	DB	2		; transmit auto XON/XOFF enabled
285 286	OTEE		DW	0		; no newly defined receive buffer
287	0150	0000	DW	0		; · · · ·
288	0,10		DW	0		; · · ·
289						
280	01F2		NVMPRT LAB	EL BYTE		. 4.5
291		02	NVMPR! LABI	2		; default table for printer
292	01F3	01	DB			; port number for printer
293	01F4	01	DB	1		; mode - data leads only
284	01F5	04	DB			; one stop bit
295	01F6	01	DB	4		; 8 data bits
296	01F7	OE	DB	1 14		; No parity
297	01F8	OE	DB			; 4800 receive baud rate
298	01F9	11	08	1 4 DC 1		; 4800 transmit baud rate
299	OIFA	13	DB	DC1		; xon character to be used
300	01FB	02	08	2		; xoff charcter to be used
301	OIFC	02	D8	2		; receive auto XON/XOFF enabled
302	01FD	0000	DW	0		; transmit auto XON/XOFF enabled
303	01FF	0000				; no newly defined receive buffer
304	0201	0000	DW DW	0		;
305	0201	0000	UW	•		;
306	0203		NVMXCOM LAI	BEL BYTE		
307		03	NVMACOM LAI	3		; default table for extended Comm
308	0204	01	DR	3		; port number for x comm
309	0205		DB	1		; mode - data leads only
310	0206	05	. DB	5		; one stop bit
311	0207	00	DB	0		; 7 space data bits
312	0208	10	08	16		; no parity change : 9600 receive baud rate
313	0209	10	DB	16		; 3600 receive baud rate ; 3600 transmit baud rate
314	020A	11	DB	DC 1		; 3600 transmit baud rate ; xon character to be used
315	0208	13	DB	003		
316	0200	02	DB	2		; xoff charcter to be used
317	020D	02	08	2		; receive auto XON/XOFF enabled ; transmit auto XON/XOFF enabled
318	020E	0000	DW.	6		
319	0210	0000	DW	ö		; no newly defined receive buffer
320		0000	DW	ŏ		•
321	V2.12		₽₩	U		i
322	0214		DATECOM LAI	BEL BYTE		. NVM - 1 000
323	0214					; NYM -> CCB values for comm data bits
	V = 1 4		DATBPRT LAI	BEL BYTE		; NVM -> CCB values for printer data bits

The Mi	icrosoft	MAC	RO	Ass	sem	b 1 e		02-2			13-2							
								TABLES & CONTRO	L BLOCKS	FOR COMM	PORTION	OF VI	ERS I	ON 2	2 BIOS			
324	0214	03	03	03	06	05	04		DB	3,3,3,6,5	5 . 4 . 4 . 4							
325		04	04															
326 327 328 329																		
327	021C							PRTYCOM	LABEL	BYTE		; NVM	->	CCB	value	s for	comm P	ARITY bit
328	021C							PRTYPRT	LABEL	BYTE		; NVM	->	CCB	value	s for	printer	r parity
329	021C	02	01	03	03	03	02		DB	2,1,3,3,3	3,2,1,3							
330		01	03															
331																		
332	0224							PRTBAUD		BYTE			->	CCB	value:	s for	printer	r baud
333	0224				8 0	09	00		DB	2,5,7,8,8	3,12,14,	16						
334		ΟE	10															
335																		

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Cros:			
N a m e	Length		
RET	0001 0002		
BLE:	0002		
ments and groups:			
N a m e	Size al	ign combine	class
ROUP	GROUP 024E BY1	E PUBLIC	'CODE'
mbo1s:	0242		
N a m e	Type Va	lue Attr	
201	Number 004		
IODEM	Number 000 Number 000		
PXOF	Alias BII Number 004		
CSUB	Number 001	A	
KGRND	Number 24/	AF	
AUD	Number 000	6	
T1	Number 000	2	
T3	Number 000	8	
T4	Number 00:	20	
T6	Number 004 Number 008	30	
IDDEM	Number 00F Alias BII		
7201	Number 004	13	
MODE	Number 00	11	
PORT	Number 000	0	
RADR	Number 000	סס	
RXOF	Number 000	9	
SIZE	Number 000 L BYTE 000	O CODE	
STPB	Number 000		
TXOF	Number 000) A	
he Microsoft MACRO Assembler MTAB	02-20-84	PAGE	Symbols-2
MTAB	Number 000) &	Symbols-2
MTAB XOFF	Number 000 Number 000 Number 004	98 97 90	Symbols-2
XOFF	Number 000 Number 000 Number 000 Number 000	98 97 10 91 95	Symbols·2
MTAB XOFF XON K16X 1. 1.7201.	Number 000 Number 004 Number 004 Number 000 Number 000 Number 000	08 77 60 11 5 5 92	Symbols-2
XOFF	Number 000	8 8 7 7 10 0 11 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Symbols-2
MTAB XOFF XON K16X 1. 17201 1TXBE 2. 27201 3. 4.	Number 000	08 67 70 00 11 55 22 22 00 03 34 45 55	Symbols-2
MTAB XOFF XON K16X 1. 17201 17XBE 2. 27201 3. 4. 5. 5. 5.	Number 000 Number 811 L BYTE 02	08 67 70 10 15 52 22 20 00 03 44 44 44 44	Glebal
MTAB XOFF XON K16X 1. 1.7201 1.TXBE 2. 2.7201 3. 4. 5. S. TBCOM TBPRT TMAX	Number 000 Alias 811 L BYTE 02 Number 02	08 67 70 00 11 55 22 22 20 00 33 44 05 34 4 CODE 4 CODE	
MTAB XOFF XON K16X 1 17201 17X8E 2 27201 3 4 5 S TBCOM TBPRT TMAX 1 3 3	Number 000	08 87 00 11 55 22 22 00 03 34 45 53 44 CODE 44 CODE 1	Glebal
MTAB XOFF XON K16X 17201 11XBE 2. 27201 3. 4. 5. S. TBCOM TBPRT TMAX 1. 3. VCOM	Number 000	08 07 10 15 22 22 0 03 34 44 05 53 4 CODE 4 CODE	Glebal
MTAB XOFF XON K16X 1.17201 1178E 2.27201 3. 4. 5. TBCOM TBPRT TMAX 1. 3. YCOM YMAX YPRT YXCOM	Number 000	08 07 00 11 5 22 22 00 03 44 55 34 44 CODE 66 11 33 11	Global
MTAB XOFF XON K16X 17201 11XBE 22 27201 3 4 5 S TBCOM TBPRT TMAX 1 3 VCOM VMAX VPRT VXCOM LTCOM LTC	Number 000	08 8 77 00 1 1 5 5 2 2 2 2 0 0 3 4 4 CODE 6 1 3 3 1 3 3 1 3 3 2 2 3 3 4 5 5 5 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	Global
MTAB XOFF XON K16X 17201 17201 17XBE 2.7201 3.	Number 000 Alias BII L BYTE 02 L BYTE 02 Number 000	08 8 77 00 0 1 1 5 5 2 2 2 2 2 0 0 3 3 4 CODE 6 1 3 3 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Global Global
MTAB XOFF XON K16X 1 172Q01 17XBE 2 272Q1 33 4 5 5 S TBCOM TBPRT THAX 11 3 YCOM VMAX VPRT VXCOM LTPRT LTXCOM LTPRT LTXCOM LTPRT LTXCOM R R R	Number 000	08 8 77 0 0 0 1 1 5 5 2 2 2 2 2 2 2 2 2 2 3 3 4 CODE 6 1 3 3 1 1 3 3 2 2 3 3 CODE F CODE F CODE 7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Global Global Global
MTAB XOFF XON K16X 1 17201 17201 1728E 2 27201 3 4 5 S TBCOM TBPRT TMAX 1 3 VCOM VMAX VPRT VXCOM LTCOM LTCOM LTCOM LTCOM LTCOM LTCOM R R R R R TTXEE ITZOOI	Number OOO	88 87 87 80 81 81 82 82 82 83 84 84 85 86 86 86 81 83 81 82 83 83 84 84 86 86 87 87 88 88	Global Global Global
MTAB XOFF XON K16X 1 17201 1728E 2 27201 3 4 5 S TBCOM TBPRT TMAX 1 3 VCOM VMAX VPRT VXCOM LTCOM LTCOM LTCOM LTCOM LTCOM LTCOM RR RR RR RR RR RR RR RR RR	Number 000	08 8 77 10 0 11 15 2 2 2 2 0 0 3 3 4 4 CODE 6 6 1 1 3 3 1 1 1 1 3 3 2 2 3 3 4 CODE CODE 2 2 2 2 8 8 8 0 0 6 6 6 6 6 6 6	Global Global Global Global Global
XOFF XON K16X 17201 17201 17XBE 2.7201 3.4 5. S. TBCOM TBPRT THACK 1.3 YCOM VMAX VYERT VXCOM LTCOM LTC	Number 000	08 8 177 100 11 15 15 12 12 12 12 12 12 12 12 12 12 12 12 12	Global Global Global Global Global
MTAB XOFF XON K16X 17201 17201 17XBE 2.7201 3.	Number 000	08 8 77 10 11 15 15 15 15 15 15 15 15 15 15 15 15	Global Global Global Global Global Global Global
MTAB XOFF XON K16X 1 1. 17201 1TXBE 2 2. 27201 3. 4. 5. S. TBCOM TBPRT TBCOM TBPRT TI 1. 3. VOOM VMAX VPRT VXCOM LTPRT LTCOM LTPRT LTCOM LTPRT LTXCOM RTXCOM LTPRT LTXCOM RTXCOM LTPRT LTXCOM RTXCOM LTPRT LTXCOM RTXCOM RTYOOL LTPRT LTXCOM RTYCOM LTPRT LTXCOM RTYCOM R	Number 000 Alias BII L BYTE 02: L BYTE 02: Number 000 N	08 8 77 00 1 1 5 5 2 2 2 2 0 0 3 3 4 4 5 0 DE 6 6 1 3 3 3 2 2 3 3 CODE 6 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Global Global Global Global Global
MTAB XOFF XON K16X 1. 17201 17X8E 2. 27201 3. 4. 5. S. TBCDM TBPRT THAX 13 VCOM VMAX VPRT VXCOM LTPRT LTXCOM LTPRT LTXCOM LTPRT LTXCOM R R R R R R R R R R R RER R RERR RERR	Number OCC	88 877 00 1 1 5 5 2 2 2 2 2 0 0 3 4 4 CODE 6 1 3 3 13 3 2 2 3 3 CODE 6 CODE 7 2 2 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Global Global Global Global Global Global Global
MTAB XOFF XON K16X 1. 1.7201 1TXBE 2. 2.7201 3. 4. 5. S. TBCDM TBPRT THAN TBPRT THAN 1. 3. YCOM VMAX VPRT VXCOM LTPRT LTCOM LTPRT LTCOM LTPRT LTCOM RR RC RC RC DTXBE I7201 MERR ECTOR DEMAX MPRT MCOM MRCOM MPRT MCOM MCOM MPCOM MCOM MPCOM MCOM MPCOM MCOM M	Number OCO	08 8 77 10 0 11 15 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Global
MTAB XOFF XON K16X 1.7201 17XBE 2.7201 3 4.5 5 5 S TBCOM TBPRT TBCOM TBPRT TBCOM TBPRT TLBCOM TBPRT TCCOM T	Number 000	08 8 77 10 0 1 1 5 5 1 1 1 1 5 2 2 2 2 2 2 2 2 2 2 2	Global Global Global Global Global Global Global
MTAB XOFF XON K16X 1 17201 177201 17XBE 2 2 3 3 4 5 5 S TBCOM TBPRT TBCOM TBPRT TMAX 1 3 VOOM VMAX VPRT VXCOM LTPRT LTCOM LTPRT LTCOM LTPRT LTXCOM RR RT RT201 MERR ECTOR DEMAX MCOM MPRT MXCOM RERR ECTOR DEMAX MCOM MPRT MXCOM RERR RERR RERR RERR RERR RERR RERR RE	Number 000 Alias BII L BYTE 02 L BYTE 02 Number 000	08 8 77 00 1 1 5 5 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Global
MTAB XOFF XON K16X 1. 17201 1TXBE 2. 2. 27201 3. 4. 5. S. TBCOM TBPRT THAX 1. 3. VCOM VMAX VPRT VXCOM LTPRT LTXCOM LTPRT LTXCOM LTPRT LTXCOM R.	Number OCC	08 8 77 00 1 1 5 5 2 2 2 2 2 0 0 3 4 4 5 0 0 E 6 1 3 3 3 2 2 3 3 E CODE 6 1 3 3 3 2 2 3 3 E CODE 6 1 2 2 2 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Global
MTAB XOFF XON. K16X. 1. 17201. 17X8E. 2. 2. 27201. 3. 4. 5. S. TBCDM. TBCDM. TBPRT TMAX 1. 3. YCOM. VMAX VMAX VMAX VPRT VXCOM. LTPRT LTCOM. LTPRT LTCOM. LTPRT LTCOM. RR. RR. RR. RR. RR. RR. RERR RERR RER	Number 000 Alias 811 L BYTE 02 L BYTE 02 Number 000	88 877 00 1 1 5 5 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	Global
MTAB XOFF XON. K16X 1. 1.7201. 17X8E. 2. 2.7201. 3. 4. 5. S.	Number 000	88 877 00 1 1 5 5 22 22 20 0 33 4 4 CODE 6 1 3 3 1 1 2 2 3 3 E CODE 6 1 3 3 1 1 2 2 2 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Global
MTAB XOFF XON. K16X 1. 1.7201. 117X8E. 2. 2.7201. 3. 4. 5. 5. S.	Number 000	08 8 17 7 10 1 1 1 5 5 7 7 10 1 1 1 5 5 7 7 10 1 1 1 5 5 7 7 10 1 1 1 5 5 7 7 1 1 1 1 1 1 1 1 1 1 1 1 1	Global
MTAB XOFF XON K16X 1 1.7201 1TXBE 2 2.7201 3.3 4.5 5. S. TBCOM TBPRT TBCOM TBPRT TBCOM TBPRT TLSCOM TBPRT TLSCOM TBPRT TLSCOM TBPRT TASE I	Number 000	08 8 77 00 1 1 5 5 2 2 2 2 2 0 0 3 3 4 4 5 0 0 E 6 6 1 3 3 3 2 2 3 3 E CODE 6 6 1 3 3 3 2 2 3 3 E CODE 6 6 1 2 2 2 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Global

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PCMODM	Number 0006		
PCPRT	L BYTE 009A Number 002A	CODE	Global
PCRADR	Number 0033		
PCRATE	Number 000A Number 0018		
PCRCVA	Number 0018 Number 0028		
PCRCVF	Number 0017		
PCRX0F	Number 002F Number 0012		
PCSIZE	Number 0021		
PCSTART	L BYTE 0000 Number 0003	CODE	
PCSTCA	Number 0022		
PCSTCF	Number 0021 Number 0028		
PCTXB	Number 0026		
PCTXOF	Number 0030		
PCXCOM	L BYTE 0124 Number 001D	CODE	Global
PCXMTF	Number 001C		
PCXOFF	Number 002E Number 002D		
PRTBAUD	L BYTE 0224	CODE	Global
PRTYCOM	L BYTE 021C Number 0003	CODE	Global
PRTYPRT	L BYTE 021C	CODE	Global
RBCOUNT	Number 0000		
RBDFLT	Number 0020 Number 0000		
RBIN	Number 0004		
RBLEN	Number 0010		
RBMAX	Number 0002 Number 0006		
RBSTART	L BYTE 0000	CODE	
RBTAIL	Number 000E Number 0008		
RBXON	Number 000A		
RCVBRK	Alias BITO Alias BIT5		
RCVXOF	Alias BIT6		
RT	Alias BITO Alias BIT4		
RLSD	Alias BIT4 Number 0018		
RTS	Alias BIT3		
SPDI	Alias BIT1 Alias BIT0		
SR1	Number 0001		
SR2	Number 0002		
SR3	Number 0003 Alias BIT1		
SRTS	Alias BIT1		
STPBMAX	Number 0003		
The Microsoft MACRO Assembler COMTAB	02-20-84	PAGE	Symbols-4
COMTAB		PAGE	Symbols-4
COMTAB SX7201	02-20-84 Number 0010 Number 0028	PAGE	Symbols-4
SX7201	Number 0010 Number 0028 Number 0021	PAGE	Symbols-4
SX7201	Number 0010 Number 0028	PAGE	Symbols-4
SX7201	Number 0010 Number 0028 Number 0021 Number 00FF Number 002A Number 0029		
SX7201	Number 0010 Number 0028 Number 0021 Number 005F Number 0024 Number 0024 L BYTE 022C	CODE	Global
SX7201	Number 0010 Number 0028 Number 0021 Number 005F Number 002A Number 0024 L BYTE 022C L BYTE 023D Number 0002		
SX7201	Number 0010 Number 0028 Number 0021 Number 005F Number 002A Number 0028 L BYTE 022C L BYTE 023D	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X87201 X8AUD1 X8AUD2 X8AUD2 X8AUD5 X8AUD6 X8AUD6 X8AUD7 X8AUD7 X8AUD8 X	Number 0010 Number 0028 Number 0021 Number 002A Number 002A Number 0029 L BYTE 023D Number 0002 Number 0002 Number 002B Alias BIT3	CODE	Global
SX7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA57201 X87201 X8AUD X87201 X8AUD2 X8M0DEM X8F7201 X8AUD2 X8M0DEM X8F7201 X8AUD3 X8AUD3 X8AUD3 X8AUD4 X8AUD6 X8AUD7 X8AUD	Number 0010 Number 0028 Number 0021 Number 002A Number 002A Number 0029 L BYTE 023D Number 0002 Number 0002 Number 002B Alias BIT3	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XAS7201 XA87201 XB7201 XB8AUD1 XB8AUD2 XBAUD1 XBAUD2 XBAUD3 XBAUD3 XBAUD4 XBAUD5 XBAUD6 XBS7201 XBAUD7 XBAUD8 XBS7201 XBAUD8 XBS7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 XA87201 XB7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 XA87201 XB7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 XA87201 XB7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 XA87201 XB7201	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9 X8AVD0	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9 X8AVD0	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
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SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9 X8AVD0	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9 X8AVD0	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
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SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
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SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0020 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD2 X8MDDEM X8S7201 X8AUD2 X8MDFM X8S7201 XMTSRK XMTXOF XMTXO	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
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SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD2 X8MDDEM X8S7201 X8AUD2 X8MDFM X8S7201 XMTSRK XMTXOF XMTXO	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global
SX7201 XA7201 XA7201 XA8AUD XA8AUD XA87201 X87201 X87201 X8AUD X87201 X8AUD2 X8MDDEM X8F7201 X8AUD9	Number 0010 Number 0028 Number 0021 Number 0029 L BYTE 022C L BYTE 023D Number 0002 Number 0002 Number 0028 Alias BIT3 Alias BIT3 Alias BIT1 Number 0010	CODE	Global

Symbol Cross Reference			efiniti	on)	Cref	- 1 .								
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7201	26# 26# 26# 26# 26# 26#													
7201. AKGRND. AUDMAX. BAUD. ITO. IT1	22222222222222222222222222222222222222	2 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	26 26 26 26 26 26 26	26 26 26	26									
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BTXOF BXON. GROUP LK16X. ODE	26# 26# 26# 21 26# 21	23 22#	23 22	23 352	23									
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RITZOI RITXBE R2 COMTAB Symbol Cross Reference R27201 R3 R4 R5 TS ATBCOM ATBPRT ATMAX C1 C3 EYCOM EYCOM EYVAX EYPRT EYXCOM FLTPRT FLTCOM FLTPRT FLTCOM SR TR NDTXBE OITZOI RR7201 RMERR YECTOR. CDEMAX VMCOM	26### (#################################	322# 323# 231 232 223# 240# 256#	248 249 273#	264	281	298								
R17201 R17XBE R2 COMTAB Symbol Cross Reference R27201 R3 R4 R5 T5 ATBCOM ATBPRT ATMAX C1 C3 EVCOM EVPRT EVYACOM FLITCOM FLITCOM FLITCOM FLITCOM FLITCOM FLITCOM FLITCOM FLITCOM FRITCOM FRITC	26## 26## 266## 266## 266### 266### 266### 337 266### 337 266### 337 266### 34### 34### 35 266#### 35 266#### 366##### 366###### 366######## 366########	322# 323# 231 232 223# 256#	248 249	264	281	298								

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COMTAB	s Botonomon	. (\		_								
CPRTY CPRADE CRADE CRCYB CRCYB CRCYB CRCYOF CRST201 CSTZE CSTZAT CCSTZE CCSTART CCSTCR CCSTART CCSTCR CCSTART CCSTCR CCSTART CCSTCR CCTXOM CCXMTF CCXMTF CCXMTF CCXCMM RRTYCMAX RRTYCMA		# 1 S S S S S S S S S S S S S S S S S S	138	177 177	Cref-	3								
RBOUT		2 2 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	26 26 26 25	26 26 26 26	26 26 26 26	26 26 26 26	2 6 2 6 2 6 2 6	2 6 2 6 2 6 2 6	26 26 26 26	26 26 25 25	26 26 26 26	26 26 26 26	26 26 26 26	2 6 2 6 2 6 2 6
SPSEL		 26# 26# 26#	······································						,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,					
Symbol Cros	s Reference	(# is e	definiti	ion)	Cref-	4								
SR3. SRLSD. SRTS SRTS STPBMAX. SX7201 TABLE. XA7201 XABAUD XAMDDEM. XAS7201 XB7201 XB7201 XB7201		26# 266# 226# 226# 26 26# 26 26# 26# 26# 26# 248 348#	26											

The M	icrosoft MACRO Ass	embler 02-	20-84	PAGE 8-1	
COMMIN		COLLECTION OF	ROUTINE IN	THE BIOS THAT	READS NVM
21			LIST		
22		;	LIST		
23		;	NML IST		; do not list macro expansion
24					
25			PUBLIC	RDNVMCOM	; Read NVM for comm, printer default
6					; optional comm, will take NVM COMM values
27					
28				NVMCOM: BYTE	; CCB for COMM
29				NVMXCOM: BYTE	; CCB for optional comm
10			EXTRN	NVMPRT: BYTE	; CCB for printer
11					NVA 1 888
12				DATBCOM: BYTE	; NVM -> CCB values for comm data bits
33				PRTYCOM: BYTE	; NVM -> CCB values for comm parity
4				PRTBAUD: BYTE	; NVM -> CCB values for printer baud rates
35				DATBPRT: BYTE	; NVM -> CCB values for printer data bits
3 6 3 7			EXTRN	PRTYPRT: BYTE	; NVM -> CCB values for printer parity
8	: EDOO	NVMSEG	EOU	OEDOOH	: base address for NVM
3 9					,
0	= 0097	SWSTOP	EOU	97H	; NVM for # stop bits in COMM port
11	= 0094	SWXOFF		9 4 H	: NVM for auto enable xon/xoff for COMM
2	= 00A0	COTPTY		OAOH	; NVM for # data and parity bits for COMM
13	= 00A1	XBCOMM		OA1H	; NVM transmit baud rate, for comm port
14	= 00A2	RBCOMM		OA2H	: NVM for receive baud rate for comm port
45	: OOA5	PDTPTY		OASH	; NVM for # data and parity bit for printer
4.6	: 00A6	PBCOMM		OASH	: NVM transmit and receive baud for printer
47					, .

```
The Microsoft MACRO Assembler
                                                                                                              02-20-84
                                                                                                                                              PAGE
                                                                                                                                                                 9 - 1
                                                                                   COLLECTION OF ROUTINE IN THE BIOS THAT READS NVM
;-----
                                                                                                                                                                  RDNVMCOM
                                                                                                      ; This routine is called to read the NVM and translate the values in NVM; for the COMM, PRINTER and put them into the BIDS comm control tables.; Each time that its called, all 3 Comm control tables are updated, the; optional comm table will take the values from the comm.
                                                                                                       : EXTRY CONDITIONS:
                                                                                                                                                                                     DS
                                                                                                                                                                                                          points to BIOS data area
                                                                                                       EXIT CONDITIONS:
                                                                                                                                                                                     DS:BX
CH
                                                                                                                                                                                                         saved
saved
                         0000
0000
0001
0004
0007
                                                                                                      RDNVMCOM:
PUSH
MOV
MOV
                                       53
BE 0000 E
BF 0000 E
B8 ED00
8E C0
                                                                                                                                              BX
SI,OFFSET NYMCOM
DI,OFFSET NYMXCOM
AX,NYMSEG
ES,AX
                                                                                                                                                                                                         ; get address of Comm CCB to be built
; get address of optional comm CCB
; point ES to NVM
                                                                                                                           MOV
                          AOOO
                                                                                                                          MOV
                                                                                                                                              AL,1 ; assume one stop bit for comm port ES: BYTE PTR SWSTOP,BITO ; check if its one stop bit L1_1$ ; If not, set it to 2 stop bits AL,3 ; Seve it in the comm CCB CBSTPB[DI],AL ; save it in the optional comm port
                         OOOC BO 01
OOOE 25: F6 05 0097 01
0014 74 02
0016 BO 03
0018 88 44 02
0018 88 45 02
                                                                                                                          MOV
Test
                                                                                                                           JZ
MOV
                                                                                                                          MO V
MO V
                                                                                                      L1_1$:
                                                                                                                                              AL,1 ; assume auto xon/xoff enabled ES: BYTE PTR SWXDFF,BITO ; If NVM indicates disabled then, L1_2$ ; set marker to disable it CBRXOF[SI],AL ; mark comm port CBRXOF[DI],AL ; mark optional comm port CBTXOF[DI],AL ; mark optional comm port
                         001E
0020
0026
0028
002A
002D
                                     BO 01
26: F6 06 0094 01
74 02
FE CO
88 44 09
88 45 09
88 45 04
                                                                                                                           MOV
Test
                                                                                                                          JZ
INC
MOV
MOV
MOV
                                                                                                      L1_2$:
                          0033
                         0036 26: A0 00A1

003A 24 0F

003C FE C0

003E 3C 0F

0040 72 02

0042 FE C0

0044 88 44 06

0047 88 45 06

0047 88 45 06

0047 88 45 06

0048 2E: A0 00A2

0048 2F C0

0050 FE C0

0052 3C 0F
                                                                                                                          MOV
AND
INC
CMP
                                                                                                                                                                                                               get transmit baud rate
mask out unwanted bits
if nvm value < E, add 1
                                                                                                                                              AL, ES: BYTE PTR XBCOMM ; AL, OFH ;
                                                                                                                                              AL
AL,OFH
L1_3$
AL
CBTXB[SI],AL
CBTXB[DI],AL
AL,ES: BYTE PTR RBCOMM
AL,OFH
AL
                                                                                                                           JB
                                                                                                                                                                                                               else add 2
put it into Comm CCB
put it into optional comm CCB
get receive baud rate
mask out unwanted bits
if nvm value ( E, add 1
                                                                                                                           INC
MOV
MOV
MOV
AND
                                                                                                      L1_3$:
                                                                                                                                               AL OFH
```

The Mi	crosoft	MACRO Assembler	02-20-8	8 4	PAGE 9-2	
P-110 A 14			COLLECTION OF ROUT	TINE I	N THE BIOS THAT READS NVM	•
101		72 02	JI		L1_4\$	
102	0056	FE CO	11	NC	AL	; else add 2
103	0058	88 44 05	L1_4\$: M6	D V	CBRCVB[SI],AL	; put it into Comm CCB
104	005B	88 45 05	— м	OΥ	CBRCVB[DI],AL	; put it into optional comm CCB
105						
106	005E	26: AO OOAO	MO	0 Y	AL, ES: BYTE PTR CDTPTY	; get data and parity bits
107	0062	24 OF	AI	N D	AL, OFH	; mask out unwanted bits
08	0054	50	Pi	USH	AX	; save for later
09	0065	BB 0000 E	MC	0 V	BX,OFFSET DATBCOM	; translate to get # data bits
110	0068	D7	XI	LAT	BYTE PTR DATBCOM	; ·
111	0069	88 44 03	M	0 V	CBDATB[SI], AL	; put it in Comm CCB
12	2300	88 45 03	M	OΥ	CBDATB[DI].AL	; put it in optional comm CCB
113	OOSF	58	PI	D P	AX	now get parity
14	0070	88 0000 E	M	οv	BX,OFFSET PRTYCOM	
115	0073	D7	XI	LAT	PRTYCOM	<u>.</u>
16		88 44 04		O V	CBPRTY[SI].AL	; save it in comm CCB
17		88 45 04		O V	CBPRTY[DI].AL	; save it in optional comm CCB
18						·
119			: Now set	un th	e printer CCB	
120			, 500		- p	
21	0074	BE 0000 E	Mi	O V	SI.OFFSET NVMPRT	; address of printer CCB default table
22	007D			οv		get data bits and parity
23	0081	24 OF		N D	AL.OFH	: take out unwanted bit
24	0083	50		USH	AX	: save value for later
25	0084	BB 0000 E		07	BX.OFFSET DATBPRT	; translate into CCB values
26	0087	D7		LAT	BYTE PTR DATBPRT	, translate into CED values
127	0088	88 44 03		D V	CBDATB[SI], AL	: put it into printer CCB
128	OOSB	58		OP	AX	; now translate the parity
29	0085	BB 0000 E		0 P		; now translate the parity
30	OOSE	D7		LAT	BX,OFFSET PRTYPRT	•
					BYTE PTR PRTYPRT	
31	0090	88 44 04	MI	OV	CBPRTY[SI], AL	; save it into printer CCB
132						
133	0093	26: AO OOA6		OV		; printer baud rate, and implied stop bits
34	0097	24 OF		N D	AL, OFH	, get rid of unwanted bits
35	0099	B4 01		OV	AH,1	; assume 1 stop bit
36		75 02		N Z	L1_9\$; (if baud rate 75, ts 2 stop bits)
37	009 D	B4 03		OV	АН,3	;
138	009F	88 64 02		0 V	CBSTPB[SI], AH	; save # stop bits into printer CCB
39	OOA2	BB 0000 E		OV	BX,OFFSET PRTBAUD	; translate printer baud rates
140	OOAS	D7		LAT	BYTE PTR PRTBAUD	i .
141	00A6			0 V	CBRCVB[SI],AL	; save it into printer CCB
142	PAOO	88 44 06	MI	٥v	CBTXB[SI],AL	1
43						
144	ODAC	5 B	P	OP	BX	; restore caller's register
145	OOAD	C3	RI	ET		; return to caller
145						•
47	OOAE		CODE EI	NDS		
48				ND		

The Micr Comnum	950f1	MA	CRO	A 3	. .		
acros:							
		N		m c	•		
LRET .							
BLE				•		٠	
gments	and (ps:				
GROUP .							
CODE .		٠					
mbols:							
7201			•				
BAUD							
PROF .							
CSUB . 201							
KGRND. Udmax. Aud							
то							
T2 T3		:	: :	:		:	: :
T5	: : :						
	: :						
KERR .							
BDATB .		:		:		:	: :
BMODE . Bport .		:	: :		: :		
BRADR .	: : :						
BRCVB . Brxof . Bsize .							
BSTART. BSTPB							
BTXB BTXOF .							
	osofi	МА	CRO	As	sen	1 b 1 d	er
MNVM							
BXOFF . BXON DTPTY .				:			
OMNVM BXOFF . BXON DTPTY . LK16X .							
DMNVM BXOFF . BXON DTPTY . LK16X . R1 R17201 . R1TXBE .							
OMNVM BXOFF BXON DTPTY L LK16X R117201. R11TXBE R27201.							
OMN V M BX OFF LK1 LK1 LK1 LK2 RR1 RR2 RR2							
BXOFF BXOFF DTPTY LK16X R117201 R117XBE R27201 R27201 R31 R31							
OMNYM BXYOTPTX CONTROL CONT							
OMN V M BBUTFN							
DMNVM BBDTF1 RN 17 T S O 1 RR 17 T S O 1 RR 17 T S O 1 RR 27 S DM AAATTBPAA CCEVUMAX							
OMNYM BBTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTT							
ON W F							
BXOFF BXON. DTPTY LK16X R1 R17XBE R27201 R21TXBE R27201 R3. R4 R5. ATBCOM AATMAX C1. C2. EVCOM EVPRT EVYPRT ESX. CM. STRON							
BXOFF BXON DTPTY LK16X R17201 R17201 R27201 R3 R4 R5 ITS ATBCOM ATBPRT AATBCOM AATBPRT AATBCOM EVC							
COMN V M LBXOFF LBXON. DTPTY LK16X R1 T201 R1 TXBE R2 T201 R3. R4. R5. LX16X R4. R5. LX16X R7.							
BXOFF BXON. DTPTY LK16X R17201 R117XBE R2. C1201 R277201 R3. R4. R5. MAATBAX CC3. MEEVPMAX EEVPMAX EEVPMAX EEVPMAX EEVPMAX EEVPMAX EEVPMAX EEVPMAX EEVPMAX EIVPMAX EIV							
OMN V M B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F							
OMN V M B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F F B X O F							
DMNV M FN. YX SBDTT1 S O 1 SBDTTT S O 1							
D M N V M F N Y X O N N V M F N Y X O N N V M F N Y X O N N V M N Y N Y N N Y N N Y N N Y N N Y N							
D M N V F F							
COMN Y M BXOFF BXON. DTPTY LR1. R1.TXBE R2.7201 R3.TXBE R2.7201 R4.TSS. R4.TSS. R4.TSS. R5.TATBE R7.TY R1.TY							
COMN Y M CBXOFF BXON DTPTY LK16x R17201 R117XBE R27201 R31 CR3 CR4 CR5 CR5 CR7 CR5 CR7 CR7 CR7 CR7							
COMN VM CBXOFF CBXON DTPTY LK16x R17201 R17201 R1728E R27201 CR2 R27201 CR3 CR4 R55 ATBCOM AATBPRT CCC CCC CCC CCC CCC CCC CCC							
CBXOFF . CBXON CDTPTY .							

PCLEN. PCMASK PCMASK Number CO15 Number CO27 PCMODM Number CO28 PCPRTY Number CO20 PCRADR Number CO21 PCSTAT Number CO21 PCSTAT Number CO22 PCSTAT Number CO21 PCSTAT Number CO21 PCSTAT Number CO21 PCSTAT Number CO22 PCSTAT Number CO21 PCTXB Number CO21 PCXDF PCXDF Number CO20 PCXDF RECOMM Number PCXDF RECOMM Number CO20 PCXDF RECOMM Number CO20 PCXDF RECOMM Number PC	he Microsoft MACRO Assembler Mnym	02-:	20-84	PAGE	Symbols-3	
PCMODE	LEN					
PCMODM						
PCPRITY Number						
PCRADR Number 0033 PCRATE Number 0010 PCRCVA Number 0012 PCRCVF Number 002F PCRXDF Number 0017 PCST201 Number 0012 PCST201 Number 0021 PCSTART L BYTE 0000 PCSTATA Number 0021 PCSTATA Number 0022 PCSTCA Number 0021 PCSTCA Number 0022 PCSTCA Number 0022 PCSTCA Number 0022 PCSTCA Number 0022 PCSTCA Number 0021 PCSTCA Number 0022 PCTXBA Number 0021 PCTXBA Number 0022 PCTXBA Number 0022 PCXMTA Number 0022 PCXMTA Number 0022 PCXMTA Number 0023						
PCRATE Number OOOA PCRCVA Number OO15 PCRCVA Number OO15 PCRCVF Number OO27 PCRCVF Number OO27 PCRXOF Number OO27 PCS7201 Number OO17 PCS7201 Number OO12 PCS1ZE Number OO12 PCS1ZE Number OO00 CODE PCSTAT Number OO22 PCSTAT Number OO22 PCSTCF Number OO22 PCSTCF Number OO22 PCSTCF Number OO20 PCXMTA Number OO10 PCXMTA Number OO10 PCXMTA Number OO10 PCXMTA Number OO10 PCXMTF Number OO20 PCXMTA Number OO20 PCXMTA Number OO20 PCXMTA Number OO45 PCXMTA Number OO46 PCXMTA Number OO04 PCXMTA Number OO06 PCXMTA N						
PCRCVF	RATE		OOOA			
PCRCVF Number 0017 PCRX0F Number 002F PCRX0F Number 0012 PCS1ZE Number 0031 PCS1ZE Number 0031 PCSTART L BYTE 0000 CDDE PCSTART Number 0023 PCSTCA Number 0022 PCSTCA Number 0022 PCSTCA Number 0028 PCSTCA Number 0028 PCSTCA Number 0028 PCSTCA Number 0026 PCSTCA Number 0030 PCXMTA Number 0010 PCXMTA Number 0010 PCXMTA Number 0010 PCXMTA Number 0020 PCXMTA Number 0000 CDDE External PCXMTA Number 0000						
Number OO2F						
Number OO12						
Number O.31	57201					
PESTAT PE						
PCSTCA PCSTCF Number PCCTXB PCCTXB Number PCCTXB Number PCCTXB PCCTXB Number PCCTXB Number PCCTXB PCCTXB Nu	START	L BYTE	0000	CODE		
Number OO2			0003			
PCSTPB PCTXB Number 0028 PCTXOF Number 0030 Number 0030 PCXMTA Number 0010 PCXMTF Number 002E PCXMTF Number 002E PCXMTF Number 002E PCXMTF Number 002E PCXMTS Number 002E PCXMTS Number 002E PCXMTS PCXMTS Number 002E PCXMTS PCXMTS Number 002E PCXMTS PCXMTS Number 0005 PCXMTS PCXMTS Number 0000 CDDE External PCXMTS PCXMTS Number 0000 CDDE External Number 0000 RECOMM Number 0000 RECOMM RECOMM Number 0000 RECOMM RECOMM Number 0000 RECOMM RECOMM RECOMM Number 0000 RECOMM RECOMM RECOMM RECOMM Number 0000 RECOMM RECOMM RECOMM Number 0000 RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM Number 0000 RECOMM RECOM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM RECOMM R	STCA					
PCTXBP PCTXBF PCTXGF Number 002C PCCXMTA Number 001D Number 001C PCXMTA Number 001C PCXMTF Number 002E PCXDN Number 002E PCXDN Number 002D PCXDN Number 002D PCXDN Number 0005 PRTYBAUD V BYTE 0000 CDDE External PRTYMAX PRTYMAX Number 0003 PRTYMAX PRTYMAX Number 0003 PRTYMAX RSCOMM Number 0000 RSCOMM Number 0000 RSCOMM Number 0000 RSDFLT RSTACL RSTAC						
PCTXOF						
PCKMTF						
PCXOFF Number OO2E PCXOFF Number OO2E PCXON Number OO2D PCXON Number OOA5 PCXON Number OOA5 PCXON PC						
PCXON Number OO2D PCXTPTY Number OOAS OOAS OOAS OOAS PCXTPTY Number OOAS O		Number	001E			
PDTPTY						
PRTBAUD						
PRTYCOM. Y BYTE COOO CDDE External PRTYMAX. Number 0000 CDDE External PRTYPRT. Y BYTE 0000 CDDE External RBCDUNT. Number 0000 RBTA Number 0000 RBHEAD. Number 0000 CDC RBTA Number 0004 RBTA RBTA Number 0004 RBTA RBTA Number 0002 CDDE CDDE RBTA RBTA Number 0006 CDDE CDDE RBTA RBTA Number 0006 CDDE CDDE RBTA RBTA Number 0006 CDDE RBTA R						
PRTYMAX	TYCOM					
PRTYPRT	TYMAX			CODE	EXTERNAL	
RECOMM RECOUNT				CDDE	External	
RBDFLT Number 0020 RBIN Number 0004 RBIN Number 0004 RBIEN Number 0010 RBIN Number 0010 RBIN Number 0002 RBIN Number 0002 RBIN Number 0005 RBIN Number 0006 RIN Number 0006 RIN Number 0006 RIN Number 0006 RIN Number 0018		Number	00A2			
RBHEAD . Number 000C RBLEN. Number 0010 RBLEN. Number 0010 RBMAX . Number 0008 RBSTART . L BYTE 0000 CODE RBSTART . L BYTE 0000 RBSTART . L BYTE 0000 RBSTART . Number 0008 RBSTART . Number 0008 RBSTART . Number 0008 RBSTART . Number 0008 RCVBRK . AliaS BITS RCVXOFF . AliaS BITS RCVXOFF . AliaS BITS RCVXOFF . AliaS BITS RCVXOF . AliaS BITS RCVXOF . AliaS BITS RCYXOF . AliaS BITO RI . AliaS BITO RI . AliaS BITO RISD . AliaS BITO RST7201 . Number 0018 RTS . AliaS BITO RTS . RT						
RBIEN NUMBER 0004 RBMAX NUMBER 0010 RBMAX NUMBER 0002 RBDUT NUMBER 0005 RBSTART L BYTE 0000 CODE RBTAIL NUMBER 0006 RBXDN NUMBER 000A RBXDN Alias BITO RCVBRK Alias BIT5 RCVVOFF Alias BIT5 RCVVOFF Alias BIT6 RSTS BIT6 RSTS Alias BIT1 SPSEL Alias BIT1	DFLT					
RBLEN. Number O010 RBMAX. Number O005 RBOUT. Number O005 RBTART. L BYTE O000 CODE RBTAIL Number O008 RBXOFF Number O008 RBXON. Number O008 RCVBRK Alias BIT0 RCVOFF Alias BIT5 RCVXOF Alias BIT6 RCVXOF Alias BIT6 RCYXOF Alias BIT6 RCYXOF Alias BIT6 ROWNMCOM L NEAR O000 CODE Global RI Alias BIT0 RI Alias BIT0 RSS BIT4 RST7201 Number O018 RT5. Alias BIT3 SPSEL Alias BIT1 SPSEL Alias BIT1	MEAD					
RBMAX. Number 0002 RBSTART						
RBOUT. Number OOO6 RBSTART L BYTE OOO0 CODE RBSTAIL Number OOOE RBSTAIL Number OOOE RBXOFF Number OOOA RCVBRK Number OOOA RCVBRK Alias BITO RCVOFF Alias BITO RCVOFF Alias BITO RCVUFF Alias BITO RITO RITO						
RBSTART. L BYTE 0000 CDDE RBXOFF . Number 0008 RBXOFF . Number 000A RBXON. Number 000A RCVBRK . Alias BIT5 RCVOFF . Alias BIT5 RCVNF . Alias BIT6 RDNVMCOM . L NEAR 0000 CDDE Global RI . Alias BIT0 RST7201 . Number 0018 RTS . Alias BIT3 SPDI . Alias BIT1 SPDI . Number 0001						
RBXOFF.	START	L BYTE	0000	CODE		
RBXON. Number OOOA RCVBRK Alias BIT5 RCVXOF Alias BIT5 RCVXOF Alias BIT5 RCVXOF Alias BIT6 RDNVMCOM L NEAR OOOO CODE Global RI Alias BIT0 RLSD Alias BIT0 RST7201 Number OO18 RTS Alias BIT1 SPSEL Alias BIT1 SPSEL Alias BIT0 Number OOO1						
RCVBRK Alias BITO RCVDFF Alias BITO RCVXDF Alias BIT5 RCVXDF Alias BIT6 RDNVMCDM L NEAR OOO CODE Global RI Alias BIT4 RST7201 Number Ool8 RTS Alias BIT3 SPDI Alias BIT1 SPSEL Alias BIT0 SR1. Alias BIT1 SPSEL Alias BIT0 SR1. Number Oool						
RCVOFF Alias BITS RCVXOF Alias BITS RCVXOF Alias BITS RDNVMCOM L NEAR 0000 CODE Global RI Alias BITO RLSD Alias BITO RST7201 Number 0018 RTS Alias BIT3 SPDI Alias BIT1 SPSEL Alias BIT0 SPSEL Alias BIT0 SPSEL Number 0001						
RCVXOF Alias BIT6 RDNVMCDM L NEAR 0000 CODE Global RI Alias BIT0 RLSD Alias BIT4 RST7201 Number 0018 RTS Alias BIT3 SPDI Alias BIT1 SPSEL Alias BIT0 SRI Number 0001						
RDNYMCDM L NEAR 0000 CODE Global RI Alias BITO RLSD Alias BIT4 RST7201 Number 0018 RTS Alias BIT3 SPDI Alias BIT1 SPSEL Alias BIT0 SRI Number 0001						
RLSD . Alias BIT4 RST7201 . Number 0018 RTS . Alias BIT3 SPDI . Alias BIT1 SPSEL . Alias BIT0 SRI . Number 0001	NVMCOM	L NEAR		CODE	Global	
RST7201						
RTS						
SPDI Alias BIT SPSEL Alias BITO SR1 Number 0001						
SPSEL						
SR1 Number 0001	SEL					
SR2Number 0002						
	2	Number	0002			

The COMN V	icrosoft				MA	CI	₹0	A:	556	mi	10	ør	02-2	0-84	PAGE	Symbols-4	
SR3. Srlsd											:			Number Alias	0003 BIT1		
SRTS														Alias	BIT1		
STPBM	ΑX													Number	0003		
SWSTO	P													Number	0097		
SWXOF	F											,		Number	0094		
SX720	1													Number	0010		
XA720	1													Number	0028		
XABAU	D													Number	0021		
XAMOD	EM													Number	OOFF		
XAS72	01													Number	002A		
X8720	1													Number	0029		
XBCOM	м													Number	00A1		
XBMOD	EM									Ĭ.				Number	0002		
X8572	01													Number	002B		
XMTBR	K													Alias	BITS		
XMTXO	F													Alias	BIT2		
XMTXD	N										Ċ		Ċ	Alias	BIT1		
?N .								·		·	·	÷	·	Number	0010		
Warni	ng	s	ev	er	e												
Error	s	E	rr	0	s												
0	-	-			-												

ymbo 1	cros	s R	e f	er	• n c							(# is d	of ini	tio	n)	Cref-	1								
		•		•		•	•	•	•			21# 21# 21# 21 21 21 21 21 21	21 21 21 21# 21# 21# 21# 21# 21#	21 21 21 21 21 21 21 21	*	21 21 21# 21# 21# 21# 21# 21# 21#	21# 21# 21 21 21 21 21 21 21# 21#	21 21 21# 21# 21# 21# 21# 21	21# 21# 21 21 21 21 21 21 21# 21#	21 21 21# 21# 21# 21# 21# 21	21# 21# 21 21 21 21 21 21 21# 21#	21 21 21# 21# 21# 21# 21# 21	21# 21# 21 21 21 21 21 21	21 21 21# 21# 21# 21# 21# 21	21# 21# 21 21 21 21 21 21	21 21 21# 21# 21# 21# 21
201 AUD DDEM . PXOF . 7201 . CSUB .		:	:	:						:	:	21# 21# 21# 21# 21# 21#														
GRND. IDMAX. UD. 100. 11. 12. 13. 14. 15. 16. 17. IDEM							· · · · · · · · · · · · · · · · · · ·					21# 21# 21# 21# 21# 21# 21# 21# 21# 21#	21 21 21 21 21 21 21	21 21 21 21 21 21		21 21 21	74 21	81								
201 . ATB . En												21# 21# 21#	111	112		127										
ODE . ORT . RTY .			:	:						:		21# 21# 21#	116	117		131										
ADR . CVB . XOF . IZE .		:	:							:	:	21# 21# 21# 21#	103 84	104 86		141										
TART. TPB . XB XOF .			:	:				:		:	:	21# 21# 21# 21#	77 95 85	78 96 87		138 142										
OFF CON PTY . COUP . C16X . DE			:								:	21# 21# 42# 16 21# 16 21# 21#	106 18 17#	18		18 147	18									
ymbo 1													(# is 6	lefini	tic	on)	Cref	- 2								
/mbo1 2 27201. 3		:	:	:				:		:	:	21# 21# 21# 21# 21#	(# is c	lefini	tic	en)	Cref	- 2								
7201.												21# 21# 21# 21# 21# 21# 21# 32# 35#	109 125	110 126	•	en)	Cref	- 2								
BCOM.BPRT.MAX.COM.MAX.COM.												21# 21# 21# 21# 21# 32# 35# 21# 21# 21# 21#	109	110	•	nn)	Cref	- 2								
/MDo1												2111211 211	109	110	•	in)	Cref	- 2								
7MD01 2.7201.3 3.4.7 5.5.7 5.6.7 5.6.7 5.6.7 5.6.7 5.6.7 5.6.7 5.6.7 5.7 5.7 5.7 5.7 5.7 5.7 5.7 5.7 5.7 5												211 ###################################	109 125	110	•	en)	Cref	- 2								
Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z												21112212222211111111111111111111111111	109	110	•	en)	Cref	-2								
V 2 2 7 2 0 1												211111 1111 1 1 1 1 5 2 3 1 6 1 8 0 8 9 1 1 1 2 2 2 2 2 2 2 2 3 3 2 2 2 2 2 2 2	109 125 77# 84# 95# 103#	110	•	en)	Cref	- 2								
D Y 223455 TTTT1.3YVVVRR DIR M												21111222111111111111111111111111111111	109 125 77# 84# 95# 138# 68 121 70	110	•	en)	Cref	-2								

CRAPE 21/CRAPE 21/CRA	Symbol Cross Reference CMASK CMODE CMODD CMODD	21# 21# 21# 21# 21#	# is de	efinitio	on)	Cref	- 3								
COMNVM Symbol Cross Reference (# is definition) COMNVM Symbol Cross Reference (# is definition) Crof-4 PD1 21/ 21/ 21/ 21/ 21/ 21/ 21/ 2	CRADR CRATE CRATE CRATE CREVA CREVA CREVB CREVF	21## 211## 221###### 221### 221########	139 114 129	1 1 5											
21	YOFF YXOF NYMCOM SD	21# 21# 25 21# 21# 21#	66#												
Wis definition Cref-4		2 1 2 1 2 1 2 1	2 1 2 1 2 1	2 1 2 1	21 21 21 21										
DI			#isd	afinitio	on)	Cref	- 4								
NBAUD 21 # MMODEM 21 # ST201 21 #	PSEL	21# 21# 21# 21# 21# 21# 21# 40# 41# 21#	74 81												
	IBAUD MODEM	21# 21# 21# 21# 43# 21# 21# 21# 21#	89												

The Microsoft MACRO Assembler Ombios	02-20-84 PAGI	. 1-1
	COMPANY CON Copyright (i All rights i	1) 1982, 1983 Digital Equipment Corporation
	COMBIOS.A86	version /VOO-01/ APRIL 11, 1983 DEVELOPMENT
	;	version /V00-02/ May 25, 1983
		1 - altered the interfaces so that the comm control block does not have to be in user DS. 2 - Take out the setting of the vectors in INITCOM. 3 - Change GETPCB so that there is an error if application tries to access non existing optional comm board. Version /VOO-O3/ Jun O1, 1983 1 - Compensate for the difference in hardware for ports on the mother board and the optional comm board. The internal/external clock is differently for both 7201. This is compensated by always setting the modem signals to deasserted state on cold boot. 2 - Fix the special receive condition for PORT B of both 7201. Version /VOO-O4/ Jun 13,1883
	;	1 - Fix printer baud rate problem
	<u>.</u>	2 - Fix programming of 7M and 7S for all ports version / Y00-05/ Jun 20, 1983
		1 - Add error handling to programming of devices 2 - Do not enable interrupts in the driver 3 - Mask out hi order bit before checking for xon/xoff type control characters.
8 9 0 1 1 2 3	NOT	E: this module was to begin with, interruptable, this may be desirable in the future, so the places where interrupts are enabled and disabled in the driver are just commented out with the following mark so that it can be easily changed:

The Microsoft MACRO Assembler	02-20-84 P	AGE 1-2
COMBIOS	COMMUNICATION PORTION OF	VERSION 2 BIOS
54	;	
55 56	:	% 1 %
57 58	•	6-23-83 Fix the interfaces to read and set modem signals.
59	;	Signais.
60	:	6-27-83
62 63		Add the hack as requested. That is add BIOS call to take over 1/2 7201 !!
64	;	
65 66	<u>;</u>	7-01-83 Add status change interrupt service calls
67	į	

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                                        02-20-84
                                                                                                                                                                              PAGE
                                                                                                     COMMUNICATION PORTION OF VERSION 2 BIOS
77881234567890123456789011234567899999999911111111
                                                                                                                                                                                                                                 ; port control block for comm port
; port control block for extended comm port
; port control block for printer port
                                                                                                                                                       EXTRN
                                                                                                                                                                                PCCOM: BYTE
                                                                                                                                                                                 PCXCOM: BYTE
PCPRT: BYTE
                                                                                                                                                        EXTRN
                                                                                                                                                       EXTRN
EXTRN
EXTRN
                                                                                                                                                                                DFLTCOM: BYTE
DFLTXCOM: BYTE
DFLTPRT: BYTE
                                                                                                                                                                                                                               ; default comm control block for comm
; default comm control block for Xcomm
; default comm control block for printer port
                                                                                                                                                                                XBAUD1:BYTE
XBAUD2:BYTE
                                                                                                                                                                                                                                 ; baud rate translation table for Comm & Xcomm; baud rate translation table for printer
                                                                                                                                                       EXTRN
                                                                                                                                                                                                                             ; this is zero, when optional comm port present
; read NVM and set defaults
                                                                                                                                                                                XOPTION: BYTE
RDNVMCOM: NEAR
                                                                                                                                                                                                                                  ; addresses of the user defined service routines
                                                                                                                                                       EXTRN
                                                                                                                                                                                 HVECTOR:WORD
                                                                                                                                                        PUBLIC
PUBLIC
PUBLIC
PUBLIC
                                                                                                                                                                               RUPT7201
XRUPT7201
INITCOM
WINITCOM
                                                                                                                                                                                                                                  ; address of mother board 7201 interrupt
; address of optional board 7201 interrupt
; cold start initialize of all 7201
; warm start initialize of all 7201
                                                                                                                                                                                                                                ; warm start initialize of all 7201
; reprogram 7201
; reprogram 7201 to default values
; reset receive char buffer to default area
; read device setup information
; receiver enable
; get input character
; write character
; write character immediately
; read modem signals
; set modem signals
; transmit break
; cease transmission of break
; set receive character interrupt service
; cancel receive char interrupt service
; set transmit buffer empty service
; cancel transmit buffer empty service
                                                                                                                                                        PUBLIC
PUBLIC
PUBLIC
PUBLIC
                                                                                                                                                                               PRG7201
DFLT7201
DFLTBUF
RDSETUP
                                                                                                                                                        PUBLIC
                                                                                                                                                                               RCVENA
RCVDIS
INSTAT
INCHAR
    106
107
108
109
                                                                                                                                                                              GETCHAR
GETCHAR
OUTSTAT
OUTCHAR
PUTCHAR
OUTNOW
RDMODEM
SETMODEM
   110
111
112
113
114
    116
117
118
118
120
121
                                                                                                                                                        PUBLIC SETMODEM
PUBLIC BRKOFF
PUBLIC RCVINT
PUBLIC RCVCANCEL
PUBLIC XMTMTY
PUBLIC XMTCANCEL
                                                                                                                                                        PUBLIC HACK7201
PUBLIC VECT7201
                                                                                                                                                                                                                                  ; setup 1/2 vector
; clean up redirection vector
   124
125
126
127
                                                                                                                                                        PUBLIC STATCHG
PUBLIC STCANCEL
                                                                                                                                                                                                                                 ; status change service call
; cancel status change service calls
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                        02-20-84
                                                                                             PAGE
                                                                                                           10-1
                                                     COMMUNICATION PORTION OF VERSION 2 BIOS
1289
1131
1332
1333
1336
1336
1336
1340
1144
1448
1155
1155
1155
1155
1155
                                                                    SUBTIL INTERRUPT SERVICE ROUTINES
                                                                    .
This is the interrupt handler routine for the 7201, on the mother; board, and the equivalent for the extended comm.
                                                                      These 2 routines are setup to share some common code.
                                                                    , Status register 2 of the 7201 will indicate the reason for the ; interrupt. This will be used as a table lookup to call the appropriate
                                                                    ;
; Since the only difference between the 2 interrupt handlers are the
; service routine table and the 7201 data port address, the two routines
; can be setup to use some common code.
                                                                    , All the service routines (tables INT7201, and XINT7201) must follow the ; the following interfaces:
                                                                    ON ENTRY TO THE SERVICE ROUTINES:
                                                                                              AX, BX, CX, DX, DS, ES, BP are saved and can be used in in any way.
                                                                                              DX contains the address of port A of the corresponding 7201\,.
158
159
161
162
163
164
165
167
168
177
177
177
177
                                                                                                                                       ; comm service routine ; save registers
               0000
                                                                   RUPT7201:
                        53
52
BB OOEA R
BA OO43
EB O8
                                                                                              BX
DX
BX,OFFSET INT7201
DX,BS7201
SHORT RUPTCOMMON
               0000
0001
0002
0005
                                                                                PUSH
PUSH
MOV
MOV
                                                                                                                                      ; get the service routine table
; get 7201 port B, status/control reg
; > go to common table lookup portion
               0008
                                                                                 JMP
                                                                   XRUPT7201:
                                                                                                                                       ; (Xtended comm service routine); save registers
               OOOA
                        53
52
BB OOFA R
BA OO2B
               000A
000B
                                                                                 PUSH
                                                                                              BX
DX
BX,OFFSET XINT7201
DX,XBS7201
                                                                                 PUSH
                                                                                                                                       ; get service routine table
; get 7201 port B, status/control reg
                                                                                 MOV
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                     02-20-84
                                                                                                                                                      11-1
                                                                          INTERRUPT SERVICE ROUTINES
0012
0012
0013
0014
0016
0018
001C
                                                                                              RUPTCOMMON:
                                                                                                                                                                                             ; common code for both service routines ; save rest of registers needed
                                50
1E 8C C8
8E D8
89 26 OOA6 R
8E D0
8C OOEA R
51
56
57
                                                                                                                                    AX
DS
AX, CS
DS, AX
SAVE_SP, SP
SAVE_SS, SS
SS, AX
                                                                                                                  PUSH
PUSH
                                                                                                                  MOV
MOV
MOV
                                                                                                                                                                                               ; set up DS to point to BIOS data area
                                                                                                                                                                                              Save stack pointers
                                                                                                                                    SS,AX
SP,OFFSET COMM_STACK
CX
SI
DI
ES
                                                                                                                  MOV
                     0020
0022
0025
0026
0027
0028
                                                                                                                  MOV
PUSH
PUSH
PUSH
PUSH
                                   06
                      0029
                                   55
                                                                                                                  PUSH
                                                                                                                                     BP
                     002A
002A
002C
002D
                                                                                               NXTINT:
                                 33 CO
EE
BO 02
EE
                                                                                                                                                                                              ; make sure certain idiot programs
; don't mess reg pointer
; set up to read status register 2 B
; .(note AH still 0)
                                                                                                                                    AX,AX
DX,AL
AL,2
DX,AL
                                                                                                                  OUT
MOV
                      002F
                                   EE
                                                                                                                  DUT
                     0030
                                                                                                                                     AL, DX
                                                                                                                  CMP
JBE
                                                                                                                                                                                            ; Range check the value to make sure ; (should be 0 to 7)
                                   83 FA 43
75 1A
BA 0042
E8 0904 R
BB 0000 E
                                                                                                                                     DX,857201
                                                                                                                                                                                            ; check if its comm & printer 7201
; If so then
; reset whole 7201
                      0035
                                                                                                                  CMP
JNZ
MOV
CALL
MOV
INC
CALL
MOV
INC
CALL
                     0035
0038
003A
003D
0040
0043
0046
0045
004F
                                                                                                                                     L1_1$
DX,OFFSET AS7201
RESET7201
BX,OFFSET PCCOM
                                                                                                                                                                                                                 reprogram port A of 7201 count up # hardware failures (put back images of 7201 reg) reprogram port B of 7201 count up # hardware failures (put back images of 7201 reg)
                                   BB 0000 E
FE 47 26
E8 0083 R
BB 0000 E
FE 47 25
E8 0083 R
EB 1D
                                                                                                                                     BYTE PTR PCFAIL[BX]
                                                                                                                                    BX,OFFSET PCPRT
BYTE PTR PCFAIL[BX]
CR7201
                                                                                                                                     CR7201
                      0052
                                                                                                                   JMP
                                                                                                                                     SHORT INTEXIT
                                                                                                                                                                                              ; exit from interrupt
                     0054
0057
005A
005D
                                                                                               L1_1$:
                                                                                                                                                                                              ; (problem in optional comm port); reset whole 7201
                                   BA 002A
E8 0904 R
BB 0000 E
FE 47 26
E8 0083 R
EB 0C
                                                                                                                  MOV
CALL
MOV
INC
CALL
JMP
                                                                                                                                     DX,OFFSET XAS7201
RESET7201
BX,OFFSET PCXCOM
BYTE PTR PCFAIL[BX]
                                                                                                                                                                                              ;
reprogram port A of 7201
; count up # hardware failures
; (put back images of 7201 reg)
; to tit from interrupt
                                                                                                                                     CR7201
SHORT INTEXIT
                      0060
                      0065
                                                                                                                                                                                              ; enable interrupts
; multiply to use it as table offsets
; find address of service routine
; save port B address
                                                                                                                  STI
                     0065 D1 E0
0067 03 D8
0069 52
                                                                                                                  ADD
PUSH
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                      02-20-84
                                                                                                               PAGE
                                                                                                                            11-2
                                                               INTERRUPT SERVICE ROUTINES
                 006A FF 17
006C 5A
006D 4A
006E B0 38
0070 EE
                                                                                                                                                               ; call service routine
; restore port B address
; get address of status/control reg 2A
; issue "and of interrupt" to 7201
; write it to control reg 0 (port A)
                                                                                                CALL
POP
DEC
MOV
WORD PTR [BX]
                                                                                                                DX
DX
                                                                                                                AL,EDI7201
DX,AL
                                                                                                DUT
                  0071
0071
0072
0073
                                                                                INTEXIT:
                            5 D
0 7
5 F
                                                                                                                BP
ES
DI
                                                                                                                                                                : restore registers
                                                                                                POP
                                                                                                POP
POP
MOV
MOV
POP
                           5E
59
8E 16 OOA8 R
8B 26 OOA6 R
1F
58
5A
5B
CF
                             5 E
                  0075
0076
007A
007E
007F
                                                                                                                CX
SS,SAVE_SS
SP,SAVE_SP
DS
AX
DX
BX
                                                                                                                                                                 Restore entry stack
                                                                                                 POP
                  0080
0081
0082
                                                                                                POP
POP
IRET
                                                                                                                                                                 ; all done, adios!
                 0083
0083
0086
0089
008B
                           8B 57 12
8A 47 0E
84 04
E8 0289 R
8A 47 0B
84 01
E8 0269 R
8A 47 0D
84 05
B4 05
B4 05
B4 03
                                                                               CR7201:
                                                                                                                DX,PCS7201[BX]
AL,PCCR4[BX]
AH,4
WRITE7201
                                                                                                MOV
                                                                                                                                                                ; get control register of 7201
; do CR4 first
                                                                                                MOV
MOV
CALL
MOV
                                                                                                                WRITE 7201
AL, PCCR1[BX]
AH, 1
WRITE 7201
AL, PCCR5[BX]
AH, 5
WRITE 7201
                                                                                                                                                                 do CR1
                                                                                                MOV
CALL
MOV
MOV
CALL
MOV
                  0091
                  0091
0093
0096
0098
009B
                                                                                                                                                                 ; do CR5
                                                                                                                AL, PCCR3[BX]
AH, 3
WRITE7201
WRITE7201
                                                                                                                                                                 do CR3
                  OOA 1
                                                                                                 MOV
                                                                                                CALRET
JMP
                  00A3 E9 0269 R
                                                                                ; Internal stack and saved pointers
                  0000 8A00
                                                                                SAVE_SP DW
SAVE_SS DW
                               40 [
??
]
                                                                                                DB 64 DUP (?)
                  : 00EA
                                                                               COMM_STACK
                                                                                                             EQU $
```

```
PAGE
The Microsoft MACRO Assembler COMBIOS
                                                                                           02-20-84
                                                                                                                                        12-1
                                                                  INTERRUPT SERVICE ROUTINES
I N T 7 2 0 1
                                                                                                                                                       & XINT7201
                                                                                      ;
; This is a table of offsets for the interrupt service routines for
; the NEC 7201 on the mother board, and the optional comm board.
                                                                                     ;
The offsets are set up to use the 7201 status register 2 (status
; affect vector) so that service routines can be fastest reached with
; the least code.
                                                                                      , NOTE : these 2 tables have to be contiguous
                                                                                     ,
;------
                  OOEA
OOEC
OOEE
OOFO
OOF2
                                                                                     INT7201 LABEL
                                                                                                                      WORD
                                                                                                                                                        ; Port B transmit buffer empty
; Port B external status change
; Port B receive character available
; Port B special receive condition
; Port A transmit buffer empty
; Port A external status change
; Port A receive character available
; Port A special receive condition
                                                                                                      DW
DW
DW
DW
DW
                                                                                                                      BTXEMT
BSTAT
BRXDAT
BRXSPC
ATXEMT
ASTAT
                              021F R
                              021F R
028B R
0143 R
023E R
0215 R
0295 R
013E R
0248 R
                                                                                                                       ARXDAT
                   00F6
                                                                                                      DW
DW
                                                                                                                       ARXSPC
                                                                                                                      WORD
XBTXEMT
XBSTAT
XBRXDAT
XBRXSPC
XATXEMT
XASTAT
                   OOFA
OOFC
OOFE
                                                                                     XINT7201 LABEL
                               021A R
0290 R
0148 R
0243 R
0276 R
027A R
                                                                                                                                                        ; Port B transmit buffer empty
; Port B external status change
; Port B receive character available
; Port B special receive condition
; Port A transmit buffer empty
; Port A external status change
; Port A receive character available
; Port A special receive condition
                                                                                                      DW
DW
DW
DW
DW
                               0148 R
0243 R
0276 R
027A R
0286 R
0281 R
                   0100
0102
0104
0106
0108
                                                                                                                       XARXDAT
                                                                                                      DW
                                                                                                                       XARXSPC
```

The M		MACRO	Assembler	02	20-84	PAGE	13-1				
. 0 40 1 0	•			INTERRUPT SER	VICE ROUT	INES					
3 1 1											
112											
3 1 3				;							
1 4				;							
3 1 5				;	DEFAUL	T TABLES	OF SERVI	CE	ROUT	INE	ES FOR THE 4 DEVICES
16				;							
317											other board
3 1 8				; POR	TXA, PORT	XB, are	for the 7	201	on	the	e optional comm port
3 1 9				;							
320											
321	010A			PORTB	LABEL	WORD					
322	010A	021F			DW						transmit buffer empty
123	0100	028B			DW	BSTAT					external status change
324	010E	0143			DW	BRXDAT					receive character available
325	0110	023E	R		DW	BRXSPC		;	Port	8	special receive condition
326											
327	0112			PORTA		WORD					
328	0112				DW	ATXEMT					transmit buffer empty
329	0114	0295			DW	ASTAT					external status change
330	0116	013E			DW	ARXDAT					receive character available
3 3 1	0118	0248	R		DW	ARXSPC		;	Port	A	special receive condition
332											
333	011A			PORTX		WORD					
334	011A	021A			DW	XBTXEM					transmit buffer empty
335	0110	0290			DW	XBSTAT					external status change
336	011E	0148			DW	XBRXDA					receive character available
337	0120	0243	R		DW	XBRXSP	2	;	Port	В	special receive condition
338											
339	0122			PORTX		WORD					
340	0122	0276			DW	XATXEM					transmit buffer empty
341	0124	027A			DW	XASTAT					external status change
342	0126	0286			DW	XARXDA					receive character available
343	0128	0281	D		DW	XARXSP	_		Port	Δ	special receive condition

The M	licrosoft	MAC	RO	ASSE	mbler	02-2	0-84	PAGE	14-1
						INTERRUPT SERVI	CE ROUT	INES	
344									
345									
346	012A					HACKA:			
347	012A	FF	1 E	0000	E		CALL	DWORD	PTR HVECTOR
348	012E	C3					RET		
349									
350	012F					HACKB:			
351	012F	FF	1 E	0004	E		CALL	DWORD	PTR HVECTOR+4
352	0133	C3					RET		
353									
354	0134					HACKXB:			
355	0134	FF	1 E	0008	E		CALL	DWORD	PTR HVECTOR+8
356	0138	C3					RET		
357									
358	0139					HACKXA:			
359	0139	FF	1 E	000E	E		CALL	DWORD	PTR HVECTOR+12
360	013D	C3			_		RET		
:									

The Microsoft MACRO Assembler COMBIOS PAGE 02-20-84 15-1 INTERRUPT SERVICE ROUTINES ARXDAT Receive character interrupt service routines. ; All three ports uses the same service routine, RECEIVER. ; On entry to RECEIVER, DS:BX must point to the appropriate Port Control Block. 013E 013E BB 0000 E 0141 EB 08 ; Port A receive character available BX,OFFSET PCCOM; get offset of the COMM port control block SHORT RECEIVER; go to common service routine. ARXDAT: MOV JMP 0143 0143 BB 0000 E 0146 EB 03 ; Port B receive character available BX,OFFSET PCPRT ; get offset of the printer port control block SHORT RECEIVER ; go to common service routine BRXDAT: MOV JMP XBRXDAT: 388 389 390 391 392 393 ; Port A receive character available BX,OFFSET PCXCOM; get the extended COMM port control block SHORT RECEIVER; go to common service routine 0148 0148 BB 0000 E JMP ; +++++++ note this code falls thru +++++++++++++ !!!!!!! (PPPPP ####

The Microsoft MACRO Assembler COMBIOS 02-20-84 DAGE 16-1 INTERRUPT SERVICE ROUTINES 399789901 399789901 399789901 399789901 4409056792 4409 RECEIVER . This is the common receive character routine used to process the receive ; character interrupt; for all 3 ports (port A , B on the 7201 on the mother; board, and port B on the extended comm port). ENTRY CONDITIONS: DS:BX points to the port control block EXIT CONDITIONS: ; !! NOTE !! REC3 is an embedded entry point from special receive characters DX,PC7201[BX] ; get data register of the 7201 PDRT
AH,AH ; Assume no error in character
AL,DX ; read the character
SYTE PTR PCRXOF[BX],2 ; check if receiv auto XON/XOFF enabled
; > if not, do not filter these chars
DL,AL ; mask out hi order bit for xon/xoff
DL,OTFH ; protocol character !!!
DL,PCXON[BX] ; check if we have XON character
SYTE PTR PCFLAG[BX],NOT
BYTE PTR PCFLAG[BX],NOT
SHORT RECX ; > exit 014B 88 57 10 32 E4 EC 75 18 8A DO 80 E2 77 3A 57 2D 75 06 80 67 14 BF EB 7C 3A 57 2E 75 06 RECEIVER: 0148 014E 0150 0151 0155 MOV XOR IN CMP JNE MOV 0159 015C 015F 0161 0165 0167 AND CMP JNZ AND BYTE PTR PCFLAG[BX], NOT RCVXDF;
SHORT RECX
; > exit
DL,PCXOFF[BX]
; check if we have XOFF
REC3
; If not treat it like other characters
BYTE PTR PCFLAG[BX], RCVXOF; else set flag, xoff received
SHORT RECX
; JMP REC1: CMP JNZ OR JMP ; This is an embedded entry point for special receive characters !! ; AX has character from the 7201 (not yet modified according to the mask) 0172 0172 0175 0177 0179 017C 8B 57 15 22 C6 OA C2 C4 6F 33 MOV DX,PCMASK[BX] ; get the mask ; alter the incoming character AL, DH ;
AL, DL ;
BP, DWORD PTR PCRADR[BX];
AX ; AND OR LES PUSH CLI MOV ; get address of the receive buffer; save character to be written; make sure interrupts are disabled here; get # characters in the buffer; check to see if we are full; (first get address for next char in); If not go store character in buffer ; %1% 017D 0181 0185 0189 26: 88 46 00 26: 38 46 02 26: 88 7E 04 75 15 AX, ES: RBCOUNT[BP] AX, ES: RBMAX[BP] DI, ES: RBIN[BP] CMP MOV JNZ : buffer full, replace last character with ascii sub 0188 58 POP ; clean up stack

l						
The M		MACRO Assembler	02-20	-84	PAGE 16-2	
COMBIO	5		INTERRUPT SERVIC	E ROUT	INES	
1 .						
448		26: 38 7E OC		CMP		; If next char in points to top of buffer
449	0190	75 08		JNZ	REC4	
450 451	0192 0196	26: 8B 7E 0E EB 02		VOM AML	DI,ES:RBTAIL[BP]	; set pointer to tail of buffer
452	0198	EB 02	REC4:	JMP	SHORT REC5	; go put ascii sub there : Else.
453		AF	REC4:	DEC	DI	
454	0199	AF		DEC	D T	get address of last input character
455	019A	4 F	REC5:	DEC		; (now place ascii sub into buffer)
456	019A	26: C6 05 1A	RECS:	MDV		; put sub character there
457	019E	EB 43		JMP		: return to caller
458	0180	EB 43	REC6:	JMP	SHURI RECA	, return to carrer
459	0140	40	RECU.	INC	AX	; count up # characters in the buffer
460	01A1	26: 89 46 00		MOV		: save new value for count
461	0145	58		PDP		; restore character
462	01A6	26: 89 05		MOV		: save new character
463	0149	26: 3B 7E OE		CMP		; check if at end of buffer space
464	OIAD	75 06		JNZ		; If so update pointer to the beginning
465	OIAF	26: 8B 7E OC		MDV	DI.ES:RBHEAD[BP]	, it so appears pointed to the beginning
466	0183	EB 02		JMP	SHORT RECS	
467	0185		REC7:	•		; Else, increment input pointer
468	0185	47		INC	DI	to next available buffer space
469	0186	47		INC	DI	
470	0187		REC8:			; (now DI points to next avail space)
471	0187	26: 89 7E 04		MDV	ES:RBIN[BP],DI	, update new address
472	01BB	80 7F 30 02		CMP	BYTE PTR PCTXOF[BX],2	; check if transmit auto xoff enabled
473	018F	75 22		JNE	RECX	; If not, go exit
474	0101	26: 8B 46 00		MOV	AX,ES:RBCOUNT[BP]	; get count of chars in buffer again
475	0105	26: 3B 46 02		CMP	AX,ES:RBMAX[BP]	; check if buffer now full
476	0109	74 OC		JZ		;) if full see if xoff should be sent
477	0168	26: 3B 46 08		CMP		; check if over hi water mark
478	OICF	72 12		JB		; > if not all done, go exit
479	01D1	F6 47 14 04		TEST		OF ; IF over hi water mark, but auto
480	0105	75 OC		JNZ	RECX	; >xoff already sent, then return
481	0107		REC9:			; (attempt to send auto xoff comes here)
482			; %1%	STI		; enable interrupts again
483	0107	8A 67 2E		MOV		; go send an xoff
484	01DA 01DD	E8 01F9 R 74 04		CALL JZ	AUTOXMT	
486	01DF	80 AF 14 04		OR OR		; If not sucessful go exit
487	0157	80 4F 14 04	RECX:	UK	BYTE PTR PCFLAG[BX], XMTX	OF; mark xoff sent by driver
488	OIES		: %1%	STI		: enable interrupts
489	01E3	80 7F 17 01	. ~.~	CMP		; check if there is a routine to call
490	01E7	75 OF		JNE		; > exit if none (or one in progress)
491	01E9	FE 47 17		INC		; mark that a routine is being called
482	OIEC	FF 5F 18		CALL		; call service routine
493	OIEF	FE 4F 17		DEC		: mark that routine has returned
494	01F2	79 04		JNS		; check that its not cancelled
495	01F4	C6 47 17 00		MOV		; If it has, reset value to zero
496	01F8	C3	RCVX:	RET		; return to caller
I						
I						

```
The Microsoft MACRO Assembler COMBIOS
                                                                      02-20-84
                                                                                           PAGE
                                                                                                         17-1
                                                     INTERRUPT SERVICE ROUTINES
A U T D X M T
                                                                   ; This routine is used by the drivers to send the auto xon or xoff
                                                                   ; This has to be a special routine because when a driver sends; these control characters, different rules applies.
                                                                   : ENTRY CONDITIONS:
                                                                                                                                   points to port control block has the character to be transmitted
                                                                                                                       DS:BX
                                                                                                                       AH
                                                                                                                                    set if character cannot be transmitted
                                                                   ; EXIT CONDITIONS:
                                                                                                                       Z
                                                                                                                      Z cleared if it is sucessfully placed into the transmit buffer.

DX, AX destroyed.
                                                                  MUTOXMT:

MOV
PUSHF
CLI
IN
TEST
JZ
MOV
AND
OR
DEC
DEC
DUT
POPF
OR
                                                                     ; get address of the 7201 status reg
; make sure interrupts are off
                                                                                            DX,PCS7201[BX]
                       EC
A8 04
74 0F
8A C4
22 47 16
0A 47 15
4A
EE
9D
0C 80
C3
                                                                                                                                    ; check if transmit buffer empty
                                                                                            AL,DX
AL,BIT2
L2_10$
AL,AH
AL,PCMASK+1[BX]
AL,PCMASK[BX]
DX
               01FD
01FE
0200
0202
0204
0207
020A
020B
020C
020D
020E
0210
                                                                                                                                    ; > go exit if buffer not empty(ZF set); put the character in transmit buffer; mask outgoing data
                                                                                                                                    ; (get address of data register); .(note Z is now cleared if); .(DX not = 2); restore flags; clear Z flag; return to caller
                                                                                             DX
DX, AL
                                                                                DR
RET
                                                                                             AL,80H
               0211 SD
0212 32 CO
0214 C3
                                                                  12_10$: POPF
XOR
RET
                                                                                                                                    ; restore flags
; return with z set
; return to caller
                                                                                             AL,AL
```

ABIOS		O2-20-84 PAGE 18-1	
		INTERRUPT SERVICE ROUTINES	
•			
í			
•		;	
3		;	
) ;		; ATXEMT	
1		; : Transmit buffer empty interrupt service for channel A. on the mother	
;		: board.	
3			
)		;	
0215	BB 0000 E	ATXEMT: ; Port A transmit buffer empty	
	BB 0000 E EB 08	MOV BX,OFFSET PCCOM; get offset of the COMM port control bl JMP SHORT TXBUFFER; go to common routine to complete servi	DCK
3 0218	20 00	CMF SHOKE IXBUTTER ; go to common routine to complete Servi	
ī			
5		;	
3			
<u>'</u>		;	
3 1		; ; Transmit buffer empty interrupt service for channel A, or the mother	
5		: board.	
1		;	
2			
		XBTXEMT: ; Port B transmit buffer empty	
021A	BB 0000 E	MOV BX,OFFSET PCXCOM; get the extended COMM port control bl	
021A 021D	BB 0000 E EB 03		
021A		MOV BX,OFFSET PCXCOM; get the extended COMM port control bl	
021A 021D 6		MOV BX,OFFSET PCXCOM; get the extended COMM port control bl	
021A 021D 6 7		MOV BX,OFFSET PCXCOM; get the extended COMM port control bl JMP SHORT TXBUFFER; go to common routine for tranmit buffe ;	
4 021A 5 021D 6 77 8 8		MOV BX,OFFSET PCXCOM; get the extended COMM port control bl JMP SHORT TXBUFFER; go to common routine for tranmit buffe	
021A 021D 6 7 8 8		MOV BX,DFFSET PCXCOM; get the extended COMM port control bl JMP SHORT TXBUFFER; go to common routine for tranmit buffe B T X E M T	
4 021A 5 021D 6 77 8 8		MOV BX,OFFSET PCXCOM; get the extended COMM port control bl JMP SHORT TXBUFFER; go to common routine for tranmit buffe ;	
021A 021D		MOV BX,DFFSET PCXCOM; get the extended COMM port control bl JMP SHORT TXBUFFER; go to common routine for tranmit buffe B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother	
021A 021D 6 7 8 9 9 9 1 1 2 2 3		MOV BX,DFFSET PCXCOM; get the extended CDMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer by TX E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board.	
021A 021D 6 7 8 8 9 0 1 1 2 2 3 3		MOV BX,DFFSET PCXCOM; get the extended COMM port control bi JMP SHORT TXBUFFER; go to common routine for tranmit buffe B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board.	
021A 021D 66 7 8 8 9 9 9 1 1 2 3 4 4 5 7	EB 03	MOV BX,DFFSET PCXCOM; get the extended CDMM port control bi JMP SHORT TXBUFFER; go to common routine for tranmit buffe B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: Port B transmit buffer empty	r empt
021A 021D 5 7 8 9 0 1 1 2 2 3 4 5 5 7 7 7 8 9 0 0 1 1 2 2 3 3 4 4 6 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7		MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer by TX E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother; board. BTXEMT: BTXEMT: BTXEMT: BX,DFFSET PCPRT; get the printer port control by	r empt
021A 021D 66 7 8 8 9 9 9 1 1 2 3 4 4 5 7	EB 03	MOV BX,DFFSET PCXCOM; get the extended CDMM port control bi JMP SHORT TXBUFFER; go to common routine for tranmit buffe B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: Port B transmit buffer empty	r empt
021A 021D 6 7 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	BB 0000 E	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer buffer empty interrupt on port B, in the 7201 on the mother board. Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: Port B transmit buffer empty	lock buffer
021A 021D 66 73 80 90 91 92 92 93 94 95 96 97 97 98 98 98 98 98 98 98 98 98 98 98 98 98	BB 0000 E 80 7F 1C 01 75 0F	MOV BX,DFFSET PCXCOM; get the extended CDMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer. B T X E M T : Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: ; Port B transmit buffer empty get the printer port control buffer: ; (Common code for all transmit empty service interrupt) CMP BYTE PTR PCXMTF[BX],1 ; check if there is a routine to JNE TXBUFX; > exit if none (or one in programs)	olock buffer call press)
021A 021D 66 78 89 90 91 90 91 90 91 90 91 91 92 92 92 92 92 92 92 92 92 92 92 92 92	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C	MOV BX,OFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer buffer and the standard buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT:	olock buffer call
021A 021D 66 73 89 90 91 92 92 93 94 95 96 97 97 97 97 97 97 97 97 97 97 97 97 97	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1D	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer. B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: MOV BX,OFFSET PCPRT ; get the printer port control buffer. TXBUFFER: ; [Common code for all transmit empty service interrupt] CMP BYTE PTR PCXMTF[BX], 1 ; check if there is a routine to JNE TXBUFX ; > exit if none (or one in program of the control buffer empty) INC BYTE PTR PCXMTF[BX] ; mark that a routine is being control buffer empty power is being control to the control buffer empty.	ilock buffer call ress)
021A 021D 66 73 88 90 91 92 93 94 95 95 96 97 97 97 97 97 97 97 97 97 97 97 97 97	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1D FE 4F 1C	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer. B T X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: Port B transmit buffer empty get the printer port control board. TXBUFFER: (Common code for all transmit empty service interrupt) CMP BYTE PTR PCXMTF[BX], 1 check if there is a routine to JNE TXBUFX ; exit if none [or one in programs of the control board in the	ilock buffer call ress)
021A 021D 66 78 89 90 91 90 91 92 92 93 94 95 96 97 97 98 90 92 92 92 92 92 92 92 92 92 92 92 92 92	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1D FE 4F 1C	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer buffer. BT X E M T Transmit buffer empty interrupt on port B, in the 7201 on the mother board. BTXEMT: MOV BX,OFFSET PCPRT ; get the printer port control by TXBUFFER: CMP BYTE PTR PCXMTF[BX], check if there is a routine to JNE TXBUFX ; exit if none (or one in programming that a routine is being control by the printer pcxmta [BX] ; call service routine buffer was that a routine has returned JNS TXBUFX ; check that its not cancelled	nlock buffer call ress) alled
021A 021D 021D 021D 021 022 023 021F 0222 0226 0222 0226 0228 0228 0228 0228	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1D FE 4F 1C 79 04 C6 47 1C 00 88 57 12	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer shows a common routine for the mother shows a common routine shows a common ro	nlock buffer call ress)
021A 021D 66 73 80 90 91 90 91 90 91 91 92 92 92 92 92 92 92 92 92 92 92 92 92	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1C FF 4F 1C 79 04 C6 47 1C 00 8B 57 12 B0 28	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer short TXBUFFER; go to common routine for tranmit buffer extended COMM port control by the property of the printer port control by the printer printer port control common code for all transmit compty service interrupt; check if there is a routine to be printer print	ilock buffer call ress) alled
021A 021D 021D 021D 021 022 023 021F 0222 0226 0222 0226 0228 0228 0228 0228	BB 0000 E 80 7F 1C 01 75 0F FE 47 1C FF 5F 1D FE 4F 1C 79 04 C6 47 1C 00 88 57 12 80 28 EE	MOV BX,DFFSET PCXCOM; get the extended COMM port control bit JMP SHORT TXBUFFER; go to common routine for tranmit buffer shows a common routine for the mother shows a common routine shows a common ro	ilock buffer call ress) alled

COMBIOS		MACRO Assembler	02-20	-84	PAGE 19-1	
LUMBIUS			INTERRUPT SERVIC	E ROUT	NES	
592						
593						
594						
595			:			
596			:		SPREI	CEIVE
597			;			
598						t is used to process the special receive
599						, for all 3 ports (port A and B on the
800			; 7201 6	on the m	mother board, and (port B on the extended comm port).
601 602			i			
502 503			;	ENTRY C	CONDITIONS:	DS:BX points to the port control block
603 604			•	EVIT	ONDITIONS:	
804 805			•	EXII CO	AUTITUMS:	
806						
	23E		BRXSPC:			: Port B (mother board) special receive condition
		BB 0000 E		MOV		get offset of the printer port control block
509 0	241	EB 10		JMP		go to common routine to handle it.
B 10						. •
	243		XBRXSPC:			; Port A (extended comm) special rcv condition
		BB 0000 E		MOV		; get the extended COMM port control block
	246	EB OB		JMP	SHORT SPREC	; go to common routine to handle it.
3 1 4						
	248		ARXSPC:			; Port A (mother board) special rcv condition
	248	BB 0000 E		MOV	BX,OFFSET PCCOM	; get offset of the COMM port control block
5 1 7 5 1 8 0 :	2 4 B		SPRECEIV			
		88 57 12		MDV	DV BC67201[BV]	; get address of the 7201 status register
	24E			IN		; get address of the 7201 status register ; read it.
		A8 02		TEST		; read it. ; see if it's a special receive condition or:
		74 15		JZ.		; > go exit if its no interrupt pending.
623						note that above check only necessary for
324						port A on the 7201
825 0:	253		SPREC:			, , , , , , , , , , , , , , , , , , , ,
	253	8B 57 12		MOV	DX,PCS7201[BX]	; get address of the 7201 status register
B 2 7			; %1%	CLI		; make sure interrupts are off
		BO 01		MOV		; set 7201 pointer to status register 1
	258			OUT	DX,AL	i
	259	EC		IN		; read status register 1
531			; %1%	STI		; now reenable them
		SA EO		MOV		; save status in AH
	25C 25E	BO 30		MOV	AL, ERR7201 DX.AL	; issue error reset to 7201
		80 E4 70		AND		; +FRMERR : keep parity, overrun, framing errors
636	. 37	BU 24 /U		MND	AR, PAKEKKTU TRERR	TRMERK ; Reep parity, overrun, framing errors
	262		SPREC1:			: (this entry point is shared with end of break
638			arketi:			; (this entry point is shared with end of break : detect error)
639						; On entry DX points to status/control reg
640						; AH has the error flags set for the character
	282	4.4		DEC		; now point to data register
	263			DEC	DX	· ···· paris for anim for form
643 0	264	EC		IN		get the data from 7201
644 0:	285	E9 0172 R		JMP		go process character (in the same way as any

The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 19-2 INTERRUPT SERVICE ROUTINES ; other character); return to caller. 645 646 0288 C3 SPRECK: RET The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 20-1 INTERRUPT SERVICE ROUTINES W R I T E 7 2 0 1 , This is a common subroutine used to write a value into a control; register in the 7201. ; This routine is created to save code, and take advantage of the ; common data structure of all 3 ports (PORT CONTROL BLOCK). DS:BX points to the port control block
AH has the control register number
AL value programmed into the register
interrutps are disabled. ENTRY CONDITIONS: EXIT CONDITION: ; common code to write to CONTROL REGISTER 3 ; value to be written in AL ; DS:BX points to the PORT CONTROL BLOCK 0269 WRITE7201: MOV XCHG PUSHF CLI OUT XCHG OUT POPF RET 0269 8B 57 12 026C 86 C4 028E 9C 028F FA 0270 EE 0271 86 C4 0273 EE 0274 9D 0275 C3 DX,PCS7201[BX] ; get status/control reg address of the 7201 AL,AH ; set pointer to correct control register ; make sure interrupts off ; now select the register number; now write to the control register; do it! ; retore flags; return to caller DX,AL AL,AH DX,AL

The M Ombio		MACRO	Assembler	02-20	-84	PAGE 21-	1
	•			INTERRUPT SERVIC	E ROUT	NES	
8 1							
3 2							
3 3				;			• • • • • • • • • • • • • • • • • • • •
3 4				:			
5				;	none of	the follwoin	ng conditions should ever happen; port not used
3 6				;	But we'	'11 service ti	he interrupts per chance it happens.
8 7				:			
8 8				1111111			• • • • • • • • • • • • • • • • • • • •
9	0276			XATXEMT:			; Port A transmit buffer empty
11	0276 0278	BO 28 EB 02			MOV	AL, ENDTXBE	
2	02/8	EB 02			JMP	SHORT XAEXI	T ; go to common exit routine
3	027A			XASTAT:			
34	027A	80 10		XASTAT:			; Port A external status change
5	027A	80 10			MOV	AL, 5x7201	; send status change acknowledged
6	027C			XAEXIT:			
7	0270	BA 002	2 A	AMEXII:	MOV	DX.ZAH	common exit for 422 port)
8	027F	EE OO			OUT	DX, AL	; address of control register
9		C3			RET	DA, AL	; ; bye, and don't disturb again !
00	0200				KEI		, bye, and don't disturb again !
1	0281			XARXSPC:			; Port A special receive condition
2	0281	BO 30			MOV	AL . ERR7201	; send special condition clear
3	0283	E8 02	7 C R		CALL	XAEXIT	(use exit code to write to cro only)
4							, table to write to cro only)
5	0286			XARXDAT:			; Port A receive character available
9 6	0286	BA 002	2 8		MOV	DX,28H	; read the data
07	0289	EC			IN	AL . DX	; drop it on the floor
0.8	028A	C 3			RET		, e.

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                 INTERRUPT SERVICE ROUTINES
709
710
711
712
713
714
715
716
717
718
719
                                                                                                                          BSTAT, ASTAT, XBSTAT
                                                                                                                          ; External status change routines for all 3 ports. These share a common; routine XSTATCHG. These routines are included only to handle the case; where break was detected.
                           028B
028B BB 0000 E
028E EB 08
                                                                                                                                                                          ; Port B external status change, mother board BX,OFFSET PCPRT; point to port control block SHORT XSTATCHG; go process external status change
720
721
722
723
724
725
726
727
728
729
730
                                                                                                                                                  MO V
JMP
                           0290
                                                                                                                                                                          ; Port B external status change, extended comm
BX,OFFSET PCXCOM; point to port control block
SHORT XSTATCHG; go process external status change
                                                                                                                         XBSTAT:
                           0290 BB 0000 E
0293 EB 03
                                                                                                                                                  MOV
                           0295
0295
0298
0298
029B
029C
029E
                                                                                                                                                                                                                                Port A external status change, mother board
point to port control block
go process external status change
get address of 7201 status register
read status
save status
reset external status change interrupt
                                                                                                                          ASTAT:
                                          BB 0000 E
                                                                                                                                                  MOV
                                                                                                                                                                          BX, OFFSET PCCOM ;
                                                                                                                          XSTATCHG
                                             8B 57 12
EC
8A EO
BO 10
EE
F6 C4 80
75 16
                                                                                                                                                  MOV
                                                                                                                                                                          DX, PCS7201[BX]
                                                                                                                                                                          DX,PCS720
AL,DX
AH,AL
AL,SX7201
DX,AL
AH,BIT7
XSTAT1
                                                                                                                                                  I N
MD V
MO V
732
7334
7336
7337
7339
7442
7443
7445
7449
7552
                                                                                                                                                                         AL, SX7201 ; reset external status change interrupt DX, AL :

AH, BIT7 ; check if break is now on  
XSTAT1 ; ) if it is, go set flag in port control table  
; If not, then first check if it was on before  
; (if it was on and now off, then  
; it must be the tail end of break)  
BYTE PTR PCFLAG[BX], RCVBRK ; check in the port control table  
XSTATX ; If it was never on, exit  
BYTE PTR PCFLAG[BX], NOT RCVBRK ; else, clear receive break flag  
AL, SR1 ; read other error on the character  
; make sure interrupts off  
DX, AL ; first point to status register 1  
AL, DX ; read the status  
; reenable interrupts  
AL, PARERR+OVRERR+FRMERR ; keep parity, overrun, framing errors  
AL, BRKERR ; set break detect error  
AH, AL ; save it in AH (for common exit routines.  
SHORT SPREC1 ; go process character in the same way as  
; special receive condition with the exception  
; that this is a break character bit set
                                                                                                                                                  DUT
                            02A1
                           02A6
02AA
02AC
02B0
                                             F6 47 14 01
74 14
80 67 14 FE
B0 01
                                                                                                                                                  TEST
                                                                                                                                                  JZ
AND
MOV
CLI
                                                                                                                          : %1%
                           02B2
02B3
                                             EE
                                                                                                                                                   OUT
                                                                                                                                                  IN
STI
AND
OR
MOV
                           0284
0286
0288
                                             24 70
0C 80
8A E0
EB A6
                           02BA
                                                                                                                                                   JMP
753
754
755
756
757
758
759
760
761
                           02BC
02BC 80 4F 14 01
                                                                                                                          XSTAT1:
                                                                                                                                                                          BYTE PTR PCFLAG[BX],RCYBRK; If break detected for the first; time, set the flag in the port control; block, and wait till it goes away before; forming the break character.
                                                                                                                                                  OR
                                                                                                                         ; NOTE THAT INTERRUPTS ARE NOT ENABLED YET !
```

PAGE

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```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                      02-20-84
                                                                                                                                                    PAGE
                                                                                       INTERRUPT SERVICE ROUTINES
762
763
784
785
786
767
768
769
770
                       02C0
02C0 80 7F 21 01
02C4 75 07 07
02C6 FE 47 21
02C8 FF 5F 22
02CC FE 4F 21
02CF 79 04
02D1 C6 47 21 00
                                                                                                                                                                                                                          ; Do the status change service call; check if there is a routine to call; > exit if none (or one in progress); mark that a routine is being called; call service routine; mark that routine has returned; check that its not cancelled; If it has, reset value to zero
                                                                                                              XSTATX:
                                                                                                                                    CMP
                                                                                                                                                           BYTE PTR PCSTCF[BX], 1
                                                                                                                                                           L3 10$
BYTE PTR PCSTCF[BX]
DWORD PTR PCSTCA [BX]
BYTE PTR PCSTCF[BX]
                                                                                                                                     JNE
                                                                                                                                    INC
CALL
DEC
JNS
                                                                                                                                                           L3_10$
BYTE PTR PCSTCF[BX],0
                                                                                                                                     MOV
                                                                                                              L3_10$: RET
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                      02-20-84 PAGE
                                                                                                                                23-1
                                                                INTERRUPT SERVICE ROUTINES
SUBTIL BIOS EXTENTION SERVICE ROUTINES
                                                                                                                                P R G 7 2 0 1
                                                                                    This is entry point to the BIOS to reprogram the 7201. It is also the interface to the initialize (soft and hard reset) the devices routines.
                                                                                                                                                 points to bios data area
has the offset of the Comm Control Block
has the segment of the Comm Control Block
                                                                                 ; ENTRY CONDITIONS:
                                                                                                                                 CX
                                                                                 EXIT CONDITIONS:
                                                                                                                                AL = FF programming device successful = O programming device unsuccessful
                                                                                 ,
; This routine will call many subroutines to perform each function in the
; reprogramming of the 7201. The convention used will be as follows:
                                                                                    On entry to each subroutine, ES:BP points to CCB
                                                                                                                                                 DS:BX points to Port control block.
                                                                                                                                                                 device number (comm, printer etc)
This is not to be altered by the
calling routines.
                                                                                                                                                 CH
                                                                                                                                                                 set to FF if for some reason the request cannot be met. Also, the high order bit of the appropriate variable in the CCB is to be set.
                                                                                                                                                 CL
                                                                                                                                                                 to be left untouched if request is successfully programmed.
                                                                                                                                                 CL
8009011234815819018223824
                                                                                                                                                 Initially CL is set to 0 by PRG7201.
                02D6
02D6 8E C2
02D8 8B E9
02DA 25: 8A 6E 00
02DE E8 0911 R
02E1 72 35
                                                                                 PRG7201:
MOV
MOV
MOV
CALL
JC
                                                                                                                                                ; set ES to have CCB segment address
; point ES:BP to the CCB (comm control block)
; get port number
; call to get port control block for device
; > exit if port does not exist
                                                                                                                ES,DX
BP,CX
CH,ES:[BP]
GETPCB
                                                                                                                 PRGERR
                  02E3 B1 00
02E5 E8 031B R
02E8 E8 0342 R
02EB E8 03B6 R
02EE E8 03E7 R
02F1 E8 0450 R
02F4 E8 04A1 R
                                                                                                                 CL,O
STOPBIT
PARITY
MODE
DATABIT
                                                                                                                                                ; assume everything is ok
; go program # of stopbits
; go program parity
; set mode, data leads or limited modem control
; go program # of data bits
; set receiver baud
; set transmit baud rate
                                                                                                 MOV
                                                                                                 CALL
CALL
CALL
CALL
CALL
```

The M COMBIO		MACRO Assembler	02-20-8	4 PAGE	23-2	
0010	-		BIOS EXTENTION SER	VICE ROUTINE	s	
25	02F7	ES OAFD R	CA	LL XONOFF		set any new xon/xoff characters
326	O2FA	26: 8A 46 09	MO	V AL, ES:	CBRXOF[BP]	; see if auto receive xon/xof is changed
827	02FE	22 CO	AN	D AL, AL		
828	0300	74 03	JZ	PRGCM2		> if not go on to next thing
829	0302	88 47 2F	MO	V PCRXOF	[BX],AL ;	save new value (enabled/ or not)
30	0305	28: 8A 46 OA	PRGCM2: MD			; check auto transmit xon/xoff
831	0309	22 CO	AN			see if there is a change
832	0308	74 03	JZ			> If none, go on to next thing
B 3 3	030D	88 47 30	MO			Else save new value
834	0310	E8 0514 R	PRGCM3: CA			call routine to setup buffer usage
835	0313	8A C1	MO			pass return code in AL
836	0315	34 FF	OX			compliment return code (to correct value)
837		C3	RE			return to caller
838				•	•	,
839	0318	80 00	PRGERR: MO	V AL,O		: indicate error
840	031A		RE.			return to caller

```
The Microsoft MACRO Assembler COMBIOS
                                                                        02-20-84
                                                                                             PAGE
                                                                                                          24-1
                                                     BIOS EXTENTION SERVICE ROUTINES
******* This has to be the first parameter to be programmed ******
                                                                                                           S T O P B I T
                                                                   . This routine is called to set up the number of stop bits.
                                                                                                                        DS:BX points to the PORT CONTROL BLOCK ES:BP points to the Comm Control Block
                                                                               ENTRY CONDITIONS:
                                                                                EXIT CONDTIONS:
                                                                                                                         Port control block updated.
               STOPBIT:
MOV
AND
JZ
CMP
JBE
OR
MOV
RET
857
858
859
860
861
862
863
864
865
866
867
                                                                                                                                      ; get parity from Comm Control Block
; check if there is a change
; > exit if no change
; check to see if in range
; If not in range, then
                                                                                              AH,ES:CBSTPB[BP]
AH,AH
L4_10$
AH,STPBMAX
                                                                                              BYTE PTR ES:CBSTPB[BP],80H;
CL,0FFH;
                                                                                                                                                  indicate error
return to caller
                                                                                                                               ;
                                                                   L4_1$:
                                                                                                                                      ; save the value in Port Control Block
; move it to correct value (for 7201)
                        88 67 28
DO E4
DO E4
8A 47 OE
24 F3
OA C4
88 47 OE
                                                                                 MOV
                                                                                              PCSTPB[BX],AH
               0330
0333
0335
0337
033A
033C
                                                                                 SHL
SHL
MOV
AND
OR
                                                                                              AH,1
AH,1
AL,PCCR4[BX]
AL,OFFH-BIT3-BIT2
868
869
870
871
872
873
874
875
876
877
                                                                                                                                      ; get value programmed into CR4; clear loweer 2 bits; set new mask; save new walue for 7201 CR4
                                                                                              AL,AH
PCCR4[BX],AL
                                                                                 MOV
                                                                   ; Note that value that goes into control register 4 is not yet put in! ; It will be done after parity is determined
                                                                  L4_10$: RET
               0341 03
                                                                                                                                      : return to caller.
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                 02-20-84
                                                                                                                                                 PAGE 25-1
                                                                                     BIOS EXTENTION SERVICE ROUTINES
PARITY
                                                                                                            This routine is called to set up the parity.
                                                                                                                                                                                                                   points to the PORT CONTROL BLOCK points to the Comm Control Block
                                                                                                                               ENTRY CONDITIONS:
                                                                                                                                                                                              Port control block updated.
CL set to FF if port control block has errors
                                                                                                                               EXIT CONDTIONS:
                         0342
                                                                                                          PARITY:
                                                                                                                                                    BYTE PTR ES:CBDATB[BP],5; check to see if 7S or 7M asked for L5_1$; If not, program parity as request AH,3; Else, force no parity
BYTE PTR ES:CBPRTY[BP],AH; make sure no parity asked for L5_2$; if so, go program the parity
BYTE PTR ES:CBPRTY[BP],0; or parity is not to be changed:
5 28
                                      26: 80 7E 03 05
72 18
84 03
26: 38 86 04
74 32
                         0342
0347
0347
0348
034F
                                                                                                                               CMP
                                                                                                                                JB
MDV
CMP
JZ
                                       74 32
26: 80 7E 04 00
74 2B
26: 80 4E 04 80
B1 FF
EB 22
                         0351
0356
0358
035D
035F
                                                                                                                                CMP
                                                                                                                                                    BYTE PIR ES: CBPRTY[BP], 80H; Else mark parity request in error CL, OFFH; indicate error in CCB SHORT L5_2$; then go program the parity
                                                                                                                                JMP
                                      26: 8A 55 04
22 E4
44 480 FC 03
74 15
26: 80 7E 03 00
75 0E
80 7F 29 05
72 E5
26: 80 4E 04 80
B1 FF
C3
                                                                                                          L5_1$:
                                                                                                                                                   AH,ES:CBPRTY[BP] ; get parity from Comm Control Block
AH,AH ; check if there is a change
L5_15$ ; > exit if no change
AH,3 ; if new parity is "NONE" then
L5_2$ ; skip following validity check
BYTE PTR ES:CBDATB[BP],0 ; if parity is changed and,
L5_2$ ; data bit also changed, go on
BYTE PTR PCDATB[BX],5 ; else, parity changed and data bit not
L5_1$ ; and old data bit is 75 or 7M
BYTE PTR ES:CBPRTY[BP],80H; mark parity request in error
CL,0FFH ; indicate error in CCB
; and return to caller
                         0361
0365
0365
0367
0366
0366
0373
0375
0379
                                                                                                                               MOV
                                                                                                                                AND
JZ
CMP
                                                                                                                                CMP
                                                                                                                                JNZ
CMP
JB
OR
                                                                                                                                MOV
                                                                                                                                                                                                                                         and return to caller
                         0382
                                                                                                                                RET
                         0383
                                                                                                           L5_2$:
                         U383
0383 80 FC 03
0386 76 08
0388 26: 80 4E 04 80
038D 81 FF
038F C3
                                                                                                                                CMP
JBE
                                                                                                                                                     AH,PRTYMAX ; see if its in range L5_3$ ; If not then, BYTE PTR ES:CBPRTY[BP].80H; CL,OFFH ; set error ret:
                                                                                                                                DR
Mov
                         0388
038D
038F
0390
0390
0393
0395
0398
039A
                                                                                                                                                                                                                                        set error return code
return to caller
 923
924
925
926
927
928
                                                                                                                                                                                   :
                                                                                                                                RET
                                                                                                           L5_3$:
                                        88 67 2A
32 CO
80 FC O3
74 O8
BO O3
FE CC
                                                                                                                                                                                                                   ; save the value in Port Control Block; assume its no parity; transform value from CCB as follows:

[CCB value, CR4 value]
                                                                                                                                                      PCPRTY[BX],AH
                                                                                                                                                    AL,AL
AH,3
L5_13$
AL,3
AH
                                                                                                                                XOR
CMP
JZ
                                                                                                                                                                                                                    (CCB value, CR4 value); even (1,3); Odd (2,1); none (3,0)
                                                                                                                                MOV
```

The M		MACR	O Assembler	02-2	0-84	PAGE	25-2		
COMBIO	•			BIOS EXTENTION	SERVICE	ROUTINES			
932	038E	74 0	2		JZ	L5_13\$;	
933	OAEO	BO C	1		MOV	AL, 1		:	
934	O3A2			L5 13\$:				:	(now mask correct for 7201 cr4)
935	03A2	8A 6	7 OE	-	MOV	AH, PCCR	\$ [B X]	:	get value programmed into CR4
936	OBAS	80 E	4 FC		AND	AH, NOT	(BITO+BIT1)	:	clear lower 2 bits
937	BAEO	OA C	4		DR	AL, AH		:	set new mask
938	AAEO	88 4	7 OE		MOV	PCCR4[B]	K1.AL	:	save new value for 7201 CR4
939	OJAD			L5 15\$:		-		:	(common exit path to set CR4, note
940								- :	that this must always be done!)
941	OBAD	8A 4	17 OE		MDV	AL . PCCR	4 [B X]	- 1	get value to be programmed to CR4
942	0380	B4 C			MOV	AH, CR4			go to common routine to programm 7201
943	0382		269 R		JMP	WRITE72	0.1	•	(and return to caller when done)
944	0385	C3			RET		•	•	return to caller.

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                                                                                      02-20-84
                                                                                                                                        BIOS EXTENTION SERVICE ROUTINES
945
946
947
948
949
950
951
952
                                                                                                                                                                                                                                                                                     M 0 0 F
                                                                                                                                                                                     This routine is called to set up the mode, data leads or limited
                                                                                                                                                                                    modem control.
953
954
955
956
957
958
                                                                                                                                                                                                             ENTRY CONDITIONS:
                                                                                                                                                                                                                                                                                                                       DS:BX points to the PORT CONTROL BLOCK ES:BP points to the Comm Control Block
                                                                                                                                                                                                             EXIT CONDITIONS: Port control block updated.
959
960
961
952
                                                           28: 8A 86 01
22 E4
74 28
80 FC 02
76 08
26: 80 4E 01 80
81 FF
C3
                                                                                                                                                                                   0388
0388
038A
038C
036E
03C1
03C3
03C8
                                                                                                                                                                                                                                                                                                                                                   ; get the mode from Comm Control Block
; check if there is a change
; > exit if no change
; check if its in range
; if not then,
                                                                                                                                                                                                                                                   AH, ES: CBMODE[BP]
                                                                                                                                                                                                                                                 AH, AH
AH, AH
AH, AH
MODEX
AH, MODEMAX
AH,
 963
                                                                                                                                                                                                                 AND
 964
965
966
967
968
                                                                                                                                                                                                                JZ
CMP
JBE
OR
MOV
 989
970
971
972
973
                                     03CA C3
03CB 88 67 27
03CE 32 C0
03D0 FE CC
03D2 74 02
03D4 80 20
                                                                                                                                                                                                                 RET
                                                                                                                                                                           L6_1$:
                                                                                                                                                                                                                                                  PCMODE[BX],AH ; save the value in Port Control Block
AL,AL ; assume its data leads only
AH ; check if its data leads
MODE1 ; > if so go program 7201
AL,20H ; Else assume its limited modem control
                                                                                                                                                                                                                MOV
                                                                                                                                                                                                                XOR
                                                                                                                                                                                                                 JΖ
                                                                                                                                                                                                                 MOV
                                        O3D4 BO 20
O3D6 BA 67 OD
O3D8 BO E4 DF
O3DC OA C4
O3DE 88 47 OD
O3E1 B4 O3
O3E3 E9 O269 R
 976
977
978
979
980
981
982
983
984
                                                                                                                                                                             MODE 1:
                                                                                                                                                                                                                                                  AH, PCCR3[BX]
AH, OFFH-BIT5
AL, AH
PCCR3[BX], AL
                                                                                                                                                                                                                MOV
AND
OR
MOV
                                                                                                                                                                                                                                                                                                                                               ; get old value in 7201 CR3
; clear the bit that programs this
; set new value
; save new value
                                                                                                                                                                                                                                                                                                                                               ; go to routine to program 7201
; go do it.
                                                                                                                                                                                                                 MOV
                                                                                                                                                                                                                                                   AH, CR3
WRITE7201
                                        03E6 C3
                                                                                                                                                                           MODEX: RET
                                                                                                                                                                                                                                                                                                                                                   ; return to caller.
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                             BIOS EXTENTION SERVICE ROUTINES
985
986
986
988
989
991
992
993
994
995
995
996
997
998
1000
1001
1003
1004
1006
                                                                                                                                                            DATABIT
                                                                                                   ; This routine is called to set up the number of data bits
                                                                                                                                                                           DS:BX points to the PORT CONTROL BLOCK ES:BP points to the Comm Control Block
                                                                                                                ENTRY CONDITIONS:
                                                                                                                 EXIT CONDTIONS:
                                                                                                                                                                          Port control block updated.
                                                                                                      O3E7
O3E7 26: 8A 66 O3
O3EB 22 E4
O3ED 74 60
O3EF 80 FC 06
O3F2 76 08
O3F4 28: 80 4E O3 80
O3F9 81 FF
                                                                                                 DATABIT:
MOV
AND
JZ
CMP
JBE
OR
MOV
BET
                                                                                                                                 AH,ES:CBDATB[BP] ; # data bit from Comm Control Block
AH,AH ; check if there is a change
DATABX ; > exit if no change
AH,DATMAX ; check if its above maximum
L7_10$ ; If not then
BYTE PTR ES:CBDATB[BP],80H ; indicate error in # of databits
CL,OFFH ; set error code
; return to caller
                       03F9
03FB
03FC
03FC
03FF
0401
                                                                                                 MGV
RET
L7_10$:
  1007
                                    C 3
  1007
1008
1009
1010
1011
1012
                                                                                                                                                                                                 ; save the value in Port Control Block; get register value for 5 data bits; get data mask for 5 data bit; If so go to it (CCB value 1); > go program 7201; next assume its 6 data bits; (mask SAND 3F, OR 00); (CCB value 2); > If it is go program 7201; next assume its 7 data bits; (mask AND 7F, OR 00);
                                    88 67 29
32 CO
BA 1FOO
FE CC
74 20
BO 40
B6 3F
FE CC
74 18
                                                                                                                     MOV
XOR
MOV
DEC
                                                                                                                                          PCDATB[BX], AH
                                                                                                                                          AL,AL
DX,1FOOH
                                                                                                                                          AH
                                                                                                                                          AH
L7_1$
AL,40H
DH,3FH
                       0406
0408
040A
040C
040E
                                                                                                                      MOV
MOV
JZ
  1013
1014
1015
1016
1017
1018
1019
1020
1021
1022
                                                                                                                                          AH
L7_1$
AL,20H
                                                                                                                      MOV
MOV
DEC
JZ
MOV
                       O40E 74 18
O410 B0 20
O412 B6 7F
O414 FE CC
O416 74 10
O418 B0 60
O41A B6 FF
O41C FE CC
O41E 74 08
                                                                                                                                          AL,20H
DH,7FH
AH
L7_1$
AL,60H
                                                                                                                                                                                                    ; If it is then go program 7201
; next assume its 8 bits
; (mask AND FF, OR OO)
  1023
1024
1025
1026
1027
                                                                                                                       MOV
                                                                                                                                          DH, OFFH
                                                                                                                      DEC
                                                                                                                                         AH
L7_1$
                                                                                                  ; 7M or 7S is done by programming to 8N then do mark or space in software
                                                                                                                                        DH,07FH
AH
L7_1$
DL,80H
  1028
  1029
1030
1031
1032
                       0420 B6 7F
0422 FE CC
0424 74 02
0426 B2 80
                                                                                                                      MOV
                                                                                                                                                                                                 ; (mask AND 7F)
; assume its 7 space
;> If so, go program 7201
; Else it must be 7M ( mask OR 80)
                                                                                                                      JZ
MOV
                       0426 B2 80
0428 50
0429 89 57 15
042C 8A 67 OF
042F 80 E4 9F
                                                                                                  L7_1$:
  1033
                                                                                                                                         AX ; save value for # data bits in receive PCMASK[BX],DX ; save new mask AH,PCCR5[BX] ; get old value in control register 5 AH,NDT (BIT6+BIT5) ; clear bits for "data bits per byte"
  1034
1035
1036
1037
                                                                                                                      PUSH
                                                                                                                      MOV
                                                                                                                       AND
```

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The Mic Combios	rosoft	MACRO Assembler	02-20-84 BIOS EXTENTION SERVICE	PAGE 27-2 E ROUTINES	
1038 1039 1040 1041 1042 1043 1044 1045 1048 1048 1048 1048 1050	0434 0437 0438 043C 043D 043F 0442 0445	OA C4 88 47 OF 84 05 E8 0288 R 58 D0 E0 8A 87 OD 80 E4 3F OA C4 88 47 OD 84 03 E9 0288 R	OR MOV CALL POP SHAPE OR MOV AND OR MOV JMP	AL, AH PCCRS[BX], AL AH, CR5 WRITE7201 AX AL, 1 AH, PCCR3[BX] AH, MOT (BIT7+BIT5) AL, AH PCCR3[BX], AL AH, CR3 WRITE7201	; put in new value for # data bits; save it in the port control block; set to go to routine to program 7201; go do it. ; get # stop bits for receive chars; put it in correct place for 7201; get old value in control register 5; clear bits for "data bits per byte"; put in new value for # data bits; save it in the port control block; set to go to routine to program 7201; go do it. ; return to caller.

```
The Microsoft MACRO Assembler
                                                                                             02-20-84
                                                                                                                          PAGE 28-1
COMBIOS
                                                                      BIOS EXTENTION SERVICE ROUTINES
1052
1053
1054
1055
1056
1057
1058
1059
                                                                                        ; This routine is called to reprogram the receiver baud rate
                                                                                                                                                                              points to Port Control Block
has value from Comm control block
device number
ENTRY CONDITIONS:
                                                                                                                                                              DS:BX
                                                                                                                                                              CH
CH
                                                                                              EXIT CONDITIONS:
                                                                                                                                                              26: 8A 46 05
22 CO
74 2A
E8 04E1 R
3C FF
75 08
B1 FF
26: 80 4E 05 80
C3
                                                                                        RCVBAUD:
                                                                                                                          AL,ES:CBRCVB[BP]; get new baud rate to be programmed
AL,AL; see if there is any change in rcv baud
RCVBX; > exit if not
GETBAUD; get the value to be written to generator
AL,OFFH; check if the value is legal
RCVB1; If not legal then
CL,OFFH; indicate programming failed
BYTE PTR ES:CBRCVB[BP],80H; mark the receive baud rate as not supported;
and return to caller
                   AND
JZ
CALL
CMP
                                                                                                         JNZ
MOV
OR
RET
                                                                                        RCVB1:
                               80 FD 02
74 17
26: 8A 86 05
88 67 2B
8A 87 0A
80 E4 F0
0A C4
                                                                                                                           CH, DEVPRT ; if the device is the printer skip following RCVBPRT ; treat it as special case AH, ES:CBRCVB[BP] ; get new baud rate to be programmed PCRCVB[BX], AH ; save it in port control block AH, PCRATE[BX] ; get old value AH, OFOH ; save transmit baud rate portion AL, AH ; get new baud rate value {both xmt & rcv}
                                                                                                                           CH, DEVPRT
                                                                                                         CMP
                                                                                                         JZ
MOV
MOV
                                                                                                          AND
                                                                                                          DR
                    0479 OA C4
0478 88 47 OA
047E 88 57 O8
0481 EE
0482
                                                                                        RCVB2:
                                                                                                                           PCRATE[BX],AL ; save the new value DX,PCBAUD[BX] ; get port address of baud rate generator DX,AL ; do it!
                                                                                                         MO V
MO V
                                                                                                         OUT .
                                                                                         RCVBX:
                    0482 C3
                                                                                                         RET
                                                                                        ; printer is a special case at this point we know that receive baud rate ; is non zero and is valid
                  0483
0483 28: 8A 46 05
0487 28: 80 7E 08 00
048C 75 05
048E 28: 88 46 05
0482 C3
                                                                                         RCVBPRT:
                                                                                                                           AL,ES:CBRCVB[BP] ; get new baud rate to be programmed
BYTE PTR ES:CBTXB[BP],0 ; if xmt baud not specified
L8_5S ; then
ES:CBTXB[BP],AL ; make transmit baud = rcv baud
return to caller
                                                                                                         MOV
CMP
JNZ
1103
                                                                                                          MOV
 1104
```

The Micro	osoft	MACR	LO A	S S 4	mbler		02-	20-84	PAGE	28-2							
MD:03						BIOS	EXTENTION	SERVICE	ROUTINES								
05																	
05	0493	28:	38	46	0 5		L8 5\$:	CMP	ES:CBTX	B[BP],AL	:	1 # 2	kmt ba	ud is	the	same as	;
07 (0497	74 0	7				_	JZ	L8_10\$;		rcv	baud	then	retur	to caller
	0499	26:	80	4E	05 80			OR	BYTE PT	R ES:CBRCVB	[BP 1 . BOH		Else m	ark e	rror	in CCB	
	049E							MDV	CL, OFFH		;	•		rece			
	04 A O	C3					L8_10\$: RET			;	ret	urn to	call	er		

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                             02-20-84
                                                                                                                                               PAGE
                                                                                  BIOS EXTENTION SERVICE ROUTINES
1113
1114
1115
1117
1118
1119
11120
1121
1122
1123
1124
1125
1128
1129
1130
1133
1133
1133
1133
1133
                                                                                                                                                                     XMTBAUD
                                                                                                       ; This routine is called to reprogram the transmit baud rate. This routine is never called for the printer transmit baud rate change. The next level; routine above this will determine if the printer has to be reprogrammed; and will call RCVBAUD; since transmit and receive baud rates must be the
                                                                                                       ; same in the printer
                                                                                                                                                                                          DS:BX points to Port Control Block ES:BP points to comm control block device number
                                                                                                                           ENTRY CONDITIONS:
                                                                                                                                                                                          {\sf CL} = FF device cannot be programmed as requested H0 bit in the CCB is set to 1 in the receive baud rate variable. otherwise {\sf CL} is left untouched
                                                                                                                          EXIT CONDITIONS:
                      1136
1137
1138
1140
1144
1144
1144
1144
1145
1147
1151
1152
1154
1155
1155
1157
                                                                                                       XMTBAUD:
                                     26: 8A 46 06
22 C0
74 37
E8 04E1 R
3C FF
75 08
B1 FF
26: 80 4E 06 80
C3
                                                                                                                                                AL,ES:CBTXB[BP]; get new baud rate to be programmed
AL,AL; check to see if there is a change
XMTBX; > exit if there is no change
GETBAUD; get the value to be written to generator
AL,OFFH; If its bad value then,
XMTBO;
CLOFFH; set return code
                                                                                                                            MOV
AND
JZ
CALL
                                                                                                                                                GETBAUD
AL, OFFH ; If its bad value ...
XMTBO ;
CL, OFFH ; Set return code
BYTE PTR ES:CBTXB[BP], 80H ; indicate tranmit baud rate in error
and return to caller
; and return to caller
                                                                                                                            CMP
                                                                                                                            JNZ
                                                                                                                             RET
                                                                                                       XMTBO:
                                     26: 8A 66 06
88 67 2C
80 FD 02
75 05
88 67 2B
                                                                                                                                                AH,ES:CBTXB[BP]; save new value from CCB to the Port control block PCTXB[BX],AH; CH,DEVPRT; If its printer then, L9_1S; PCRCYB[BX],AH; mark printer rcv baud same as xmt baud
                                                                                                                            MOV
MOV
CMP
JNZ
MOV
                                                                                                                                                                                        mark printer rcv baud same as xmt baud;
> skip rest (programming generator different)
                                      EB 10
                                                                                                                             JMP
                                     8A 67 OA
8O E4 OF
DO EO
DO EO
DO EO
OA C4
                                                                                                                            MD V
AND
                                                                                                                                                 AH, PCRATE[BX]
AH, OFH
                                                                                                                                                                                      ; get old value programmed into generator
; save receive baud rate portion
; move value to ho nibble
; .(CL not to be touched)
                                                                                                                                                AL,1
AL,1
AL,1
AL,1
AL,1
                                                                                                                            SHL
                                                                                                                            SHL
SHL
SHL
OR
1158
1159
1160
1161
1162
1163
1164
                                                                                                                                                                                         ; get the new baud rate value to be programmed
                      O4D9 88 47 OA
O4DC 88 57 O8
O4DF EE
O4EO
                                                                                                                                                                                         ; save new value into the port control block ; get port address of baud rate generator ; do it!
                                                                                                                                                 PCRATE[BX],AL
                                                                                                                            MOV
                                                                                                                            MOV
```

The Microsoft MACRO Assembler COMBIOS 02-20-84 BIOS EXTENTION SERVICE ROUTINES 04E0 C3 1166 RET The Microsoft MACRO Assembler 02-20-84 PAGE 30-1 COMBIOS BIOS EXTENTION SERVICE ROUTINES 1167 1168 1169 1170 1171 1172 1173 1174 1176 1177 1178 GETBAUD . This routine is used to get the value to be that the generator is ; to be programmed corresponding to the baud rate value specified in ; the Comm control block. DS:BX points to port control block
it is assumed that it resides
in the same segment address as
the translate tables.

CH device number
AL new baud rate value ENTRY CONDITIONS: EXIT CONDITIONS: AL AL has the value to be written the baud rate generator AL = FF this baud rate no supported

04E1 04E1 3C 00 04E3 72 15 04E5 3C 11 04E7 77 11

04E8 53 04EA BB 0000 E 04ED 80 FD 02 04F0 75 03 04F2 BB 0000 E 04F5 FE C8 04F7 D7 04F8 58 04F9 C3

O4FA BO FF O4FC C3

GETBAUD:

L34_1\$:

L34_10\$: MDV

CMP

JB CMP JA

PUSH MOV CMP JNZ MOV

DEC XLAT POP RET

AL,0 L34_10\$ AL,BAUDMAX L34_10\$

AL XBAUD1 BX

AL,OFFH

BX
BX,OFFSET XBAUD1
CH,DEVPRT
L34_1\$
BX,OFFSET XBAUD2

; check range; > If out of range indicate bad baud

; save BX, for translation ; assume its printer port ; see if its the printer ; > If so go do table lookup ; Else point to table for Comm & Xcomm

; table starts at 0 (1 in ccb); translate value; restore register; return to caller.

; out of range, indicate bad value ; return to caller

```
The Microsoft MACRO Assembler COMBIOS
                                                                                         02-20-84
                                                                                                                    PAGE
                                                                  BIOS EXTENTION SERVICE ROUTINES
1209
1210
1211
1212
1212
1213
1214
1215
1216
1217
                                                                                                                                     X O N O F F
                                                                                      This routine is used to setup the characters to be used in the
                                                                                   ; xon/xoff protocol.
1217
1218
1219
1220
1221
1222
1223
                                                                                    ;
; Note that the protocol is the same, as a standard xon/xoff, and that
; the only change allowed is which character can be used to perform
; this.
                                                                                                                                                                       points to port control block
it is assumed that it resides
in the same segment address as
the translate tables.
                                                                                                    ENTRY CONDITIONS:
                                                                                                                                                      DS:BX
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233
                                                                                                                                                      CH
ES:BP
                                                                                                                                                                   device number
Comm control block
                                                                                                    EXIT CONDITIONS:
                                                                                                                                                      AL
                                                                                                                                                                       destroyed.
                                                                                        04FD
04FD
0501
0503
0505
0508
050C
050E
0510
                                                                                   xonoff:
                              26: 8A 46 07
22 C0
74 03
88 47 2D
26: 8A 46 08
22 C0
74 03
88 47 2E
C3
                                                                                                                                                                     ; get new xon character
; check if there is a change
; hif not, go check xoff
; save new xon character
; get new xoff character
; check if there is a change
; how change
; save new value
; return to caller
                                                                                                                    AL,ES:CBXON[BP]
AL,AL
xONOF1
PCXON[BX],AL
AL,ES:CBXOFF[BP]
AL,AL
                                                                                                    MOV
 1234
                                                                                                    JZ
MOV
MOV
AND
1234
1235
1236
1237
1238
1239
1240
                                                                                   XONOF1:
                                                                                                     JZ
MDV
                                                                                   XONOFX:
```

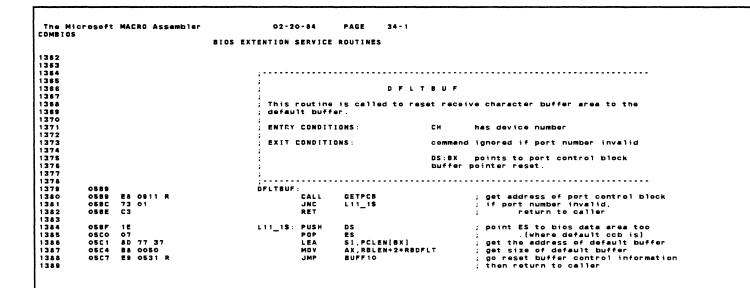
```
The Microsoft MACRO Assembler COMBIOS
                                                                                                02-20-84
                                                                                                                           PAGE
                                                                                                                                            32-1
                                                                       BIOS EXTENTION SERVICE ROUTINES
1242
1243
1244
1245
1246
1247
1248
1249
1250
                                                                                                                                                 BUFFER
                                                                                             This routine is called to calculate the buffer control information.
                                                                                          ; ENTRY CONDITIONS:
                                                                                                                                                DS:BX points to port control block ES:BP points to Comm control block
1252
1253
1254
1255
1256
1257
1258
                                                                                          ; EXIT CONDITIONS:
                                                                                                                                                CL = FF If unsucessful (buffer area too small)
CL left alone otherwise.
                    0514
0514
0518
051A
051C
051F
                                26: 88 46 OB
23 CO
74 5A
3D OOSO
77 OA
B1 FF
26: 81 4E OB OOSO
EB 4B
                                                                                          BUFFER:
                                                                                                                             AX,ES:CBSIZE[BP] ; check new size of buffer
AX,AX ; If its zero, exit
BUFFX ;
AX,RBLEN+2*(RBDFLT) ; check if its > default size
BUFF1 ; If not then,
CL,OFFH ; set return code
WORD PTR ES:CBSIZE[BP],80H ; indicate offending variable
SHORT BUFFX ; go return to caller
                                                                                                            MDV
                                                                                                            AND
JZ
CMP
1259
1260
1261
1262
1263
                                                                                                            JA
MDV
                   051F
0521
0523
0529
052B
052B
052D
24 FE
26: C4 76 OD
                                                                                                                              AL,0FEH ; round down to even number SI,DWORD PTR ES:CBRADR[BP] ; get address of the buffer
                                                                                                            AND
                                                                                           ; now calculate and enter all the buffer control information
                                                                                          BUFF10: PUSHF
                    0531 9C
0532 FA
                                                                                                                                                                                     ; diable interrupts first
                                                                                                                                                                                        This is an embedded entry point for INITCOM, used when the default buffer is set up (ES:SI point to buffer area )
DS:BX points to port control block AX has the size of the buffer
                                                                                          ; First clear all flags related to xon/xoff, since we now have a new buffer
                    0533
0537
053A
053D
0540
                                80 67 14 39
89 47 31
89 77 33
8C 47 35
2D 0010
                                                                                                            AND
                                                                                                                              BYTE PTR PCFLAG[BX], NOT (XMTXON+XMTXOF+RCYXOF+APPXOF)
                                                                                                                                                                                     (XMIXON+XMIXOF+RCVXOF+APPXOF);
Save size of new buffer;
save new offset to buffer;
save segment address of new buffer;
subtract buffer space from buffer;
header information.
                                                                                                                               PCSIZE[BX],AX
PCRADR[BX],SI
PCRADR+2[BX],ES
AX,RBLEN
                                                                                                             MO V
MO V
                                                                                                            MO V
SUB

    ; header information.
    ; calculate address of first character in
    ; (address where first char go to)
    ; put it into buffer control info
    ; Use same offset for initial char out
    ; use same offset for head of buffer pointer

                    0543
0545
0548
0540
0550
0554
                                 8B D6
83 C2 10
26: 89 54 04
26: 89 54 06
26: 89 54 0C
03 D0
                                                                                                                               DX,SI
                                                                                                            MDV
1290
1291
1292
1293
1294
                                                                                                                               DX,RBLEN
ES:RBIN[SI],DX
ES:RBOUT[SI],DX
ES:RBHEAD[SI],DX
                                                                                                             ADD
                                                                                                             MO V
MO V
                                                                                                             MOV
                                                                                                             ADD
```

	: MACRO Assembler	02-2	0-84	PAGE 32-2	
8102		BIOS EXTENTION	SERVICE	ROUTINES	
5 0556	44		DEC	DX	; (want address of the last word
8 0557	4.4		DEC	DX	of the buffer)
7 0558	26: 89 54 OE		MOV	ES:RBTAIL(SI).DX	; put address into buffer control info
8					• • • • • • • • • • • • • • • • • • • •
	D1 E8		SHR	AX. 1	; divide by 2 to get max number chars
			MOV		: save new value
1					
	28: 57 04 0000		MOV	WORD PTR ES:RBCOUNT[SI]	,0 ; put indicate no characters in buffer
					: xoff threshold is buffer half full
					; save calculated xoff threshold value
					; calcuate auto xon threshold (1/4 of
					: xoff threshold)
					; save value.
				LU. KUNUMIUII, MA	; restore user flags
	••		FUFF		, restore aser riags
		BUPPY.			
	5 0586 6 0557 7 0558 8 0557 0558 1 0582 0 0582 0 0587 4 0589 0 0580 0 0587 0 0571 0 0571 9	5	BIOS BIOS EXTENTION 5	BIOS EXTENTION SERVICE 5	BIOS EXTENTION SERVICE ROUTINES 5

The Mic	rosoft	MACR	O Assemble	•	02-20-84	PAGE 33	· 1	
COMBIOS				BIOS EXT	ENTION SERVICE	ROUTINES		
1311								
1312								
1313							. 	
1314					;			
1315					;	D 1	F L T 7 2 0 1	
1316					;			
1317						is called to	reprogram a	particular device to the default
1318					value.			
1319					;			
1320								
1321					ENTRY CONDIT	IONS:	CH DS	device number
1322							us	points to bios data area
1323 1324								
1324					; 			
1325					, • • • • • • • • •			
1327	0577				DFLT7201:			
1328		FR C	911 R		CALL	GETPCB		: check if port is present
1329	057A				JE	L10 10\$: > exit if not
1330			589 R		CALL	DELTBUE		; call routine to setup default receive
1331								: char buffer (and get address of pcb)
1332								, ,
1333	057F	E8 0	7DF R		CALL	RCVCANCEL		; cancel rcv interrupt service
1334	0582	E8 C	819 R		CALL	XMTCANCEL		; cancel xmt buffer empty service
1335	0585	E8 (000 E		CALL	RDNVMCDM		; go read NVM for default comm values
1336								
1337	0588				PUSH	CX		; save device number
1338	0589				MOV	CX,PCDFLT[; get address of default settings (CCB)
1339			F OE 40		OR			6X ; make sure its 16X for clock divisor
1340	0590	8C [MOV	DX,DS		; set segment of CCB to bios data area
1341			2D6 R		CALL	PRG7201		; go reprogram 7201 to default values
1342	0595	59			POP	CX		; restore device number
1343								
1344	0596	C6 4	7 14 00		MOV	BYTE PTR P	CFLAG[BX],O	; clear all flags <
1345								
1346	059A 059D	8A 4			MOV OR	AL,PCCR5[B	A J	; enable transmitter (note that at this ; point the port is guaranteed to
1347 1348	059F				MOV	AL,08 PCCR5[BX].		; point the port is guaranteed to ; be present, so it is ok to
1348		B4 (MOV	AH. CR5	46	: write to the device
1349			75 269 R		CALL	WRITE7201		; write to the device
1350)169 K		CALL	RCVENA		; : enable the receiver
1351	USA /	E0 (JOEA K		CALL	REVENA		, minute the receiver
1352	0544	ВО			MOV	AL.SX7201		
1354			7 12		MDV	DX . PCS7201	(RY)	
1355		EE .			DUT	DX.AL		
1356						,		
1357	05B0	84	17 OB		MOV	AL . PCCR1[B	x1 : now e	et register 1 before anything else
1358	05B3				MOV	AH. CR1	, , , , , , , , , ,	
1359	0585		289 R		CALL	WRITE7201		•
1360		'					•	
1361	05B8				L10 10\$:	RET		



```
The Microsoft MACRO Assembler COMBIOS
                                                                                              PAGE 35-1
                                                                            02-20-84
                                                       BIOS EXTENTION SERVICE ROUTINES
1390
1391
1392
1393
1394
1395
                                                                       ,.....
                                                                                                                  RDSETUP
1396
1397
1398
1399
1400
1401
1402
1403
1404
1405
1406
                                                                          This routine is called to retreive the information that is programmed into the specified port.
                                                                                                                              CX,DX contains offset,segment of CCB
first byte of CCB must have
port number
                                                                                  ENTRY CONDITIONS:
                                                                                                                                AL = 0 port information loaded
AL = FF invalid port number, or port
not connected.
                                                                                    EXIT CONDITIONS:
                                                                          .....
               05CA
05CA
05CC
05CE
05D2
05D4
                                                                       RDSETUP:

MDV

MDV

MDV

XOR

CALL
1408
1409
1410
1411
1412
1413
1414
1415
1416
1417
1418
                        8B E9
8E C2
26: 8A 6E -00
32 C0
E8 0911 R
72 10
83 C3 27
                                                                                                   BP,CX
ES,DX
CH,ES:[BP]
AL,AL
GETPCB
                                                                                                                                 ; point ES:BP to comm control block
                                                                                                                               get port number
assume invalid port address
get address of port control block (DS:BX)
> exit if bad port number or not connected
point to beginning of Port control block
that matches comm control block
get ready to transfer
transfer the whole table minus port number
get destination offset into DI
point DI passed port number
                                                                                                    L12_10$
BX,PCMODE
                05D7
                0509
                                                                                      ADD
               05DC 88 F3
05DE 89 0010
05E1 88 FD
05E3 47
                                                                                                   SI,BX
CX,CBLEN-1
DI,BP
DI
                                                                                      MOV
                                                                                      MOV
1420
1421
1422
1423
1424
                                                                                      CLD
REP P
MOVSB
                05E4
05E5
                                                                                                                               ; get the correct direction ; move whole string
                         FC
F3/ A4
                                                                                             MOVSB
                                                                                                                                ; indicate information sucesfully passed
1425
1426
1427
                05E7 BO FF
05E9
05E9 C3
                                                                       L12_10$:
                                                                                                    AL, OFFH
                                                                                                                                ; return to caller
```

```
The Microsoft MACRO Assembler COMBIOS
                                                              02-20-84
                                                                              PAGE
                                                                                           36-1
                                              BIOS EXTENTION SERVICE ROUTINES
RCVENA
                                                           ; This routine is called to enable the receiver.
                                                                    ENTRY CONDITIONS:
                                                                                                        CH
                                                                                                                    device number
                                                                    EXIT CONDITIONS:
                                                                                                       Command ignored if port is invlaid
             OSEA
OSED
OSEF
                     E8 0911 R
73 01
C3
                                                                      CALL
                                                                                                        ; get port control blook address
; If port number is invlaid then,
; return to caller
                                                                      JNC
                                                                                  L13_1$
             05F0
                                                          L13_1$:
                                                                                 AL,PCCR3[BX] ; read last value put into control register 3
AL,BITO ; enable receiver
BYTE PTR PCFLAC[BX],OFFH-RCVOFF; set flag that receiver is enabled
SHORT RCVCOMMON; go use common code for disable & enable
                     8A 47 OD
OC 01
80 67 14 DF
EB OF
                                                                      MOV
OR
AND
                                                                      JMP
                                                           ;------
                                                                                              RCVDIS
                                                           ; This routine is called to disable the receiver.
                                                                   ENTRY CONDITIONS:
                                                                                                      CH
                                                                                                                   device number
                                                                     EXIT CONDITIONS:
                                                                                                        Command ignored if port is invlaid
             05FB
05FE
0500
0601
                     E8 0911 R
73 01
C3
                                                                      CALL
                                                                                  GETPCB
                                                                                                         ; get port control blook address; if port number invalid then,; > return to caller
                                                                     JNC
RET
                                                          L14_15:
                                                                                 AL,PCCR3[BX] ; get last value put into control register 3
AL,OFFH-BITO ; clear last bit
BYTE PTR PCFLAG[BX],RCVOFF ; set flag that receiver is disabled
                     8A 47 OD
                                                                     MOV
             0601
                                                                      AND
             060A
                                                          RCVCOMMON:
                     88 47 OD
84 O3
E9 O269 R
                                                                      MOV
MOV
JMP
                                                                                  PCCR3[BX],AL
AH,CR3
WRITE7201
                                                                                                        ; restore CR3 value in port control block
; write it to control register 3
; go write it to the control register
             060A
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                   02-20-84
                                                                                     PAGE
                                                                                                  37-1
                                                 BIOS EXTENTION SERVICE ROUTINES
This routine is called to get the status of the receive
                                                                ; character buffer
                                                                ; ENTRY CONDITIONS:
                                                                                                            has segment address of bios data area
has the device number
                                                               ; EXIT CONDITIONS:
                                                                                                     AX destroyed.
                                                                                                    AL = 0 no character avialable, DR invlid port #
AL = FF character avialable
                                                                   -----
              0612
0612
0614
0617
0619
061C
061F
0621
0623
                      32 CO

E8 0911 R

72 OC

C4 5F 33

26: 8B 07

23 CO

74 02

B0 FF

C3
                                                                                        AL,AL ; assume invlaid port number
GETPCB ; get address of the port control block
INSTX ; > exit if port number invalid
BX,DWORD PTR PCRADR[BX] ; get address of the receive character buffer
AX,ES:RBCOUNT[BX] ; get # characters available
AX,AX ; If no character available then
INSTX ; go exit (return code all set)
AL,OFFH ; else set proper return code
                                                                           XOR
CALL
JC
LES
MOV
AND
JZ
MOV
RET
                                                               INSTX:
```

```
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                                                                                                                  02-20-84
                                                                                                                                                    PAGE
                                                                                                                                                                         38 - 1
COMBIOS
                                                                                     BIOS EXTENTION SERVICE ROUTINES
1504
1505
1506
1507
1508
1509
1510
1511
1512
1513
1514
1515
1517
1518
1518
1519
                                                                                                          INCHAR
                                                                                                           ; This is the lowest level of the routines available to draw a ; character out of the receive character buffer. This routine will return ; to the caller if no character is available.
                                                                                                           : ENTRY CONDITIONS:
                                                                                                                                                                          CH
                                                                                                                                                                                              device number
                                                                                                           ; EXIT CONDITIONS:
                                                                                                                                                                         AL = FF
                                                                                                                                                                                               If character available BP, CX, AX is destroyed. CH = status of character CX = ascii character
1521
1522
1523
1524
1525
                                                                                                                                                                                             If none available, or invalid port number, all registers except AX is preserved.
1526
1526
1527
1528
1529
1530
1531
                                                                                                          INCHAR:
                        0626
0626
0629
0628
                                       E8 0911 R
73 03
32 C0
                                                                                                                                                     GETPCB
                                                                                                                                                                                                                    ; get address pf pcb in to DS:BX
; If invalid port number
; set error code
; return to caller
                                                                                                                                JNC
                                                                                                                                                     L 15_1$
AL, AL
1531
1532
1533
1534
1535
1536
                        062B
062E
062E
0631
0634
                                        C 3
                                                                                                                                RET
                                      C4 8F 33
E8 065A R
9C
FA
26: 8B 46 00
23 C0
74 20
48
26: 8B 76 06
26: 8B 0C
26: 8B 0C
26: 3B 0C
26: 3B 76 0E
26: 8B 76 0E
26: 8B 76 0C
26: 8B 76 0C
                                                                                                          L15_1$:
                                                                                                                               LES
CALL
PUSHF
                                                                                                                                                     BP,DWORD PTR PCRADR[BX] ; get receive character buffer to ES:BP
TXXDN ; go do auto transmit xon/xoff chores
; disable interrutps
                                                                                                                                                                                                                   coisable interrutps

coisable interrutps

get # characters in the buffer

check to see if any characters

ye oexit if none

count down # characters in the buffer

update count

get pointer to next character drawn

get the next character & status

check to see if at end of buffer

if we are then, set pointer to the

beginning of the buffer

(go put it into buffer table)

Else, increment pointer by 2

(point to next character)

update pointer to the next character

set return code: character available
                        0635
                                                                                                                                CLI
                       0635
0636
063A
063C
063E
063F
1539
1540
1541
1542
1543
1544
1545
1546
1547
1548
1549
                                                                                                                                                     AX,ES:RBCOUNT[BP]
AX,AX
INCHAX
                                                                                                                                MOV
                                                                                                                                AND
JZ
DEC
                                                                                                                                                    INCHAX
AX
ES: RBCOUNT[BP], AX
S!, ES: RBCOUT[BP]
CX, ES: O[SI]
SI, ES: RBTAIL[BP]
INCHA1
SI, ES: RBHEAD[BP]
SHORT INCHA2
                                                                                                                                MOV
                       0643
0647
054A
064E
0650
0654
                                                                                                                                MO V
MO V
                                                                                                         MOV
CMP
JNZ
MOV
JMP
INCHA1: INC
                        0657
 1550
1551
1552
1553
1554
1555
                                        26: 89 76 06
BO FF
                                                                                                          INCHA2: MOV
                                                                                                                                                     ES:RBOUT[BP],SI
AL,Offh
                                                                                                                                                                                                                    ; restore caller's flags ; return to caller.
                                                                                                          INCHAX: POPF
                        065E 9D
065F C3
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                            02-20-84
                                                                                                   PAGE
                                                                                                              39-1
                                                         BIOS EXTENTION SERVICE ROUTINES
1556
1557
1558
1559
1559
1560
1561
1562
1563
1564
                                                                                                                  GETCHAR
                                                                       , ; This routine is called to read a character out of the receive buffer, and ; to wait till a character is available
1565
                                                                        ; ENTRY CONDITIONS:
                                                                                                                 same as INCHAR
1566
1567
1568
1569
1570
                                                                                                                 same as INCHAR, but character will always be available and the character will be in AX
                                                                      GETCHAR:
1571
1572
1573
1574
1575
                                                                                                                                           ; go to common routine to get character
; check if sucessful
; repeat till sucesful
; put character in correct register
; return to caller
                          E8 0626 R
22 C0
74 F9
88 C1
                                                                                                   INCHAR
                                                                                     AND
JZ
MOV
                                                                                                   AL,AL
GETCHAR
AX,CX
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                       02-20-84
                                                                                             PAGE
                                                                                                           40-1
                                                     BIOS EXTENTION SERVICE ROUTINES
1577
1578
1579
1581
1582
1583
1584
1586
1586
                                                                                                           TXXON
                                                                    ;
; This routine is called to see if its time to send an XON thru the port.
; It will exit immediately if xon, for any reason cannot be sent; and
; a flag is set to send the auto xon as soon as possible.
                                                                   : The criteria to send an XDN are as follows:
1588
1589
1590
1591
1592
1593

1 - an auto XOFF was previously sent by the driver
2 - receive character buffer reached a low mark
3 - application did not send an XOFF

                                                                                                                        DS:BX points to port control block ES:BP points to receive buffer area
                                                                               ENTRY CONDITIONS:
1594
1595
1596
1597
1598
1599
                                                                                                                         CX preserved
DS:BX preserved
ES:BP preserved
                                                                               EXIT CONDITIONS:
; NOTE: If application sent an xoff, and auto xoff was also sent, then
; at the low water mark, auto xoff sent flag is cleared, but no
; auto xon is sent !!!
                                                                           ......
               066A
066A
086E
                                                                   TXXON:
                         80 7F 30 02
75 36
                                                                                              BYTE PTR PCTXOF[BX],2 ; check if transmit auto xon/xoff is TXXONX ; enabled, exit if not
                                                                                CMP
JNZ
                                                                                             BYTE PTR PCFLAG[BX],XMTXON ; If already flagged to send XON as
appXXONS ; ) go check if it can be sent
                                                                                 TEST
               0670
                         F6 47 14 02
75 1E
               0674
                                                                                 JNZ
                                                                                             F6 47 14 04
74 2A
26: 88 46 00
26: 38 46 0A
               0676
                                                                                TEST
               067A
                                                                                 J Z
MD V
               0880
                                                                                 CMP
                         26: 38 46 07 75 20 80 67 14 FB F6 47 14 80 75 16 80 4F 14 02
               0684
0686
088A
068E
0690
0694
                                                                                JNZ
AND
TEST
JNZ
OR
                                                                   TXXONS:
                        F6 47 14 08
75 OC
8A 67 2D
E8 01F9 R
74 04
                                                                                TEST
1623
1624
1625
1626
1627
1628
                                                                                JNZ
MOV
CALL
JZ
                                                                   ; clear transmit xon asap and xoff was sent flag
```

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BYTE PTR PCFLAG[BX], NOT (XMTXON + XMTXOF) OBA2 80 67 14 F9 OBA6 C3 1631 1632 AND TXXONX: RET

: return to caller

```
The Microsoft MACRO Assembler COMBIOS
                                                                                 02-20-84
                                                                                                           PAGE
                                                                                                                        41-1
                                                             BIOS EXTENTION SERVICE ROUTINES
1634
1635
                                                                             :-----
1636
1637
1638
1639
1640
                                                                                                                          0 U T S T A T
                                                                            ; This routine is called to get the status of the transmitter. ; The items checked are as follows:
1641
1642
1643
1644
1645
                                                                                           1 - Transmit buffer empty
2 - Not in an xoff'd mode
                                                                             ; This is the routine called when the application program wishes to ; check the status. Note there are other items that may be usefull ; that can be added to this, namely:
1846
1647
1648
1649
                                                                                           3 - break being transmitted. If this condition arises, it is
up to the appication program to keep track of this.
1650
1651
1652
1653
1654
1655
                                                                                            4 - CTS not prresent (for limited modem control). Note that
in limited modem control, if CTS is not on, transmit
buffer will eventually get to a point where it won't
                                                                                                            be ready.
1656
1657
1658
1659
1660
1661
1662
1663
1664
1665
1666
                                                                                The following are the motivation to leave them out of this routine:
                                                                                           1 - most CP/M programs are written in such a way that they
tend to check the status often and not always needing
the remaining information. In those cases, efficiency
of execution time is the most important factor.
                                                                                           2 - The information on the other items can be obtained in
other bios function calls.
1668
1869
1670
1671
1672
1673
                                                                                            ENTRY CONDITIONS:
                                                                                                                                          C H
DS
                                                                                                                                                         device number
has BIOS segment address
                                                                                                                                          DX destroyed

AL = FF if character can be transmitted that is transmit buffer empty, not in an xoff'd mode.

Carry flag is cleared.
                                                                                           EXIT CONDITIONS:
1674
1675
1676
1677
1678
                                                                                                                                          AL = 0 OTHERWISE, AND
                                                                                                                                          1679
1680
1681
1682
1683
1684
1685
                                                                            ; Note: the carry flag set/not set is to be used only between ; BIOS routines for quick execution time, and is not to be used by the application program, since we cannot predict
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                              02-20-84
                                                                                                                                             PAGE
                                                                                                                                                                41-2
                                                                                  BIOS EXTENTION SERVICE ROUTINES
1686
1687
1688
1689
1690
1691
1692
1693
                                                                                                                         what the operating system will do with this flag.
                                                                                                      OUTSTAT:
                                                                                                                      -----
                                                                                                                                               GETPCB ; get address pf pcb in to DS:BX
L16_5S ; > exit if port number invalid
AH,PCFLAG[BX] ; Acheck if XDFF was received
AH,RCYXOF;
DX.PCS7201[BX] ; get address of 7201 status/control register
AL,DX ; read the status
AL,BIT2 ; get the transmit buffer empty bit
AH,BIT2 ; Now set bit 2 in AH if transmit buffer
AH,AL ; is not empty Note 7201 (value in AL) ; has bit 2 set if buffer IS empty !
AL,OFFH ; assume ok to transmit (AH zero)
L16_10S ; If its so go exit
AL,AL ; Else set flag to indicate cannot transmit
RET ; return to caller
                      OBA7
                                    E8 0911 R
72 15
8A 67 14
80 E4 40
8B 57 12
                       OSAT
OSAC
OSAC
                                                                                                                           MOV
                                                                                                                           AND
                                                                                                                           MOV
IN
AND
OR
XOR
                       0682
1695
1696
1697
1698
1699
                                     EC
24 04
80 CC 04
32 E0
                       0685
                       0686
0688
0688
                      088D
088F
06C1
06C3
06C4
                                    BO FF
74 03
32 CO
FS
C3
                                                                                                                           MOV
                                                                                                     L16_5$: XOR
STC
1701
 1702
 1703
1704
                                                                                                      L16_10$:
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                                                           PAGE
                                                                                                                                                                                 42-1
                                                                                                                        02-20-84
                                                                                         BIOS EXTENTION SERVICE ROUTINES
DUTCHAR
                                                                                                                This routine is called to put a character into the transmit buffer. If for any reasons the character cannot be immediately placed on to the buffer, a return is made to the caller, with an indication that transmission was unsucessful. No attempt is made here to find out; why the character cannot be transmitted (for fast execution time), if the 7201 has buffer empty, and the port is not xoff'd then the character will be placed into the buffer.
                                                                                                                                                                                                        CH
DS
DL
                                                                                                                                                                                                                               device number
has BIOS data area segment address
character to be transmitted
                                                                                                                                    ENTRY CONDITIONS:
                                                                                                                                                                                                        DL is destroyed in all cases.
(this is ok, since in the next call, the value is reloaded from the BIOS descriptor).

CL = 0 cannot send character
CL = FF character placed into the buffer
                                                                                                                                                                                                        DL
                                                                                                                                    EXIT CONDITIONS:
                                                                                                                                    Note, that to implement auto xon/xoff properly, the transmited
characters have to be examined to see if xoff or xon
is sent by the application (or user).
Further, it is faster and easier to always check for th
instead of only when auto transmit xon/xoff is enabled.
                                                                                                               OUTCHAR:

MOV
PUSHF
                         06C5
06C5
08C7
                                          SA CA
SC
                                                                                                                                                                                                         ; save character to be transmitted ; make sure interrutps are off
                          06C8
06C8
06CC
                                          FA
E8 06A7 R
86 C8
72 25
                                                                                                                                      CALL
XCHG
JC
                                                                                                                                                                                                      ;
go see if we can transmit, also get PCB;
transfer return codes
; > exit if we can't transmit.
                                                                                                                                                             DUTSTAT
                                                                                                                                                            CL,AL
OUTCHX
                                                                                                                                                           AL,PCXON[BX] ; check if xon charcter is sent
L17_1$ ; If it is, make sure "application sent xoff"
BYTE PTR PCFLAG[BX], NOT APPXOF ; flag is cleared.
SHORT L17_2$ ; now go send character
AL,PCXOFF[BX] ; check if xoff being sent
L17_2$ ; > If not go send the character
                                         3A 47 2D
75 06
80 67 14 7F
EB 0D
3A 47 2E
75 08
                                                                                                                                      CMP
                          06D0
                         06D0
06D3
06D5
06D9
06DB
                                                                                                               LMP
JNZ
AND
JMP
L17_1$: CMP
JNZ
                                                                                                                 , else, set flag that application program sending xoff, and clear flag ; to send auto xon as soon as possible.
                                                                                                                                                             BYTE PTR PCFLAG[BX], APPXOF
BYTE PTR PCFLAG[BX], NOT XMTXON
```

The I		MACRO	Assembler	02-2		PAGE 42-2		
				BIOS EXTENTION	SERVICE	ROUTINES		
1758								
1759	OSES			L17 2\$:			:	now prepare data to be transmitted
1760	OSES	88 57	15	- · · - · ·	MOV	DX,PCMASK[BX		mask the outgoing data
1761		22 CE			AND	AL, DH	•	
1762	OSED	OA CZ			DR	AL.DL	- 1	
1763	OSEF	88 57	10		MOV	DX, PC7201 [BX	1	get data register address of 7201
1764	06F2	EE			OUT	DX, AL	:	put out the character
1765	08F3	B1 FF	•		MOV	CL, OFFH		set return code that character transmitted
1766								
1767	06F5	9 D		OUTCHX:	POPF		;	restore caller's flags
1768	OFF	C3			RET			return to caller

```
The Microsoft MACRO Assembler COMBIOS
                                                          02-20-84
                                                                       PAGE
                                                                                   43-1
                                           PIDS EXTENTION SERVICE ROUTINES
;------
                                                                                      PUTCHAR
                                                      ;
; This routine is called to transmit a character, and to repeat until
; the character is sucessfully accepted by the 7201
                                                      : ENTRY CONDITIONS:
                                                                                                same as OUTCHAR
                                                        EXIT CONDITIONS: Same as OUTCHAR charcater will be taken by 7201
                                                       ; EXIT CONDITIONS:
                                                      PUTCHAR:
            08F7
08F8
08F8
08FC
08FE
                   52
E8 06C5 R
5A
22 C9
74 F7
C3
                                                                           DX ; save character to be transmitted
DUTCHAR ; call common routine to output char
DX ; restore character
CL,CL ; check if it was sucessful
PUTCHAR ; repeat til sucessful
                                                                PUSH
CALL
POP
AND
                                                                JZ
RET
```

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                             02-20-84 PAGE 44-1
                                                                                BIOS EXTENTION SERVICE ROUTINES
1790
1791
1792
1793
1794
                                                                                                                                                                   0 U T N D W
1796
                                                                                                          This routine is called to transmit the character regardless of the state of the modem signals or anything else.
1796
1797
1798
1799
1800
1801
1802
                                                                                                           Note that the absense of CTS, in limited modem control mode made prevent the transmission of the character indefinitely. It is for this reason that the CTS signal is ignored for the transmission of this particular character.
1803
1804
1805
1806
1807
                                                                                                                                                                   CH
DS
DL
                                                                                                                                                                                        has device number
points to BIOS data area
has character to be transmitted
                                                                                                          ENTRY CONDITIONS:
 1808
                                                                                                         EXIT CONDITIONS:
                                                                                                                                                                                        command ignored if port number invalid
1808
1809
1810
1811
1812
1813
                                                                                                           NOTE THAT THIS WILL NOT PROCESS auto XON/XOFF
                      0701
0701
E8 0911 R
0704
73 01
0705
C3
0707
0707
8A CA
0709
8B 57 12
070C
F6 47 0D 20
0710
74 1E
0712
B0 03
0714
9C
0715
FA
0715
EE
0717
8A 47 0D
0711
CE
0711
9D
1814
1815
1816
1817
1818
                                                                                                       OUTNOW:
                                                                                                                          CALL
JNC
RET
                                                                                                                                                                                        ; get address pf pcb in to DS:BX
; If port number invalid
; > return to caller
; Else continue
                                                                                                                                               GETPCB
L18_1$
                                                                                                      L18_1$:
                                                                                                                                               ; Else continue

CL,DL ; save character to be transmitted

DX,PCS7201[BX] ; get status/control reg address of 7201

BYTE PTR PCCR3[BX],BIT5 ; temporary disable "auto enable" if used

OUTN2 ;> If not used go wait for buffer empty

AL,CR3 ; (Now disable it)

, make sure interrupts disabled
                                                                                                                           MOV
                                                                                                                           MOV
TEST
JZ
MOV
1820
1821
18223
18224
1825
1827
1828
1831
1833
1833
1833
1833
1834
1838
1840
1842
                                                                                                                            PUSHF
                                                                                                                           CLI
OUT
MOV
                                                                                                                                               DX,AL
AL,PCCR3[BX]
AL,OFFH-BITS
DX,AL
                                                                                                                                                                                       get value programmed into control reg 3 take out auto enable bit do it! restore interrupt state go wait for buffer empty and then send char point Dx back to status register
                                                                                                                           AND
                      O71C EE
071D 9D
071E E8 0730 R
0721 42
0722 42
0723 EC
0724 A8 04
0726 75 FB
0728 84 47 0D
072B B4 03
072D E9 0269 R
                                                                                                                           POPF
CALL
INC
INC
                                                                                                                                               OUTN2
                                                                                                                                               D X
                                                                                                                           IN
TEST
JNZ
MOV
MOV
                                                                                                                                               AL,DX
AL,BIT2
L18_5$
AL,PCCR3[BX]
AH,CR3
WRITE7201
                                                                                                                                                                                        : wait for buffer empty
                                                                                                      L18_5$:
                                                                                                                                                                                        ; get original value programmed into control reg 3 ; set up to use common routine to do this ; go to it, and bye bye!
                                                                                                                           JMP
                                                                                                      OUTN2: PUSHF
                                                                                                                                                                                         ; make sure interrutps are off
```

The M		MAC	RO	Assembler	02-20	-84	PAGE 44-	
					BIOS EXTENTION S	ERVICE	ROUTINES	
1843	0732	EC				IN	AL , DX	; wait for buffer empty
1844	0733	A 8	04			TEST	AL, BIT2	;
1845	0735	75	03			JNZ	L19_1\$	
1846	0737	9 D				POPF	_	; restore state of interrupt mask
1847	0738	EB	F 6			JMP	SHORT DUTN2	; loop, till buffer empty
1848	073A				L19_1\$:			
1849	073A	8 A	C 1			MOV	AL,CL	; restore character to be transmitted
1850	073C	8 B		15		MOV	CX, PCMASK[B	
1851	073F	22				AND	AL, CH	
1852	0741	ÖĀ				OR	AL, CL	
1853	0743	4 A				DEC	DX	point to data register
1854	0744	4.4				DEC	DX	
1855	0745	EE				OUT	DX.AL	send it!
1856	0746	9 D				POPF	5,75	: restore caller's interrupt mask
1857	0747	C3				RET		; return to caller

```
The Microsoft MACRO Assembler COMBIOS
                                                                             02-20-84
                                                                                                     PAGE
                                                                                                                   45-1
                                                         BIOS EXTENTION SERVICE ROUTINES
1859
1860
1861
1862
1863
1864
1865
1866
1867
1868
1869
1870
                                                                         ; This routine is called to read the modem signals.
                                                                                    ENTRY CONDITIONS:
                                                                                                                                                device number
                                                                                                                                  AL bitO-bit6
                                                                                                                                                             has the modem signals
                                                                                                                                  AL bit7 = 1
                                                                                                                                                              if modem signals not available
1872
1873
1874
1875
1876
1877
1878
1879
1880
1881
1882
                                                                                                                                  AH bit 0 = 1 transmitter empty = 0 transmitter not empty
                                                                       RDMODEM:

XOR
CALL
JC
MOV
CMP
JZ
IN
XOR
AND
JJ
HP
L35_1$: MOV
L35_2$: MOV
AND
PUSH
MOV
IN
                                                                                                    33 CO
E8 0911 R
72 25
8B 57 06
81 57 07
EC
34 FF
24 1F
EB 02
BO 80
80 87 14
80 E4 E0
50
8B 57 12
EC
A8 04
58
74 03
80 CC 01
                07488ADF100775589BDF14786BCEF114
IN
TEST
                                                                                                                                ; check if .....;
; restore outgoing info
; > if not empty exit
; Else set flag that transmitter is empty
; return to caller.
                                                                                       POP
JZ
OR
                                                                        L35_10$:
```

```
PAGE
  The Microsoft MACRO Assembler
                                                                                                                02-20-84
                                                                                                                                                                      46-1
COMBIOS
                                                                                   BIOS EXTENTION SERVICE ROUTINES
1899
1900
1901
1902
1903
1904
1905
                                                                                                                                                                        SETMODEM
                                                                                                         ; This routine is called to set the modem signals. Note that ; the first 3 bits in the mask are set (to clear the diagnostic leds)
1907
1908
1909
1910
1911
1912
1913
1913
1915
1916
1917
1918
1919
1920
1921
                                                                                                                                                                                                                 device number
modem signal mask
                                                                                                                             ENTRY CONDITONS:
                                                                                                                                                                                             AL = FF if sucessful
AL = 0 if port number invalid or,
modem signal cannot be set
                                                                                                                            EXIT CONDITIONS:
                                    8A C2
E8 0911 R
72 11
8B 57 06
81 FA FFFF
74 08
24 0F
34 FF
EE
B0 FF
C3
                                                                                                         SETMODEM:
                                                                                                                                                                                        ; put the modem signals into correct reg; get port control block; > go exit if invalid port number; get address of the port to set modem signals; see if modem signals can be set; > if not go exit with error condition set; clear first 4 bits; (zero bit value asserts the line); do it.; set command processed return code; return to caller
                                                                                                                              MOV
CALL
JC
MOV
CMP
                        0775
0777
0777A
077C
077F
0783
0785
6787
0788
                                                                                                                                                   AL.DL
                                                                                                                                                   GETPCB
L20_10$
DX,PCMDDM[BX]
DX,OFFFFH
                                                                                                                                                   L20_10$
AL,OFH
AL,OFFH
DX,AL
AL,OFFH
                                                                                                                               JZ
 1922
1923
1924
1925
1926
1927
1928
1929
                                                                                                                              AND
XOR
OUT
MOV
RET
                                                                                                         L20_10$:
RET
                                                                                                                                                                                             ; set error return code ; return to caller
                        078D
078F
                                        32 Co
C3
                                                                                                                                                    XOR AL,AL
```

```
The Microsoft MACRO Assembler COMBIOS
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                                                                                                      PAGE
                                                                                                                     47-1
                                                           BIOS EXTENTION SERVICE ROUTINES
1931
1932
1933
1934
1935
1937
1938
1939
1940
1941
1942
1944
1945
                                                                                                                       BRKON
                                                                           ; This routine is called to initiate the sending of break.
                                                                                        ENTRY CONDITIONS:
                                                                                                                                    CH
                                                                                                                                                   port number
                                                                                         EXIT CONDITIONS:
                                                                                                                                      command ignored if port number invalid
                                                                           BRKON:
                                                                                                        GETPCB
                 0790
                          E8 0911 R
73 01
                                                                                         CALL
                                                                                                        CL,PCFLAG[BX]; set flag to indicate break being sent CL,XMTBRK ; AL,PCCFS[BX]; set byte to be written.
                                                                                                                                      ; get address of port control block
; If port number invalid,
                 0790
0793
0795
0796
0796
0796
0797
1946
1947
1948
1949
1950
                                                                                         JNC
RET
                           C3
                                                                           L21_1$:
                           8A 4F 14
80 C9 08
8A 47 OF
OC 10
EB 11
                                                                                         MOV
                                                                                         OR
MDV
1951
1952
1953
1954
1955
1956
                                                                                           JMP
                                                                                                         SHORT BRKCOMMON ; go and write it to 7201
1957
1958
1959
1960
1961
1963
1964
1965
1966
1967
1968
1969
1971
                                                                                                                       BRKOFF
                                                                              This routine is called to stop the sending of break.
                                                                                        ENTRY CONDITIONS:
                                                                                                                                     CH
                                                                                                                                                    port number
                                                                                         EXIT CONDTIONS:
                                                                                                                                      Command ignored if port is invalid
                 07A3
07A3
07A6
07A8
                                                                           BRKOFF:
                           E8 0911 R
73 01
C3
                                                                                                                                      ; get address of port control block
; If invalid port number
; > return to caller
                                                                                                         GETPCB
                                                                                         CALL
                                                                                          JNC
                                                                                                        CL,PCFLAG[BX] ; clear flag that show break being sent CL,NOT XMTBRK ; AL,PCCR5[BX] ; get old value in control register 5 AL,NOT BIT4 ; clear send break bit in 7201
                            8A 4F 14
80 E1 F7
8A 47 OF
24 EF
                                                                          L22_1$: MOV
1973
1974
1975
1976
1977
                 07AC
07AF
07B2
                                                                                          AND
MOV
AND
                                                                                          will have to consider sending the auto Xon.
This is messy, but necessary for proper handling!
If buffer empty, and auto xoff was sent, we must send it
now, since it may be the last time we get control.
host is Xoff'd, and missed Xon because of break being sent!
 1978
1979
1980
1981
1982
1983
```

The Microsoft MACRO Assembler COMBIOS BIOS EXTENTION SERVICE ROUTINES 1984 1985 1985 1987 0784 0784 88 4F 14 0787 84 05 0789 E9 0269 R PCFLAG[BX],CL ; restore flag
AH,CR5 ; This is to be written to control reg 5
WRITE7201 ; Go to common subroutine to do it. MOV The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 48-1 BIOS EXTENTION SERVICE ROUTINES RCVINT , This routine is called to set up the receive character interrupt ; service routine. ENTRY CONDITIONS: CX,DX points to user information block EXIT CONDITIONS: AL = 0 invalid port number AL = FF function successfully performed. RCVINT: 07BC O7BC 8B E9 O7BE 8E C2 O7CO 26: 8A 6E 00 O7C4 32 C0 O7C6 E8 0911 R O7C9 72 13 MOV MOV MOV XOR CALL BP,CX ES,DX CH,ES:[BP] AL,AL GETPCB ; point ES:BP to user's control block ; get port number ; assume invalid port number ; get address of port control block ; > exit if port number invalid JC. L23_10\$ OTCB 28: C4 46 01 OTCF 9C OTDO FA OTD1 89 47 18 OTD4 8C 47 1A OTD7 C5 47 17 01 OTDB 9D OTDC BO FF LES PUSHF CLI MOV AX,DWORD PTR ES:1[BP] ; get address of routine to be called ; disable interrupts to do rest ; put it into the port control block; put segment address into PC block; set flag to indicate its on; restore user's flag state; set sucessful return code PCRCVA[BX],AX PCRCVA+2[BX],ES BYTE PTR PCRCVF[BX],1 MOV MOV POPF MOV AL, OFFH O7DE C3 RET L23 10\$: : return to caller ;..... RCVCANCEL 2028 2029 2030 2031 2032 2033 2034 2035 2036 2037 2038 2039 2040 , This routine is called to cancel the receive character interrupt; service routine. ENTRY CONDITIONS: CH port number EXIT CONDITIONS: command ignored if port number invalid O7DF O7DF E8 O911 R O7E2 72 O4 O7E4 C5 47 17 OO RCVCANCEL: JC JC MOV GETPCB GETPCB ; get address of port control block L24_1\$; ignore command if invalid port # BYTE PTR PCRCVF[BX],0 ; clear flag to call service routine

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2041 07E8 C3 L24_1\$: RET ; return to caller The Microsoft MACRO Assembler COMBIOS PAGE 02-20-84 49-1 BIOS EXTENTION SERVICE ROUTINES 2042 2043 2044 2045 2045 2046 2047 2048 2049 2050 2051 X M T M T Y , This routine is called to set up the transmit empty buffer interrupt ; service routine. CX,DX points to the BIOS descriptor ENTRY CONDITIONS: AL = 0 if port number invalid or not connected. EXIT CONDITIONS: AL = FF if command sucessfully implemented O7E9
O7E9
SE C2
O7ED 26: 8A 6E OO
O7F1 3 C0
O7F3 E8 0911 R
O7F6 72 20
O7F8 26: C4 46 O1
O7FD PA
O7FE 89 47 1D
O801 8C 47 1F
O804 C6 47 1C 01
O808 9D
O809 8A 47 OB
O800 CC O2
O80E 88 47 OB
O811 84 O1
O813 E8 0269 R
O816 B0 FF
O818 C3 BP,CX ES,DX CH,ES:[BP] AL,AL GETPCB L25_1\$ AX,DWORD PTR ES:1[BP] ; point ES:BP to user's control block get port number assume invalid port number get address of port control block > ignore command if invalid port get address of routine to be called disable interrupts to do rest put it into the port control block put segment address into PC block set flag to indicate its on restore user's flag state get image of control register 1 PCXMTA[BX],AX ;
PCXMTA+2[BX],ES ;
BYTE PTR PCXMTF[BX],1 ; AL,PCCR1[BX] OR MOV MOV CALL MOV AL,PCCR1[BX]
AL,CR1TXBE
PCCR1[BX],AL
AH,1
WRITE7201
AL,OFFH save new value go write it to 7201 ; set return sucessful code ; return to caller L25_1\$: RET , ------XMTCANCEL This routine is called to cancel the transmit empty buffer interrupt service routine. ENTRY CONDITIONS: CH port number EXIT CONDITIONS:

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0819

XMTCANCEL:

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BIOS EXTENTION SERVICE ROUTINES

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The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 48-2 BIOS EXTENTION SERVICE ROUTINES ES 0911 R
72 11
CS 47 1C 00
8A 47 0B
24 FD
88 47 0B
84 01
ES 0259 R
C3 0818 081C 081E 0822 0825 0827 082A 082C 082F ; get address of port control block ; ignore command if port number invalid ; clear flag to call service routine ; get image of control register 1 CALL 2095 2096 BETPLE
L28_1\$
BYTE PTR PCXMTF[BX],O
AL,PCCR1[BX]
AL,NOT CR1TXBE
PCCR1[BX],AL 2087 2098 2089 2100 2101 MOV MOV AND MOV ; save new value ; go write it to 7201 MOV AH, 1 WRITE7201 CALL L26_1\$: RET 2102 ; return to caller

```
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                                                                                                                        PAGE
    The Microsoft MACRO Assembler
                                                                                                                                          50-1
 COMBIOS
                                                                       BIOS EXTENTION SERVICE ROUTINES
H A C K 7 2 0 1
                                                                                         ; This routine is called to setup the interrupt vectors so that on ; the following interrupt from the appropriate 7201, the application ; service routine is called instead of the one in the BIOS.
                                                                                                                                                                               point to a 5 byte descriptor where the first byte is the device number, and the next 4 are the offset, and segment of the user service routine.
                                                                                                          ENTRY CONDITION:
                                                                                                                                                              CX,DX
                                                                                                          EXIT CONDITIONS:
                                                                                                                                                              NONE, no check is done at all.
                                                                                         DEVICE NUMBER ARE AS FOLLOWS:
                                                                                                                                                              1 = comm port (PORT A)
2 = printer port (PORT B)
3 = optional comm (RS423) PORT B
4 = optional comm (RS422) PORT A
                     0830
0830
0832
0834
0838
                                                                                         HACK7201:
MGV
MGV
MGV
                                                                                                                          BP,CX
ES,DX
BL,ES:[BP]
BH,BH
BX
                                 8B E9
8E C2
26: 8A 5E OO
32 FF
4B
D1 E3
26: C4 SE O1
                                                                                                                                                               ; point ES:BP to descriptor area
                                                                                                                                                             ; get device number
; make it into offset to table
                    0838
0838
0838
0839
0841
0842
0843
0844
0846
0846
                                                                                                          XOR
                                                                                                          DEC
SHL
LES
PUSHF
                                                                                                                                                       ES:1[BP] ; point ES:BP to user service routine
; make sure interrupts are off
; save segment of service routine
                                 9C
O6
FA
&B &7 O865 R
&B BF O86D R
B9 O004
                                                                                                                           ES
                                                                                                          PUSH
                                                                                                          CLI
MOV
MOV
                                                                                                                           AX, HACKTAB[BX]
DI, FIXTAB[BX]
CX, 4
DS
                                                                                                                                                              ; get address of the kludge to jump to user; now modify the offset of service routines; (4 offsets to be fixed); point ES to bios area
                                                                                        MOV
PUSH
POP
CLD
L27_1$: STOS
LOOP
                                                                                                                           ES
                     0850
0851
0852
0853
                                O7
FC
AB
E2 FD
                                                                                                                                                                               (make sure of directions correct)
                                                                                                                            WORD PTR[DI]
L27_1$
                                                                                                                                                                               repeat for 4 offset values
                                                                                                                           ES ; restore segment of service routine 

BX,1 ; calculate save area for vector (dev# -1)*4 

HYECTOR[BX],BP ; save user service routines into the table 

BX,2 ; HYECTOR[BX],ES ;
                     0855
0856
0858
085C
085F
                               07
D1 E3
89 AF 0000 E
83 C3 02
8C 87 0000 E
                                                                                                          POP
  2153
2154
2155
2156
                                                                                                          SHL
MOV
ADD
```

The I		MACRI	Assembler	02-2	0-84	PAGE 50	- 2			
				BIOS EXTENTION	SERVICE	ROUTINES				
2157	0863	8 D			PGPF			; reinstate	interrupt st	tatus
2158	0884	C3			RET			; return to	caller	
2159										
2180	0885			HACKTAB	LABEL	WORD				
2181	0885	012A	R		DW	HACKA				
2162	0867	012F	R		DW	HACKB				
2163	0869	0134	R		DW	HACKXB				
2164	0888	0139	R		DW	HACKXA				
2165										
2166	OSED			FIXTAB	LABEL	WORD				
2167	086D	00F2	R		DW	INT7201+8				
2168	086F	OOEA	R		DW	INT7201				
2169	0871	OOFA	R		DW	INT7201+16				
2170	0873	0102	R		DW	INT7201+24				

```
The Microsoft MACRO Assembler COMBIOS
                                                                                                                                                                                                       02-20-84
                                                                                                                                                                                                                                                                    PAGE
                                                                                                                                                   BIOS EXTENTION SERVICE ROUTINES
;-----
                                                                                                                                                                                                                                                                                                         V E C T 7 2 0 1
                                                                                                                                                                                           . This routine is called to clean the vectors so that they will point ; to the default service routines.
                                                                                                                                                                                                                              ENTRY CONDITION:
                                                                                                                                                                                                                                                                                                                                            СН
                                                                                                                                                                                                                                                                                                                                                                               has device number
                                                                                                                                                                                                                           EXIT CONDITIONS:
                                                                                                                                                                                                                                                                                                                                              NONE, no check is done at all.
                                                                                                                                                                                             ; DEVICE NUMBER ARE AS FOLLOWS:
                                                                                                                                                                                                                                                                                                                                              1 = comm port (PORT A)
2 = printer port (PORT B)
3 = optional comm (RS423) PORT B
4 = optional comm (RS422) PORT A
                                        0875
0875
0877
32 FF
0877
0877
32 FF
0870
0870
0870
0810
0880
0884
89
0004
0888
1E
0889
07
0888
1C
0880
1C
088
                                                                                                                                                                                          VECT7201:
MOV
XOR
DEC
SHL
MOV
MOV
MOV
                                                                                                                                                                                                                                                                   BL,CH
BH,BH
BX
BX,1
DI,VECTAB [BX]
SI,OURTAB [BX]
CX,4
                                                                                                                                                                                                                                                                                                                                                : calculate the offset from device number
                                                                                                                                                                                                                                                                                                                                        ; point to table to be fixed
; point to default table
; 4 offsets to be fixed
; make sure of direction for STOS, LODS
; point ES to BIOS area
                                                                                                                                                                                                                                 CLD
PUSH
POP
PUSHF
 2201
2202
2203
2204
2205
2206
2207
2208
2208
2211
2212
2213
2213
2215
2216
2217
2217
2218
2218
                                                                                                                                                                                                                                                                                                                                                        make sure were not interrupted
                                                                                                                                                                                                                                 CLI
                                                                                                                                                                                                                                                                                                                                                ; repeat get default vector
; put it into vector table
; until all 4 vectors (per port) are fixed
; reinstate interrupts status
; return to caller
                                                                                                                                                                                         L28_1$: LODS
STOS
LOOP
POPF
                                                                                                                                                                                                                                                                    WORD PTR [SI]
WORD PTR [DI]
L28_1$
                                                                                                                                                                                                                                 RET
                                       0892
0892 OOF2 R
0894 OOEA R
0896 OOFA R
0898 0102 R
                                                                                                                                                                                                                                                                    WORD
INT7201+8
INT7201
INT7201+16
INT7201+24
                                                                                                                                                                                           VECTAB LABEL
DW
DW
DW
                                                                                                                                                                                                                                 DW
                                          089A
089A 0112 R
089C 010A R
089E 011A R
08AO 0122 R
                                                                                                                                                                                                                                                                    WORD
PORTA
PORTB
PORTXB
PORTXA
                                                                                                                                                                                          OURTAB LABEL
DW
DW
 2222
```

```
PAGE
  The Microsoft MACRO Assembler
                                                                            02-20-84
                                                                                                                 52-1
COMBIOS
                                                          BIOS EXTENTION SERVICE ROUTINES
STATCHG
                                                                         ; This routine is called to set up the status change interrupt ; service routine.
                                                                                      ENTRY CONDITIONS:
                                                                                                                                 CX,DX points to user information block
                                                                                      EXIT CONDITIONS:
                                                                                                                                AL = 0 invalid port number
AL = FF function successfully performed.
                08A2
08A2
08A4
08A5
08AC
08AC
                                                                         STATCHG:
                           8B E9
8E C2
26: 8A 8E 00
32 C0
E8 0911 R
72 13
                                                                                                     BP,CX
ES,DX
CH,ES:[BP]
AL,AL
GETPCB
                                                                                       MOV
                                                                                                                                                  ; point ES:BP to user's control block
                                                                                       MOV
XOR
Call
                                                                                                                                                ; get port number
; assume invalid port number
; get address of port control block
; > exit if port number invalid
                                                                                       JC
                                                                                                     L29_10$
                OSB1 28: C4 46 01
OSB5 9C
OSB6 FA
OSB7 89 47 22
OSBA 8C 47 24
OSBD C6 47 21 01
                                                                                      LES
PUSHF
CLI
MOV
MOV
                                                                                                     AX,DWORD PTR ES:1[BP] ; get address of routine to be called ; disable interrupts to do rest
                OSB1 26: C4 46 0'
OSB5 9C
OSB6 FA
OSB7 83 47 22
OSBA 8C 47 24
OSBD C6 47 21 01
OSC1 9D
OSC2 BO FF
                                                                                                                                                ; put it into the port control block; put segment address into PC block; set flag to indicate its on; restore user's flag state; set sucessful return code
                                                                                                     PCSTCA[BX],AX
PCSTCA+2[BX],ES
BYTE PTR PCSTCF[BX],1
                                                                                       MOV
2252
2253
2254
2255
2256
2257
2258
                                                                                                     AL,OFFH
                 08C4 C3
                                                                        L29_10$:
                                                                                                     RET
                                                                                                                                                               : return to caller
                                                                         ......
2259
2260
2261
2263
2265
2265
2265
2265
2270
2277
22773
2274
                                                                                                                  STCANCEL
                                                                         ; This routine is called to cancel the status change interrupt ; service routine.
                                                                                      ENTRY CONDITIONS:
                                                                                                                                                port number
                                                                                      EXIT CONDITIONS: command ignored if port number invalid
                08C5
08C5 E8 0911 R
08C8 72 04
08CA C6 47 21 00
08CE C3
                                                                                                     GETPCB
                                                                                  CALL
                                                                                                     GETPCB ; get address of port control block 130_1$ ; ignore command if invalid port # ; clear flag to call service routine ; return to caller
                                                                                       MOV
                                                                         L30 1$: RET
    he Microsoft MACRO Assembler
                                                                            02-20-84
                                                                                                    PAGE 53-1
COMBIOS
                                                         BIOS EXTENTION SERVICE ROUTINES
INITCOM
                                                                           This is the hard reset routine to reset all the variables and tables for all three ports (comm, printer and extended comm if it isinstalled)
                                                                                                                                                points to BIOS data area contains 2, if optional comm port present.
                                                                         ENTRY CONDITIONS
                                                                                                                    DS
                                                                                                                    DS: XOPTION
                                                                         ; EXIT CONDITIONS:
                                                                                     -----
                 OSCF
                                                                         INITCOM:
MOV
                           BA 0042
E8 0904 R
                                                                                                                                                ; reset 7201 on mother board
                                                                                       CALL
                08D5 80 3E 0000 E 02
08DA C8 06 0003 E 01
08DF 75 0B
08E1 C6 06 0003 E 00
08E6 BA 002A
08E9 E8 0904 R
                                                                                                                                             ; check if optional comm board present
; mark that optional board not present
; > if not skip optional board's 7201
; mark that optional board present
; reset 7201 on the optional board
;
                                                                                       CMP
                                                                                                     XOPTION.2
                                                                                                     XOPTION, 2
LS1_1$
PCXCOM+PCSTAT, 0
DX, XAS7201
RESET7201
                                                                                       MOV
                                                                                       JNZ
MOV
MDV
CALL
                                                                                                     ; {now set default values to each port}

CH, DEVMAX ; start with highest device number

GETPCB ; get port control block address

L31_3$ ; skip this port if device don't exist

BYTE PTR PCCR1[BX],CR17201 ; set CR1 so to be programmed later

DFLT7201 ; to default values

DL, DL ; deassert modem signals, and for the

SETMODEM ; xcomm port, set internal clocking

CH ; repeat for all devices

L31_2$ ;
                 OBEC
                                                                        L31_1$:
                         B5 03
E8 0911 R
72 0C
C6 47 0B 15
E8 0577 R
32 D2
E8 0775 R
FE CD
75 EB
                08EC
08EC
08EE
08F1
08F3
08F7
08FA
08FC
0901
                                                                         MDV
L31_2$: CALL
                                                                                       MD V
                                                                                       CALL
                                                                        XOR CALL
L31_3$: DEC
JNZ
                                                                         ; Note that if AL is OFFh, some part of the default was not programmed ; this is set by \ensuremath{\mathsf{PRG7201}}
                                                                                       RET
                                                                         RESET7201:
PUSHF
CLI
MOV
                 0804
                 0904
0904
0905
0906
0908
0908
0908
090C
2319
2320
2321
2322
2323
2324
2325
2326
2327
                           9 C
FA
80 18
EE
BO 02
EE
BO 10
                                                                                                                                                 ; first disable interrupts
                                                                                                     AL,RST7201
DX,AL
AL,CR2
DX,AL
AL,CR27201
DX,AL
                                                                                                                                                 ; reset 7201
; reset 7201
; channel A
; point to control register 2A
                                                                                       DUT
                                                                                       MO V
                                                                                                                                                 ; put 7201 in status affect vector mode
                                                                                       MOV
                                                                                       DUT
```

The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 53-2 BIOS EXTENTION SERVICE ROUTINES ; all done, restore interrupt flags The Microsoft MACRO Assembler COMBIOS 02-20-84 PAGE 54-1 BIOS EXTENTION SERVICE ROUTINES

e Microsof [.] Bios	t MACRO Assembler	02-20-84	PAGE	55-1	
	BIO	S EXTENTION SERV	ICE ROUTINE	:s	
2		END	1 F		
3		;			
4		;			
5		;		GETPCB	
6					black for Abs
7 8		; GETPCB 15	called to	get the port con	trol block for the appropriate
9		; device.			
•		ENTRY CON	DITIONS.	CH = device nu	mher
ĭ		, ENIRI CON	D1110N3.		f bios data area
2		:			
3		EXIT COND	ITION:	If port number	valid & known to be connected then:
4			-	BX = a	ddress of the port control block.
5		;			egment address of the bios data area
6		1			her registers are untouched
7		4		CARRY	FLAG CLEARED
8		;			
9		;		ELSE, Carry fla	ag set.
) 1		·			
1		;			
9 0911		GETPCB:			
	80 FD 03	CMP	CH, DEV	/MAX	; check if port number valid
	77 14	AL	L33 15		; > if not go set error flag & return
	22 ED	AND			i
7 0918	74 10	JZ	L33 15	į.	:
8 091A	8A DD	MOV	BL, CH		; set up table pointer
	32 FF	XOR			:
	D1 E3	SHL			;
1 0920		MOV			; do table lookup.
	80 7F 03 00	CMP			; check if port present
	74 01	JZ	L33_2	<i>i</i>	; > go exit if present
4 092A		L33_1\$: STC		*	; set return code not to touch port
5 092B	L3	L33_2\$: RET			; return to caller
6 7					
7 8 092C		PCBTAB LAB	EL WORD		
	0000 E	PUBIAB LAB		T PCCOM : comm	port control block
	0000 E	DW			ter port control block
	0000 E	DW			nded comm port control blockM
2			55	, , , ,	
3 0932		CODE END	s		
4		END			

The Microsoft MACRO Assembler COMBIOS	02-20-84	PAGE	Symbols-1	
cros:				
Name	Length			
ALRET	0001			
EQ	0002			
ABLE	0002			
egments and groups:				
N a m e	Size al	ign combine	class	
GROUP	GROUP			
CODE	0932 BY	E PUBLIC	'CODE'	
ymbols:				
Name	Type Va	lue Attr		
	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
7201	Number 00			
BAUD	Number 00			
MODEM	Number 00			
PPXOF	Alias BI' L NEAR OI			
RXSPC	L NEAR OZ			
S7201	Number 00			
SCSUB	Number 00	I A		
STAT	L NEAR 02			
TXEMT	L NEAR 02			
UTOXMT	L NEAR OI			
7201	Number 00: Number 24:			
AUDMAX	Number 00			
BAUD	Number 00			
ITO	Number 00) 1		
IT1	Number 00			
1T2	Number 00			
IT3	Number 00 Number 00			
IT5	Number 00			
176	Number 00			
177	Number 00	ВО		
MODEM	Number 00			
RKCOMMON	L NEAR O7			
BRKERR	Alias BI L NEAR 07		Global	
RKON	L NEAR O7		Global	
RXDAT	L NEAR OI			
RXSPC	L NEAR 02			
\$7201	Number 00			
STAT	L NEAR 02			
BTXEMT	L NEAR 02 L NEAR 05			
BUFF1	L NEAR OS	26 CODE		

	MACRO	Assemb	1er	02-2	10-84	PAGE	Symbols-2
UFF10				L NEAR		CODE	
SUFFER				L NEAR	0514	CODE	
BUFFX				L NEAR	0576	CODE	
BDATB				Number	0003		
BLEN				Number	0011		
BMODE				Number	0001		
BPORT				Number	0000		
BPRTY				Number	0004		
BRADR				Number	OOOD		
BRCVB				Number	0005		
BRXOF				Number	0008		
BSIZE				Number	000B		
BSTART				L BYTE	0000	CODE	
BSTPB				Number	0002		
BTXB				Number	0006		
BTXOF				Number	A000		
BXOFF BXON				Number Number	0008		
LK16X				Number	0040		
DMM_STACK				NUMBER E NEAR	OOEA	CODE	
R1				Number	0001	CODE	
R17201				Number	0015		
RITXBE				Number	0002		
R2				Number	0002		
R27201				Number	0010		
R3				Number	0003		
R4				Number	0004		
R5				Number	0005		
R7201				L NEAR	0083	CODE	
TS				Alies	BITS		
ATABIT				L NEAR	03E7	CODE	
DATABX				L NEAR	044F	CODE	
DATMAX				Number	0006		
C1				Number	0011		
сз				Number	0013		
EVCOM				Number	0001		
DEVMAX				Number	0003		
EVPRT				Number	0002		
DEVXCOM				Number	0003		
DFLT7201				L NEAR	0577	CODE	Global
FLTBUF				L NEAR	0589	CODE	Global
FLTCOM				V BYTE	0000	CODE	External
DELTPRT				V BYTE	0000	CODE	External
FLTXCOM				V BYTE	0000	CODE	External
SR				Alias	BIT2		
TR				Alias	BIT2		
NOTXBE				Number	0028		
017201				Number	0038		
RR7201				Number	0030		
IXTAB				L WORD	086D	CODE	
RMERR				Alias L Near	BITS		
				L NEAR L NEAR	04E1	CODE	# Parks 1
GETBAUD Getchar				LNEAR	0660		Global

The Microsoft OMBIOS	MACRO	Assembl	er	02-2	0-84	PAGE	Symbols-3	
ETPCB				L NEAR	0911	CODE		
CK7201				L NEAR	0830	CODE	Global	
CKA				L NEAR	012A	CODE		
CKB				L NEAR	012F	CODE		
ACKTAB				L WORD	0865	CODE	· · · · · · · · · · · · · · · · · · ·	
ACKXA				LNEAR	0139	CODE		
ACKXB				L NEAR	0134	CODE		
VECTOR				V WORD	0000	CODE	External	
NCHA1 NCHA2				L NEAR	0656	CODE		
NCHAR				L NEAR L NEAR	0658 0626	CODE	Globel	
NCHAX				L NEAR	065E	CODE	G 10Da 1	
NITCOM				L NEAR	OSCF	CODE	Global	
NSTAT				L NEAR	0612	CODE	Glopal	
ISTX				L NEAR	0625	CODE	- · · · · · · · · · · · · · · · · · · ·	
T7201				L WORD	OOEA	CODE		
TEXIT				L NEAR	0071	CODE		
0_10\$				L NEAR	0588	CODE		
11_1\$				L NEAR	OSBF	CODE		
12_10\$				L NEAR	05E9	CODE		
13_1\$				L NEAR	OSFO	CODE		
				L NEAR	0601	CODE		
				L NEAR	062E	CODE		
6_10\$				L NEAR	0664	CODE		
6_5\$				L NEAR	0 6 C 1	CODE		
				L NEAR	OEDB	CODE		
7_28				L NEAR	OSES	CODE		
18_1\$				L NEAR	0707	CODE		
				L NEAR	0723	CODE		
				L NEAR	073A	CODE		
1_1\$				L NEAR	0054	CODE		
20_10\$				L NEAR	078D	CODE		
21_1\$				L NEAR	0796	CODE		
				LNEAR	07A9	CODE		
23_10\$				L NEAR	07DE	CODE		
24_1\$				L NEAR	07E8	CODE		
				L NEAR	0818	CODE		
				LNEAR	082F	CODE		
				L NEAR	0852	CODE		
18_1\$ 19 10\$				L NEAR	088C	CODE		
:9_10\$:_10\$				L NEAR L NEAR	08C4 0211	CODE		
				LNEAR	OSCE	CODE		
				L NEAR	OSEC	CODE		
				L NEAR	OSEE	CODE		
				L NEAR	OSFF	CODE		
		: : : :		L NEAR	092A	CODE		
				L NEAR	0928	CODE		
				L NEAR	04F5	CODE		
4_108				L NEAR	OAFA	CODE		
35 1\$				L NEAR	075F	CODE		
35_10\$				L NEAR	0774	CODE		
35_2\$				L NEAR	0751	CODE		
·- <u>-</u> - · · · ·					J			

he Microsoft MACRO Assembler MBIOS	02-20-84	PAGE	Symbols-4
_10\$	L NEAR 02DS L NEAR 0330	CODE	
108	L NEAR 0341	CODE	
13\$	L NEAR 0361 L NEAR 03A2	CODE	
15\$	L NEAR 03AD L NEAR 0383	CODE	
3\$	L NEAR 0390 L NEAR 03CB	CODE	
18	L NEAR 0428 L NEAR 03FC	CODE	
10\$	L NEAR 04A0 L NEAR 0483	CODE	
18	L NEAR 04C9 L NEAR 04D9	CODE	
DE	L NEAR 03B6	CODE	
DE1	L NEAR 03D6 Number 0002	CODE	
DEX	L NEAR 03E6 L NEAR 002A	CODE	
TINT1	L NEAR 0065 L WORD 089A	CODE	
CHAR	L NEAR OSCS L NEAR OSFS	CODE	Global
N2	L NEAR 0730	CODE	
STAT	L NEAR 0701 L NEAR 08A7	CODE	Global Global
EERR	Alias BIT5 Alias BIT4		
ITY	L NEAR 0342 Number 0010	CODE	
TAB	Number 0008 L WDRD 092C	CODE	
DM	V BYTE 0000 Number 000B	CODE	External
R2	Number 000C		
R3	Number 000D Number 000E		
R5	Number 000F Number 0029		
ALL	Number 0004 Number 0026		
D	Number 0014 Number 0000		
EN	Number 0037 Number 0015		
10DE	Number 0027		
10DM	Number 0005 V BYTE 0000	CODE	External
PRTY	Number 002A Number 0033		
CVA	Number 000A Number 0018		
ne Microsoft MACRO Assembler	02-20-84	PAGE	\$vmbols-5
ABIOS		PAGE	Symbols-5
CVB	Number 002B Number 0017 Number 002F	PAGE	Symbols-5
CVB	Number 002B Number 0017	PAGE	Symbols-5
EIOS CYB	Number 0028 Number 0017 Number 002F Number 0012 Number 0031 L BYTE 0000	PAGE	Symbols-5
CV8	Number 002B Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0003		Symbols-5
IE IOS ICVB	Number 002B Number 0017 Number 002F Number 0031 L BYTE 0000 Number 0023 Number 0022 Number 0021		Symbols-5
EIOS CVB	Number 002B Number 0017 Number 002F Number 0031 L BYTE 0000 Number 0023 Number 0022 Number 0022 Number 0022 Number 0022 Number 0028 Number 0020	CODE	Symbols-5
BIOS CVB	Number 002B Number 0017 Number 002F Number 0031 L BYTE 0000 Number 0003 Number 0003 Number 0021 Number 0021 Number 0022 Number 0028		Symbols-5 External
EIOS CYB	Number 0028 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0021 Number 0022 Number 0021 Number 0021 Number 0021 Number 0021 Number 0020 Number 0030 V BYTE 0000 Number 001D	CODE	
EIOS CYE CYF XOF XOF Z201 IZE TART TART TART TART TAGA TCGA .	Number 0028 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0020 Number 0020	CODE	
BIOS CYB	Number 002B Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 002C Number 0030 V BYTE 0000 Number 001D Number 001C Number 002C Number 001C Number 001C Number 001C Number 002C	CODE	
BIOS CVB	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 0026 Number 0010 Number 001D Number 001D Number 001D Number 001D Number 001E Number 001L	CODE CODE CODE CODE CODE	
BIOS CVB	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 002C Number 0030 V BYTE 0000 Number 001C Number 001C Number 001C Number 002E Number 002E Number 001C	CODE	
BIOS CYB CYF XOF XOF XOF IZE IZE TART TART TART TCA TCA TCF TCA TCF TOM TCA TOFF ON TA TART	Number 0028 Number 0017 Number 0017 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0021 Number 0021 Number 0021 Number 0020 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0113 L NEAR 0305 L NEAR 0305	CODE CODE CODE CODE CODE CODE CODE CODE	External
EIOS CYB CYF XOF 7201 IZE TART TART TAT TCA TCF TCA TCF TOM MTA MTA OFF ON TA TXB TXA	Number 0028 Number 0017 Number 0017 Number 0031 L BYTE 0000 Number 0021 Number 0022 Number 0021 Number 0021 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0110A L WORD 0124 L WORD 0126 L WORD 0110A L NEAR 0206 L NEAR 0306 L NEAR 0316 L NEAR 0316	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CVB CVF XOF 7201 IZE TART TART TAT TCA TCF TCA TCF TOB TCF TOB TOB TOB TOB TOB TOB TOB TO	Number 0028 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0112 L WORD 0122 L WORD 0110 L WORD 0120 L WORD 0110 L NEAR 0306 L NEAR 0316 L NEAR 0003 L NEAR 0003 L NEAR 0003	CODE CODE CODE CODE CODE CODE CODE CODE	External
EIOS CYB CYF CYF CYF TACT TACT TACT TACT TACT TACT TACT TAC	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 002C Number 0030 V BYTE 0000 Number 001D Number 001D Number 001D Number 001C Number 001C L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0114 L NEAR 02D6 L NEAR 0316 L NEAR 0316 L NEAR 0316 L NEAR 0318 Number 0003 L NEAR 0517 Number 0000 Number 00000 Number 00000 Number 00000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CYB CYF XOF 7201 IZE TART TART TAT TAT TCA TCF TPB XBB XOF COM MTA MTF OFF ON TTA TTA TTA TTA TTA TTA TTA	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 0021 Number 002C Number 002C Number 0030 V BYTE 0000 Number 001C Number 0003	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CV8 CVF CVF XOF TAOF TAOF TIZE TAAT TAAT TCA TCF TPB XB XOF COM MTA MTF OFF ON TTAA TTAA TTAA TTAA TTAA TTAA TTAA T	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0021 Number 0021 Number 0021 Number 002C Number 002C Number 001C Number 00003 Number 00003 Number 000000000000000000000000000000000000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CYB CYF XOF 7201 IZE TART TART TAT TCA TCF TCA TCF TCF TOM MTA MTA OFF COM MTA MTA TXB TXB TXB TXB TXB TXB TXB T	Number 0028 Number 0017 Number 0017 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0021 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0100 L NEAR 0318 L NEAR 0003 L NEAR 0003 L NEAR 0003 L NEAR 0003 L NEAR 0000 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CYB CYF XOF XOF 7201 IZE TART TART TAT TTCA TCG TCF TCG TCF TOM MTA OFF COM MTA OFF COM MTA OFF COM CM3 CM3 CM3 CM4	Number 0028 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0112 L WORD 0110 L WORD 0110 L WORD 0100 L WORD 0100 L NEAR 0310 L NEAR 0305 L NEAR 0310 L NEAR 0305 L NEAR 0305 L NEAR 0305 L NEAR 0310 L NEAR 0300	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
ETOS CYB CYF CYF XOF XOF XOF TACT TART TART TAT TCA TCF TCF TOB XOF COM MTA MTF OFF ON TA TXA TXA TXA TXB TXA TXB TXA TXB TXA TXB TXA TXB TXA TXB TXA TXA	Number 0028 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0022 Number 0022 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0122 L WORD 0114 L NEAR 0308 L NEAR 0308 L NEAR 0310 L NEAR 0310 L NEAR 0310 L NEAR 0308 L NEAR 0310 L NEAR 0308 L NEAR 0300 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CVB CVF CVF	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0023 Number 0021 Number 0021 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 00112 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0114 L NEAR 0316 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
IS IOS ICYB ICYF ICYF IXOF IXOF IZE ITART ITART ITART ITART ITART ITART ITARA ITCF ITARA ITCF ITARA ITCF ITARA ITCF ITARA ITCF ITARA	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0021 Number 0021 Number 0021 Number 002C Number 002C Number 0010 Number 0010 Number 0010 L WORD 0112 L WORD 0110 L NEAR 0305 L NEAR 0305 L NEAR 0310 L NEAR 0310 L NEAR 0517 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global
EIOS CYB CYF CYF XOF TXOF TYPE TART TART TART TCA TCF TCA TCCA TC	Number 0017 Number 0017 Number 0017 Number 0012 Number 0021 Number 0022 Number 0022 Number 0022 Number 0021 Number 0020 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0011 Number 0011 Number 0011 Number 0010 Number 0010 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0114 L NEAR 0316 L NEAR 0467 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global
EIOS CYB CYF XOF 7201 IZE TART TART TAT TCGA TCF TCF TOGA TCF TOGA TOFF COM MTA OFF ON TA TXA TXA TXA TXA TXA TXA TXA	Number 0017 Number 0007 Number 00012 Number 00013 Number 00021 Number 00021 Number 00021 Number 00021 Number 00021 Number 00021 Number 00020 Number 00010 Number 0010 Number 0010 Number 0010 Number 0011 Number 0011 Number 0011 Number 0010 L WORD 0112 L WORD 0112 L WORD 0110 L NEAR 0216 L NEAR 0316 L NEAR 0466 Number 0000 Number 000467 L NEAR 04676 L NEAR 04683 A11as BITO L NEAR 0483 A11as BITO L NEAR 0483	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global
IS IOS IC YB IC YB IC YF IC YF IC YF IC YF IC YF II ZE IT ART IT ART IT ART IT CA IT CF IT CA IT CA	Number 0017 Number 0017 Number 0017 Number 0011 Number 0022 Number 0022 Number 0022 Number 0020 Number 0020 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0010 Number 0011 Number 0011 Number 0011 Number 0010 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0112 L WORD 0110 L NEAR 0310 L NEAR 0400 Number 0000 Number	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global Global Global Global
IC YB IC YB IC YB IC YB IC YF IC YC	Number 002B Number 0017 Number 0017 Number 0012 Number 0031 L BYTE 0000 Number 0021 Number 0021 Number 0021 Number 002C Number 002C Number 002C Number 001C Number 0006 L NEAR 0316 L NEAR 0316 L NEAR 0316 L NEAR 0316 L NEAR 0003 L NEAR 05F7 Number 00004 Number 00004 Number 00004 Number 00004 Number 00004 Number 00006 L BYTE 00000 Number 00006 L BYTE 00000 Number 00006 L NEAR 0467 L NEAR 0458 A 1 i as S B I T 0 L NEAR 0483 A 1 i as S B I T 0 L NEAR 0483 A 1 i as S B I T 0 L NEAR 0483 A 1 i as S B I T 0 L NEAR 0483	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global Global Global
RETOS CCYB CCYF CXOF CXOF CY7201 IZE ITART ITART ITART ITART ITART ITOGA	Number 0017 Number 0017 Number 0017 Number 0012 Number 0021 Number 0021 Number 0022 Number 0022 Number 0021 Number 0021 Number 0021 Number 0021 Number 0010 Number 0010 Number 0010 Number 0011 L WORD 0112 L WORD 0110 L NEAR 0216 L NEAR 0316 L NEAR 0316 L NEAR 0316 L NEAR 0316 L NEAR 0003 L NEAR 0316 L NEAR 0003 L NEAR 0000 Number 0000	CODE CODE CODE CODE CODE CODE CODE CODE	External Global Global Global Global Global

e Microsoft MACRO Assembler BIOS	02-20-84	PAGE	Symbols-6	
VMCOM	L NEAR 0000	CODE	External	
ETUP	L NEAR OSCA	CODE	Global	
:1	L NEAR 0167 L NEAR 0172	CODE		
:4	L NEAR 0198	CODE		
5	L NEAR 019A L NEAR 01A0	CODE		
:7	L NEAR 0185	CODE		
8	L NEAR 0187 L NEAR 01D7	CODE		
EIVER	L NEAR OIL	CODE		
X	L NEAR 01E3	CODE		
ET7201	L NEAR 0904 Alias Bito	CODE		
D	Alias BIT4			
7201	Number 0018 Alias BIT3			
T7201	L NEAR 0000	CODE	G l oba l	
TCOMMON	L NEAR 0012 L WORD 00A6	CODE		
E_SP	L WORD OOAS	CODE		
10DEM	L NEAR 0775	CODE	Global	
:C	Alias BIT1 L NEAR 0253	CODE		
:C1	L NEAR 0262	CODE		
CEIVE	L NEAR 024B L NEAR 0268	CODE		
L	Alias BITO	CODE		
	Number 0001			
	Number 0002 Number 0003			
	Alias BIT1			
нс	Alias BIT1	can-	Clabal	
ICEL	L NEAR 08A2 L NEAR 08C5	CODE	Global Global	
IIT	L NEAR 031B	CODE		
IAX	Number 0003 Number 0010			
FER	L NEAR 0222	CODE		
X	L NEAR 0237	CODE		
I	L NEAR 066A L NEAR 0694	CODE		
(X	L NEAR OSAS	CODE		
/201	L NEAR 0875 L WORD 0892	CODE	Globa1	
7201	L NEAR 0269	CODE		
)1	Number 0028			
	Number 0021	CODE		
	L NEAR 027C			
IT	L NEAR 027C Number 00FF			
(IT	Number OOFF L NEAR 0286	CODE		
MODEM	Number OOFF			
IXIT	Number OOFF L NEAR 0286 L NEAR 0281	CODE	Symbols-7	
XIT	Number OOFF L NEAR O286 L NEAR O281 Number OO2A	CODE	Symbols-7	
XIT	Number OOFF L NEAR 0286 L NEAR 0281 Number 0024	CODE CODE	Symbols·7	
IIT	Number OOFF L NEAR O286 L NEAR O221 Number OO2A	PAGE CODE	Symbols-7	
IT	O2-20-84 L NEAR 027A L NEAR 0276 Number 0274 L NEAR 0276 Number 029 V BYTE 0000	PAGE CODE CODE	External	
IT	Number OOFF L NEAR O286 L NEAR O221 Number OO2A	PAGE CODE		
IT	O2-20-84 L NEAR 027A L NEAR 027A L NEAR 0276 Number 0029 V BYTE 0000 Number 0002 L NEAR 0000	PAGE CODE CODE CODE CODE	External	
IT	O2-20-84 L NEAR 027A L NEAR 027A L NEAR 0276 Number 0002 V BYTE 0000 V BYTE 0000 Number 0002 L NEAR 0148 L NEAR 0243	PAGE CODE CODE CODE	External	
IT	O2-20-84 L NEAR 027A L NEAR 027A L NEAR 0276 Number 0029 V BYTE 0000 V BYTE 0000 Number 0029 L NEAR 0148 L NEAR 0243 Number 0028 L NEAR 0243 Number 0028 L NEAR 0243 Number 0280	PAGE CODE CODE CODE CODE CODE CODE	External	
Microsoft MACRO Assembler MICROSOFT MACRO ASSEM	O2-20-84 L NEAR 027A L NEAR 027A L NEAR 0276 NUMBER 0276 NUMBER 0276 NUMBER 0276 NUMBER 0200 V BYTE 0000 V BYTE 0000 L NEAR 0148 L NEAR 0148 L NEAR 0148 L NEAR 0248 L NEAR 0248 L NEAR 0248 L NEAR 0240 L NEAR 021A	PAGE CODE CODE CODE CODE CODE CODE	External	
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Microsoft MACRO Assembler 10S AT	O2-20-84 L NEAR 027A L NEAR 0276 NUMBER 0276 NUMBER 0276 NUMBER 0276 NUMBER 0276 NUMBER 0200 V BYTE 0000 V BYTE 0000 V BYTE 0000 L NEAR 0148 L NEAR 0243 NUMBER 0243 NUMBER 0243 NUMBER 0243 NUMBER 0243 L NEAR 0488 L NEAR 0448	PAGE CODE	External	
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T	NUMBER 0285 L NEAR 0281 NUMBER 0224 L NEAR 0274 L NEAR 0276 NUMBER 0276 NUMBER 0276 NUMBER 0290 V BYTE 0000 V BYTE 0000 V BYTE 0000 L NEAR 0148 L NEAR 0248 L NEAR 0290 L NEAR 0290 L NEAR 0290 L NEAR 0290 L NEAR 021A L WURD 00FA L NEAR 0486 L NEAR 0486 L NEAR 0481 L NEAR 0480 L NEAR 0480 L NEAR 0480 L NEAR 0480 L NEAR 0481	CODE CODE CODE CODE CODE CODE CODE CODE	External External	
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Crosoft MACRO Assembler	NUMBER 0286 L NEAR 0281 NUMBER 0281 NUMBER 0281 NUMBER 0224 L NEAR 0276 NUMBER 0276 NUMBER 0276 NUMBER 0280 L NEAR 0243 NUMBER 0148 L NEAR 0243 NUMBER 0243 NUMBER 0243 NUMBER 0243 NUMBER 0243 NUMBER 0250 L NEAR 0214 L WEAR 0481 L NEAR 0513 V BYTE 0000 L NEAR 0513 V BYTE 0000 L NEAR 0280	PAGE CODE CODE	External External Clobal Clobal	

Symbol Cross Refe	744	(# is d			Cref									
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N7201 NBAUD NMODEM NPPXOF RXXDAT RXXSPC SX7201 SX7201 SX5SUB SXAT AUTOXMT	78# 78# 299 300 78# 78# 297	1283 330 331 205 456 329 328 520#	1619 375# 615# 2292 728# 550# 1626	1748	1756									
B7201 SAKGRND SAUDMAX BBAUD SITO SITO SITO SIT1 SIT2 SIT3 SIT4 SIT4 SIT5 SIT6 SIT6 SIT7 SMODEM SRKCOMMON SRKCOMMON	78# 78# 78# 78# 78# 78# 78# 78# 78# 78#	1183 78 78 78 78 78 78 78 78	78 78 78 78 78 78 78 73	78 78 78 871 1952 978 1037	936 78 525 1975 1037 1045	1447 621 871 1821	1470 936 1696	1897 1697	1835	1844	1894			
SRKOFF SRKON SRXDAT SRXDAT SRX57201 SSTAT SSTAT SSTAT SUFF1 SUFF1 SUFFER SUFFER SUFFER	117 116 295 295 296 78# 294 293 1262 1272# 834	750 1967# 1944# 324 325 167 323 322 1266# 1388 1257# 1265	382# 607# 203 720# 577#											
CALRET CBDATB CBLEN CBMODE CBPORT CBPRTY	 78# 78# 78#	895 1418 962 898	911 967 900	902	1005	915	922							
COMBIOS									-					
Symbol Cross Refe CBRADR CBRCVB CBRXOF CBSIZE CBSIZE CBSTART CBSTPB CBTXB CBTXB CBTXOF CBXOFF	78# 78# 78# 78# 78# 78# 78#	(# is d 1268 1072 826 1258 858 1101 830 1237	efiniti 1079 1264 863 1103	1084	Cref 1100 1137	1109	1147							
Symbol Cross Refe CBRADR CBRCVB CBRXOF CBSIZE CBSIZE CBSTART CBSTART CBSTART CBSTOF CBXOFF CBXON CCROUP CLK16X CODE CROUP CLK16X CRI CRITZO1 CRITXBE CRI CRITXBE CR2 CR2 CR2 CR2 CR3	78# 78# 78# 78# 78# 78# 78# 78# 78# 78#	1268 1072 826 1258 858 1101 830	1079 1264 863	1084	1100	1109	1147							
Symbol Cross Refe CBRADR CBRCVB CBRXOF CBSIZE CBSIZE CBSIZE CBSTART CBSTPB CBIXOF CBXOFF CBXOFF CBXOFF CBXOFF CBXON CGROUP CLK16X CODE CRI	78# 78# 78# 78# 78# 78# 78# 78# 78# 78#	1268 1072 826 1258 858 1101 830 1237 1233 75 1358 2306 2074 2324 2324 2324 2324 2324 2324 2324 23	1079 1264 863 1103 75 74 2099 1048 1349 220	1106 75 2373	1137	1109	1147				·			
Symbol Cross Refe	78# 78# 78# 78# 78# 78# 78# 78# 78# 78#	1288 1072 826 1258 858 1101 830 1237 1233 74# 273# 1358 2306 2074 2326 281 842 21040 212 899# 1051# 1003	1079 1264 863 1103 75 74 2089 1048 1349 220 2354 1149 2307 1379#	1084 1106 75 2373	1137	1109	1147							
Symbol Cross Refe CBRADR CBRCVB CBRXOF CBSIZE CBSIZE CBSIZE CBSTART CBSTART CBSTXB CBTXOF CBXON CGROUP CLK16X CODE COMM_STACK CR1 CR17201 CR1TXBE CR2 CR2 CR2 CR2 CR2 CR3 CR4 CR5 CR4 CR5 CR4 CR5	78# 78# 78# 78# 78# 78# 78# 78# 78# 78#	1288 1072 826 1258 858 1101 830 1237 1233 74# 2138 2306 2074 2326 2326 2326 2012 1040 212 999# 1051# 1003	1079 1264 863 1103 75 74 2089 1048 1349 220	1084 1106 75 2373 1475 1986 248#	1137	1109	1147							
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Symbol Cross Reference										
,		(#isd	efiniti	on)	Cre	f - 3				
ACKA	346#	2161								
ACKB	350#	2162								
CKXA	2143 358#	2180#								
:KXB	354#	2163								
CTOR	94#	347	351	355	359	2154	2155			
IA1	1546	1549#								
CHA2	1548	1551#								
HAR	108	1528#	1572							
TCOM	1540 98	1554#								
TAT	107	1484#								
<u> </u>	1497	1501	1503#							
7201	166 213	292# 221	2167	2168	2169	2170	2213	2214	2215	2216
10\$	1329	1361#								
1\$	1414	1428#								
18	1443	1445#								
18	1466	1468#								
10\$	1530 1701	1533# 1704#								
<u>_</u> 5\$	1691	1702#								
<u>. 18</u>	1747	1750#								
	1749 1816	1751 1818#	1759#							
55	1834#	1836								
<u> </u>	1845	1848#								
\$	204	215#								
15	1919	1922	1929#							
10s	1969	1972#								
10\$	2011	2022#								
1\$	2038	2041#								
1\$	2096	2103#								
~ , &		2150								
.15	2206#	2208								
10\$	2245 526	2256# 537#								
- 15	2272	2274#								
_15	2297	2302#								
		2311 2310#								
	2355	2357	2384#							
2\$	2363	2365#								
_19	1199	1201#	1207#							
	1883	1888#	. 20 /#							
_10\$	1880	1896	1898#							
100	1887 765	1889# 769	771#							
10\$	862	866#	771#							

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	Kerer ence		(* 15 6		011,	CFU	7 - •								
			878#												
			905# 932	914 934#											
L5_15\$			939#	234#											
L5_2\$			901	904	910	912	919#								
L5_3\$			925#												
L6_1\$			970#												
L7_1\$			1017	1021	1025	1031	1033#								
L7_10\$ L8_10\$			1008#												
L8 55			1106#												
L9_1\$			1153#												
L9_4\$. 1152	1162#												
MODE		. 821	961#												
MODE1		 . 974	976#												
MODEMAX		 . 784													
MODEX		 . 964	984#												
NXTINT		 . 1934	,												
NXTINT1			223#												
OURTAB		 . 2199	2218#												
OUTCHAR		 . 111	1738#	1785											
OUTCHX			1767#												
DUTN2			1831	1841#	1847										
OUTSTAT			1814#	1742											
OVRERR				749											
PARERR		. 784	635	749											
PARITY			894#	,											
PC7201		 . 781		1763											
				1163											
PCBTAB			2368#												
PCCOM PCCR1				376	551	816	729	2369							
PCCR2				1357	2073	2075	2098	2100	2306						
PCCR3				977	980	1044	1047	1445	1469	1474	1821	1827	1837		
PCCR4		 . 78/		870	873	935	938	941	1339			,			
PCCR5		 . 78/		1036	1039	1346	1348	1951	1974						
PCDATB				1009											
													•		
PCFAIL PCFLAG		 . 784		211 427	219 479	486	741	747	250	1007					
		 1618	1619	1621	1623	1631	1692	743 1748	756 1756	1283 1757	1344	1448	1471 1972	1510 1985	1613
										,					
PCMASK				528	529	1035	1760	1850							
PCMODE				1415		. 423	. ,	1000							
				1920											
PCPRT		 . 824	210	383	578	608	721	2370							
PCRADR		 . 784	437	1285	1286	1488	1534								

A	1154 2017 493 588 2362 2251 768	1162 485 619	2018 626	2040 672	731				
B	493 588 2362 2251 768	619			721				
F 78# 489 491 78# 417 829 01 78# 249 521 E 78# 1284 RT 78# 2296 2298 178 787 787 2250 278 F 78# 764 768 B 78# 867 L 78# 472 833 18 M 81# 218 390 1A 78# 584 2069 2 F 78# 581 583 F 78# 581 583	588 2362 2251 768	619			771				
F 78# 417 829 101 78# 249 521 1E 78# 1284 RT 78# 1284 RT 78# 1286 2296 2298 2 14 787 767 2290 2 15 78# 767 2290 2 16 78# 867 1 78# 867 1 78# 472 833 1 16 81# 218 380 16 78# 584 2089 2 17 78# 584 583 583 17 78# 584 583	588 2362 2251 768	619			771				
E 78# 1284 RT 78# TT 78# TT 78# TT 78# TT 78# 2296 2298 2 A 767 2250 2 F 78# 767 2250 2 B 78# 867 L 78# 1148 F 78# 472 833 1 M 81# 218 390 A 78# 584 2069 2 F 78# 581 583	2362 2251 768		626	672	771				
RT	2251 768	770			, , ,	1354	1694	1820	1892
TT 78# 2296 2298 2 A 78# 767 2250 2 FF 78# 764 768 B 78# 867 L 78# 1148 FF 78# 472 833 1 M 81# 218 390 A 78# 584 2069 2 F 78# 581 583	2251 768	770							
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		,,,	2252	2273					
F									
M	1607								
A	564	612	725	2296	2298	2371			
F	2070								
	585	587	2071	2097					
l	1240 1625	1750 1746							
	. 0 2 5	. , - 0							
321# 2220									
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IR									
MAX									
NT	474	1302	1499	1538	1542	1615			
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	1293 1291	1547							
l	1290	1387				`			
	1300								
	1551								
	1297	1545							
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78# 1307 1616									
1077 1081#									
UD									
RT 1083 1099#									
K	756								
(1074 1093# NCEL 119 1333 2037#									
IMMON									
S 106 1464#									
IA									
IT									
490 494 496#									
	1283	1693							

Symbol Cross Reference	(# is c	definiti	ion)	Cref	- 6								
RDMODEM	114 1877#												
RDNVMCOM	92# 1335												
RDSETUP	104 1408#												
REC1	422 425#												
REC3	418 426	433#	644										
REC4	449 452#												
REC5	451 455#												
REC6	443 458#												
REC7	464 467#												
REC8	466 470#												
REC9	476 481#												
RECEIVER	377 384	413#											
RECX	424 428	457	473	478	480	485	487#						
RESET7201	206 217	2293	2300	2319#									
RLSD	78# 78#												
RST7201	78# 2322												
RTS	78#												
RUPT7201	96 163#												
RUPTCOMMON	168 178#												
SAVE_SP	183 241	266#											
SAVE_SS	184 240	267#											
SEQ	78 78	78	78	78	78	78	78	78	78	78	78	78	78
	78 78	78	78	78	78	78	78	78	78	78	78	78	78
	78 78	78	78	78	78	78	78	78	78	78	78	78	78
	78 78	78	78	78	78	78	78	78	78	78	78	78	78
	78 78												
CETHODEN													
SETMODEM	115 1916# 78#	2309											
SPREC	609 613	625#											
SPREC1	637# 752	625#											
SPRECEIVE	618#												
SPRECX	622 646#												
SPSEL	78#												
SR1	78# 628	744											
SR2	78#												
SR3	78#												
SRLSD	78#												
SRTS	78#												
STATCHG	126 2239#												
STCANCEL	127 2270#												
STOPBIT	819 857#												
STPBMAX	78# 861												
SX7201	78# 694	734	1353										
TABLE	78 78	78											
TXBUFFER	552 565	579#											
TXBUFX	582 586	588#											
TXXON	1535 1606#												
TXXONS	1611 1622#												
TXXONX	1608 1614	1617	1620	1624	1627	1632#							
VECT7201	124 2193#												

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COMBIOS
  Symbol Cross Reference
                                                                                           (# is definition)
                                                                                                                                                  Cref-7
                 . . . . . . . . . . . . . 2198
VECTAB
                                                                                           2212#
WRITE7201. . . . . . . . . . . . . . . .
                                                                                                                258
                                                                                                                                262
                                                                                                                                                 668#
                                                                                                                                                                943
                                                                                                                                                                                982
                                                                                                                                                                                              1041
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                                                                                                                                                                                                                                                                                               1987
                                                                                78#
78#
691
78#
309
310
78#
XA7201
XA7201
XABAUD .
XAEXIT .
XAMODEM .
XARXDAT .
XARXSPC .
                                                                                                696#
                                                                                                                703
                                                                                                             705#
701#
2299
693#
689#
                                                                                                342
343
216
341
340
XAS7201.
XASTAT .
                                                                                308
307
78#
88#
78#
305
306
78#
304
XASTAT .
XATXEMT .
XB7201 .
XBAUD1 .
                                                                                              1197
1200
                                                                                                              1203
XBAUD2
XBAUD2 .
XBMODEM .
XBRXDAT .
XBRXSPC .
XBS7201 .
XBSTAT .
                                                                                              336
337
174
335
334
302#
1146#
1136#
1653
XBSTAT . XBTXEMT . XINT7201 XMTBO . XMTBAUD . XMTBAUD . XMTBRK .
                                                                               304
303
173
1142
824
78#
                                                                                                              1950
                                                                                                                              1973
1139
                                                                              1139
121
120
78#
78#
1235
825
                                                                                              1165#
1334
2059#
479
1283
1237#
                                                                                                             2094#
                                                                                                                               1283
1621
                                                                                                                                              1613
1631
                                                                                                                                                               1618
1757
 XONOF 1
                                                                                             1237#
1232#
1241#
2295
170#
755#
726
763#
 XONOFF
XONOFF
XONOFX
XOPTION.
XRUPT7201.
XSTAT1
XSTATCHG.
XSTATX
                                                                                                                730#
```

```
The Microsoft MACRO Assembler End of BIOS
                                                                   02-20-84
                                                                                       PAGE
                                                                                                 1 - 1
                                                                           PAGE
TITLE
NAME
                                                                                       60,132
End of BIOS
END
COMPANY CONFIDENTIAL Copyright (C) 1983 Digital Equipment Corporation All rights reserved.
                                                              cgroup group code
code segment byte public 'code'
assume cs:cgroup
                                                              public public packet_adr,xfrpkt,i88pkt public packet_fnccod,status,drvno,secno,trackn,nsect,dmaadr,exstat public packet_base
                                                               ;Merely defines the end of the BIOS
                                                              packet_base equ this byte + 400h
ttrack equ this byte + 400h
db 4 dup (Offh)
                 0400
                                                                                                                ;280 Track position
              0000
                            04 [
                                                              tformat equ this byte + 400h
db 4 dup (0ffh) ; Z80 disk type bytes
               - 0404
                            04 [
              0004
              0008
                            04 [
                                     cc <sub>1</sub>
                                                                           db 4 dup (Occh)
                                                                                                                ;Filler
              : 040C
                                                               packet_adr equ this byte + 400h
                                                                                                                             ;Base address of pointers/buffers
              : 040C
000C 0000
: 040E
000E 0000
                                                               xfrpkt equ
                                                                                        this word + 400h
                                                                                                                             ;pointer to "packet"
                                                                           edn
qa
qa
                                                               i 88pkt
                                                                                        this word + 400h
                                                                                                                             ;returned pointer from Z80
48
49
50
51
52
53
54
55
                                                               ;The packet for the 280.
              = 0410
= 0410
0010 00
= 0411
0011 00
= 0412
                                                                                       this byte + 400h
this byte + 400h
                                                               packet
fnccod
                                                                                                                             ; Z80 command packet
                                                                                                                             ;Function code
                                                                           edn
edn
                                                               status
                                                                                        this byte + 400h
                                                                                                                             ;retnd status,
                                                               drvno
                                                                                        this byte + 400h
```

```
The Microsoft MACRO Assembler 
End of BIOS
                                                           02-20-84
                                                                            PAGE
                                                                                     1 - 2
            0012 00

= 0413

0013 00

= 0414

0014 00

= 0415

0015 00

= 0416

0016 000
5755901234567689012345677777777777
                                                                             o
this byte + 400h
                                                                                                             ;drive and side
                                                                 equ
                                                      secno
                                                                             this byte + 400h
                                                       trackn
                                                                  equ
                                                                                                             ;Track #
                                                                             this byte + 400h
                                                                            this word + 400h
0
this byte + 400h
0
                                                                                                             ;# of sectors to process
                                                       dmaadr
                                                                  equ
                    0000
                                                                 ap
edn
qA
                                                                                                             ; DMA address (abs)
            = 0418
0018 00
                                                       exstat
                                                                                                             ; extended status
            0019 0200 [
                                                      buffer db 200h dup (OCCh) ;280 buffer area
                                cc 1
            0219
                                                      endbios label byte
            0219
                                                      code ends
                                                                 end
```

```
The Microsoft MACRO Assembler
End of BIOS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                Symbols-1
                                                                                                                                                                                                                                                                                                                                               02-20-84
                                                                                                                                                                                                                                                                                                                                                                                                                                                    PAGE
     Segments and groups:
                                                                                                                              N & m e
                                                                                                                                                                                                                                                                                                                         Size
                                                                                                                                                                                                                                                                                                                                                                                      align
                                                                                                                                                                                                                                                                                                                                                                                                                                               combine class
    GROUP
0219
                                                                                                                                                                                                                                                                                                                                                                                    BYTE
                                                                                                                                                                                                                                                                                                                                                                                                                                                  PUBLIC 'CODE'
    Symbols:
                                                                                                                                N a m e
                                                                                                                                                                                                                                                                                                                         Type
                                                                                                                                                                                                                                                                                                                                                                                      Value
                                                                                                                                                                                                                                                                                                                                                                                                                                                   Attr
N a m e

BUFFER

DMAADR

DRVNO
ENDBIOS
EXSTAT
FNCCOD
I&BPKT
NSECT
PACKET
PACKET
PACKET
SECNO
STATUS
TFORMAT
TRACKN
TTRACK
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               Global Length =0200
Global
Global
Global
                                                                                                                                                                                                                                                                                                                                                                                                                                                   CODE
CODE
CODE
CODE
CODE
                                                                                                                                                                                                                                                                                                                       POPULATION OF THE POPULATION O
                                                                                                                                                                                                                                                                                                                                                                             Global
Global
                                                                                                                                                                                                                                                                                                                                                                                                                                                      CODE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Global
Global
Global
Global
                                                                                                                                                                                                                                                                                                                                                                                                                                                    CODE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Global
Global
                                                                                                                                                                                                                                                                                                                                                                                                                                                    CODE
CODE
CODE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Global
Global
Global
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Global
  Warning Severe
Errors Errors
O O
```

```
End of BIOS
Symbol Cross Reference
                      (# is definition)
                                   Cref-1
BUFFER . . . . . . . . . . . . . . .
13
12#
                           12
                               75
73#
FNCCOD . . . . . . . . . . . . . . . .
                    18
                        51#
45.
                    16
NSECT. . . . . . . . . . . .
                    18
                        81#
50#
                        24#
                        57#
53#
30#
59#
25#
16
                        43#
```

```
The Microsoft MACRO Assembler MSDOS 2.00 BIOS and System initialization
                                                                               02-20-84
                                                                                                       PAGE
                                                                                                                     1 - 1
                                                                                        PAGE
TITLE
NAME
                                                                                                       80,132
MSDOS 2.00 BIOS and System initialization
BIOSINIT
COMPANY CONFIDENTIAL
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                                                                          cgroup group code
                                                                          ; This module sets the proper packet ptr, initializes ;the bios IO devices, and sets up SYSINIT. After the DDS takes ;control, this module is overwritten.
                                                                          biosseg equ
biossiz equ
                                                                                                     40h ;where we load BIOS,
16*1024 ;generous BIOS size,
                 = 0040
= 4000
                                                                          public biosinit
                                                                                       dev1st:byte
ioinit:near
endbios:near
hd_init:near
ptd:near
packet_base:near
packet_adr:near
dos_pointer:word
dskdev:word, hdskdev:near, aux2dev:near, xoption:byte
                                                                          extrn
                                                                          extrn
extrn
extrn
extrn
                                                                           extrn
                                                                          extrn
extrn
extrn
                                                                           SYSINIT externals:
                                                                           extrn sysinit:far
                                                                                                                                     ;entry point,
                                                                          extrn current_dos_location:word ; where the DOS ; is loaded, ; where it will ; reside, ; top of device ; chain, ; size, paras,
                                                                          extrn default_drive:byte
                                                                          extrn buffers:byte
                                                                          extrn files:byte
```

```
The Microsoft MACRO Assembler MSDOS 2.00 BIOS and System initialization
                                                                                                                      PAGE
                                                                                           02-20-84
page
code segment byte public 'code'
assume cs:cgroup,ds:cgroup
                                                                                                      USEFUL EQUATES
                                                                                     ;
include defs.ash
TRUE EQU -1
FALSE EQU NOT TRUE
                     # - 0001
# 0000
                                                                                    ;Version number info
                     : 0002
: 0005
: 0018
                                                                                     VER_NO
REV_NO
MOD_NO
                                                                                                                                                        ; version_number
; rev_number
; mod_number %% update this equate
                                                                                                                       EQU
EQU
                                                                                                                                       5
24
                     . 000D
                                                                                     CR
LF
BELL
ESC
CTRL_S
CTRL_Q
CTRL_Z
                                                                                                                                       ODH
OAH
O7H
18h
13h
                                                                                                                                                        ;carriage return
;line feed
;Bell character
;ESCape
                                                                                                                       EQU
                     : 000A
: 0007
: 001B
: 0013
                                                                                                                       EQU
                                                                                                                       EQU
                        0011
                                                                                                                       EOU
                     = 001A
                                                                                                                       EQU
                     : 01E0
                                                                                     DOSBASE
                                                                                                                       EOU
                                                                                                                                       01EOh
                                                                                                                                                        ; MSDOS.SYS base segment
                                                                                    IVRELOC
                                                                                                                       EQU
                                                                                                                                      TRUE
                                                                                                                                                         : vector relocate
                                                                                     ;
ldrdata equ
ldrpkt equ
xfrpkt equ
datlen equ
pktfcn equ
z80int equ
wprsnt equ
xcprsnt equ
                     : FEF0
: FF00
: FEFC
: 0010
: 0025
                                                                                                                       ofefoh
ldrdata+10h
ofefch
oloh
o25h
                                                                                                                                                        ;location of command packet
;where Z80 thinks packet ptr is
;length of disk data and packet
;move packet function code
;z80 interrupt
;Winnie present
;Xcomm present
                                                                                                                       00h
01
02
                      . 0000
                                                                                      ; Initialize the BIOS, then set the stuff for ;SYSINIT, and jump to it. All of this code gets ;overwritten.
                                                                                      ;The booted disk drive is in CX.
                     0000
                                                                                      biosinit:
                                8C C8
8E D0
8C 0210 R
8E D8
8E C0
                     0000
                                                                                                                       ax,cs
ss,ax
sp,offset stack ;temp stack,
ds,ax
                     0002
0004
0007
                                                                                                       mov
                                                                                                       mov
                                                                                                                       PAGE 1-3
```

```
The Microsoft MACRO Assembler 02-20-84 MSDOS 2.00 BIOS and System initialization
 104
                                    OOOB 51
                                                                                                                                                                                                          push
                                                                                                                                                                                                                                             СX
                                                                                                                                                                                                                                                                                                                  ; save booted drive
:Initialize all the hardware, and the interrupt vectors.
                                      000C E8 0000 E
                                                          78 33 CO 8E CO 1E 8E D8 BE FEFO BF 0000 E B9 0010 FC F3 A4
                                       000F
                                                                                                                                                                                                            sti
                                                                                                                                                                                                                                          ax,ax
es,ax
;set ES: to 0
ds
;save data segment
ds,ax
;and set it to 0 too
si,offset ldrdata
;move it from default pkt
di,offset packet_base
;to bios area
cx,offset datlen
;mov all the data
movsb
;quickmove
ds
;restore mysterious ds:
                                                                                                                                                                                                                                                                                                                 :Need ints on
                                      000F
0010
0012
0014
0015
0017
                                                                                                                                                                                                            xor
mov
push
mov
                                                                                                                                                                                                            mov
mov
                                      001D
0020
0021
0023
                                                                                                                                                                                                           mov
cld
rep
pop
                                      0024
0027
0028
0031
                                                                                                                                                                                                                                              bx,offset ldrpkt ;build packet for move
byte ptr es:[bx],pktfcn ;command to move packet
es:word ptr 2[bx],offset packet_adr
es:xfrpkt,offset ldrpkt
                                                          BB FF00
26: C6 07 25
26: C7 47 02 0000 E
26: C7 06 FEFC FF00
                                                                                                                                                                                                            mov
                                                                                                                                                                                                            mov
mov
                                                                                                                                                                                                                                              z80int,al ;call the z80 es:word ptr xfrpkt,0 ;see if any return yet watack ;wait until he does
                                      0038 E8 00
003A 28: 83 3E FEFC 00
0040 75 F8
                                                                                                                                                                                                            out
                                                                                                                                                                         watack: cmp
jnz
                                                                                                                                                                           ;Print the system signon message
                                      0042 BB 00E1 R
0045 E8 0000 E
                                                                                                                                                                                                            mov
                                                                                                                                                                                                                                              bx, offset hello
                                                                                                                                                                                                                                                                                                                                               :print the hello message
                                                                                                                                                                                                            call
                                                                                                                                                                                                                                              pstr
                                                                                                                                                                          ;Do some preliminary hardware configuration (adjust device table, etc.)
                                                             80 3E 0000 E 01
75 13
E8 0000 E
0A C0
74 0C
8B 0000 E
C7 07 0000 E
C7 47 02 0040
80 3E 0000 E
02 75 0C
8B 0000 E
C7 07 0000 E
C7 07 0000 E
C7 07 0000 E
C7 47 02 0040
88 ---- E
8E D8
                                                                                                                                                                                                            cmp
jne
call
or
je
mov
                                                                                                                                                                                                                                              xoption, wprsnt
                                                                                                                                                                                                                                                                                                                                               ;have winnie?
                                      0048
004D
004F
0052
0054
0058
                                                                                                                                                                                                                                              xopti1
hd_init
al,al
xopti1
                                                                                                                                                                                                                                                                                                                                                   ;yes - call hard disk init
;any units
;jump if none
;point at floppy
                                                                                                                                                                                                                                             bx,offset dskdev point at floppy word ptr o[bx],offset biosseg xoption,xcprsnt px,offset dskdev brook ptr o[bx],offset biosseg px,offset dskdev prod ptr o[bx],offset dskdev word ptr o[bx],offset dskdev prod ptr o[bx],of
                                                                                                                                                                                                                                               bx, offset dskdev
                                                                                                                                                                                                             mov
                                       005D
                                                                                                                                                                           mov
xoptil: cmp
                                       0050
0062
0067
0069
006C
0070
0075
                                                                                                                                                                                                             jne
mov
                                                                                                                                                                                                             mov
                                                                                                                                                                                                             mov
                                                                                                                                                                           xopti2:
                                                                                                                                                                           assume ds:sed sysinit
                                       007A
007B
007D
                                                          59
FE C1
88 OE 0000 E
                                                                                                                                                                                                                                              cx ; get booted
c1 ; drive,
default_drive, c1
                                                                                                                                                                                                             MOV
```

```
The Microsoft MACRO Assembler MSDOS 2.00 BIOS and System initialization
                                                                                       02-20-84
                                                                                                                   PAGE
                                                                                                                                   1-4
159
160
                  0081 8C C8
0083 05 0400
0086 A3 0000 E
                                                                                                                  ax,cs
ax,(biossiz/18)
current_dos_location,ax
                                                                                                  MOV
add
                                                                                                  MOV
                  0089 B8 0000 E
008C B1 04
008E D3 E8
0080 05 0041
0083 A3 0000 E
0086 2E: A3 0000 E
                                                                                                                  ax,offset endbios
c1,4
ax,c1
ax,41h
final_dos_location,ax
cs:dos_pointer,ax
                                                                                                  mov
                                                                                                                                                                   ; loc of bios end (above Z80 region)
                                                                                                  mov
shr
add
mov
mov
                                                                                                                                                                   convert to paragraph; + base segment + 1
                                                                                                                                                                  ; FORMAT uses this to find MSDOS
                                                                                  ; Determine memory size (faster than SYSINIT)
                  008A 33 DB
008C BA 1000
008F
008F 8E C2
                                                                                                                  bx, bx
dx, 1000h
                                                                                                                                                                    ;Start at 84K
                                                                                  tst mem:
                              8E C2
B1 OE
26: 8A O7
F6 DO
26: 88 O7
26: 3A O7
F6 DO
                                                                                                                  es,dx
c1,14
a1,es:[bx]
a1
es:[bx],a1
a1,es:[bx]
                                                                                                  mov
                                                                                                                                                                   ;Max # of sections (896K);get byte;compliment byte;write it back;same?
                   00A1
                                                                                                  MOV
                  00A1
00A5
00A6
00A8
00AB
                                                                                                  mov
not
mov
                                                                                                  cmp
not
                                                                                                                                                                    invert it back to original value
                             F6 D0
26: 88 07
75 08
81 C2 1000
FE C9
75 E2
                                                                                                                   al
es:[bx],al
done_mem
dx,1000h
                  0080
0083
0085
0089
                                                                                                                                                                   ;done if no match
;step to next 64K
                                                                                                  dec
                                                                                                                                                                    ;loop till done
                                                                                                                   tst_mem
                   OOBB
                                                                                                  jnz
                   COBD
                                                                                  done_mem
                   OOBD 89 18 0000 E
OOC1 C6 06 0000 E 04
OOC6 C6 08 0000 E 08
                                                                                                                  memory_size,dx ; DX := # of paragraphs in system buffers,4 ;;A good number (maybe made larger by user) files,8 ;; Edit CONFIG.SYS to change
                                                                                                  mov
mov
mov
                                                                                  ;; edit CONFIG
;Setup the pointer to our chain of IO devices,
;and leave default drive and buffers.
                                                                                                  mov word ptr device_list+2,cs
mov word ptr device_list,offset dev1st
mov dx,10Ch ;;; *** Disable MHFU for SYSINIT
mov al.0 ;;; MHFU port
cli ;;; Ints off first
out dx,al ;;; Now disable
jmp sysinit ;Invoke SYSINIT (ints restore in REINIT)
                              8C OE 0002 E
C7 O8 0000 E 0000 E
BA 010C
BO 00
FA
EE
EA 0000 ---- E
                  OOCB
OOCF
OODS
OODS
                   OODA
OODB
OODC
                   00E1
                                                                                                                   byte
                                                                                                   label
                                                                                  he110
                                                                                        rept
                                                                                                   db
db
db
                                                                                                                   'Version '
VER_NO+'O'
                                                                                                                  (REV_NO/10)+'0'
(REV_NO MOD 10)+'0'
                                                                                                   db
                                                                                                   db
db
                                                                                                                   (MOD_NO/100)+'0'
(MOD_NO MOD 100)/10+'0'
                                                                                                                   PAGE
 The Microsoft MACRO Assembler MSDOS 2.00 BIOS and System initialization
                                                                                        02-20-84
                                                                                                                                1 - 5
                                                                                                                   (MOD_ND MOD 10)+'0'
                                                                                                  db
db
OOE1 43 8F 70 79 72 69 67 68 74 20 31 39 38 33 20 44 88 67 69 74 61 6C 20 45 71 75 69 70 6D 65 6E 74 20 43 6F 72 70 6F 72 61 74 69 6F 8E 010D 0D 0A 00
                                                                                        endm
                                                                                                  db
                                                                                                                   'Copyright 1983 Digital Equipment Corporation'
                                                                                                  db
                                                                                                                   cr,1f,0
                                                                                   Stack for booting.
                                    80 [
                                                                                                  dw 128 dup (?)
                   0110
                   = 0210
                                                                                  stack equ $
                   0210
                                                                                  code ends
```

### ### ##############################	N a m e	GROUP	align		
N m m m	S: Name			COMBINE	class
N a m e	.s: 	9210			
N a m e	N a m e		911E	POBLIC	CODE.
Dev		Туре	Value	Attr	
LL		L NEAR			External
Idea	I IT	L NEAR	0000	CODE	Global
Number	2	Number	4000		
Number OO13		Number	000D		External
NEET_DOS_LOCATION Y WORD OOO		Number	0013		
FAULT_DRIVE.	IT_DOS_LOCATION	V WORD	0000		External
V SYTE	T_DRIVE	V BYTE	0000		
SBASE . Number O1EO S POINTER . V WORD 0000 External NOEV . V WORD 0000 External DBIOS . L NEAR 0000 External DBIOS . L NEAR 0000 External DBIOS . L NEAR 0000 External Number 0018 Number 0000	_LIST	V BYTE	0000		
Note	E	Number	01E0	CODE	
Number 0018 0000 External 0000 External 00000 000000	'	V WORD	0000		Externa 1
No. Number Numb		Number	001B		External
NEAR L N		V BYTE	0000		
L BYTE OOE	: v <i>.</i>	L NEAR	0000		External
RELDC.		L BYTE	00E 1	CODE	
Number FF00	C	Alias	TRUE		External
MORY SIZE	'	Number	FFOO		
L NEAR	_SIZE	V WORD	0000		External
NEAR OOOO		L NEAR	0000		
TR	'_BASE	Number	0025		
SINIT		Number	0005		External
NEAR OOSE CODE Number OOO2 Number OOO2 Number OOO2 Number OOO2 Number OOO2 Number OOO2 Number OOO OOO Number OOO OOO Number OOO	IT	L FAR	0000	CODE	External
The Microsoft MACRO Assembler 02-20-84 PAGE Symbols-2 SDDS 2.00 BIDS and System initialization ATACK L NEAR 003A CODE PRSNT Number 0001 CPRSNT Number 0002 FRPKT Number FEFC OPTI1 L NEAR 0052 CODE OPTI2 L NEAR 0052 CODE OPTI2 L NEAR 0052 CODE OPTION V BYTE 0000 External BOINT Number 0000	IM	L NEAR	009F	CODE	
DOS 2.00 BIOS and System initialization TACK L NEAR 003A CODE RSNT		Number	0002		
TACK L NEAR OO3A CODE RSNT					
DS 2.00 BIDS and System initialization ACK					
\$ 2.00 BIDS and System initialization CK	Aicrosoft MACRO Assembler	02-20	0-84	PAGE	Symbols-2
Number 0001 SNT			• • •		· ,
RSNT				CODE	
T12 L NEAR 0002 CODE T12 L NEAR 0075 CODE T10N V BYTE 0000 External INT Number 0000 ning Severe ors Errors	(T	Number	0002		
TTI2 L NEAR 0075 CODE PTION V BYTE 0000 External PTINT		L NEAR	0062		
INT Number 0000 ning Severe ors Errors	2	V BYTE	0000	CODE	External
ors Errors		Number	0000		
0	Errors				
	•				

Symbol Cross Reference		15 04	finiti	on)	Cref-1	
JX2DEV	30#	149				
ELL	72#					
IOSINIT	20	98#				
BIOSSEG	17#	145	150			
BIOSSIZ	18#	161				
	~~~	180				
CGROUP	11	51	5 1			
CODE	11 70#	50# 225	50	236		
CTRL O	75#	225				
CTRL_S	74#					
CTRL_Z	76#					
CURRENT_DOS_LOCATION	36#	162				
DATLEN	85#	117				
DEFAULT_DRIVE	44#	158				
DEVICE_LIST	40#	196	197			
DONE_MEM	184	188#				
DOSBĀSE	78#					
DOS_POINTER	29# 30#	169 143	148			
	30#	143	140			
ENDBIOS	24#	164				
ESC	73#					
FALSE	58#	6 1				
FILES	48#	191				
FINAL_DOS_LOCATION	38#	168				
HDSKDEV	30#	144				
HD_INIT	25#	140				
HETLO	133	204#				
IOINIT	23#	108				
IVRELOC	80#					
LDRDATA	82#	83	115			
LDRPKT	83#	122	125			
LF control of a co	71#	225				
MEMORY_SIZE	42#	189				
MOD_NO	68#					
BAGUET ABB						
PACKET_ADR	28# 27#	124 116				
PKTFCN	86#	123				
PSTR	26#	134				
REV_NO	67#					
STACK	101	234# 151	154	202		
<del></del>	377		134	202		

									•			alizati			
Symbo	1	Cre	95	5 1	R 0	fe	re	nc	9				(# is	definition)	Cref-2
rst_me	М.											175#	187		
/ER_NO												66#			
WATACK												128#	128		
WPRSNT					٠		٠					88#	138		
XCPRSN'	τ.											89#	146		
XFRPKT												84#	125	128	
XOPTI1												139	142	146#	
XOPTI2												147	1514	,	
XOPTIO												30#	138	146	
ZBOINT												87#	127		

```
The Microsoft MACRO Assembler MSDOS driver initialization
                                                                                                                        02-20-84
                                                                                                                                                             PAGE
                                                                                                                                                                                    1 - 1
                                                                                                                                      PAGE
TITLE
NAME
                                                                                                                                                             60,132
MSDOS driver initialization
                                                                                                                                      COMPANY CONFIDENTIAL
Copyright (C) 1983 Digital Equipment Corporation
All rights reserved.
                                                                                                                                       09/23/83
                                                                                                                cgroup group code
                         0000
                                                                                                                code segment byte public 'code' assume cs:cgroup,ds:cgroup
                                                                                                                public
extrn
extrn
extrn
                                                                                                                                      ioinit
r100:byte,xoption:byte
fastcon:near,clkisr:near,z80isr:near,intret:near
physdisk:near,inthdl:near
rupt720:near, xrupt7201:near
clk_80_50:byte, clk_18_20:word, clk_adj:byte, ticker:byte
                                                                                                                include iodef.ash
;Rainbow Interrupt numbers.
;1-Apr-83 sgs added dskio int vector
;19-Mar-83 sgs added profile int vector [user clock]
;the int profile is called by the RTC interrupt service for each tick.
;The ax and ds register don't need to be saved [done in clkisr].
                                                                                                        64h
2ch
65h
                                                                                                                                                                                   ;100. user interface to clock interrupt
;80Hz int,
;direct disk io for format
                         = 0084
= 002C
= 0085
                                                                                                                profile equ
clk_int equ
dskio equ
                                                                                                                ;
17-Mar-83 sgs changed to include int 20-28;
interrupt 22-24h are duplicated at 42-44h for consistency.
;These are the relocated Rainbow interrupts.
;interrupts, 20h-26h moved to 40h-46h;
ATTENTION Modules IOINIT and REINIT may have to be
;changed if int vectors 40h to 46h are changed
                         = 0040
= 0041
= 0042
= 0043
= 0044
= 0045
= 0047
= 0018
                                                                                                                vert equ
spare41 equ
spare42 equ
comdma equ
                                                                                                                                                             40h
41h
42h
43h
44h
45h
46h
47h
18h
                                                                                                                                                                                   ;DMA ctrl optional comm. board
;7201 comm./printer
;extented comm. option
;new UART vector,
;Z80 interrupt,
;new RDM access,
                                                                                                                comdma equ
aux_prn equ
excom equ
uart equ
z80 equ
rom equ
                                                                                                                 :DEC Rainbow
                                                                                                                                                     IO port stuff.
                                                                                                                 kdp
ksp
                                                                                                                                                                                    ;8251 data port,
;8251 status,
                                                                                                                                       equ
```

02-20-84

Values in

wprsnt equ xcprsnt equ

syspar bit8 equ

edn edn edn

auxdp prndp auxp prnp PAGE

40h 41h 42h 43h

TION

OEEOOH O1FFEH O1OOH 1 - 2

;7201 data,

;7201 command,

;Winnie preset ;XCOMM present

> ;segment of system RAM ;offset to SYSPAR word ;Majik bit in SYSPAR

The Microsoft MACRO Assembler - MSDOS driver initialization

= 0040 = 0041 = 0042 = 0043

= 0001 = 0002

= EE00 = 1FFE = 0100

5675596123645678

```
The Microsoft MACRO Assembler - MSDOS driver initialization
                                                                                        02-20-84
                                                                                                                  PAGE
                                                                                   page
; First re-init the default vectors by invoking ROM service
                   0000
                                                                                   ioinit:
                    0000 BF 000C
0003 CD 28
                                                                                                                  di,0Ch
40
                                                                                                                                                   ;Setup ROM vectors
                                                                                   get clock rate from rom
                   0005 OE
0008 1F
0007 BF 000E
000A CD 28
                                                                                                  push
pop
mov
int
                                                                                                                   cs
ds
di,0Eh
40
                                                                                                                                                   ;ds:=blos_seg
;return clock rate
                               B3 3C
B7 FD
BA 01AA
22 CO
74 07
B3 32
B7 03
BA 0200
                                                                                                                  b1,60
bh,-3
dx,426
a1,a1
c1k60
                                                                                                  mov
mov
                                                                                                                                                   ; assume 60 Hz
; adjust value
; 0.01 sec add value
; 0 = 60 Hz
                    2000
                    000E
0010
0013
0015
0017
0018
001B
001E
001E
                                                                                                   mov
and
                                                                                                  jz
mov
mov
                                                                                                                                                   ; it's 50Hz
                 001E 88 1E 0000 E
0022 89 18 0000 E
0022 88 3E 0000 E
002A 88 1E 0000 E
002B FC
                                                                                  c1k60:
                                                                                                                  clk_60_50,bl
clk_16_20,dx
clk_adj,bh
ticker,bl
                                                                                                                                                  ; set 80/50 Hz
; set 0.01 sec add value
; set adjust value
; init ticker
                                                                                                   mov
                                                                                                   mov
                                                                                                  cld
                                                                                  ;For the Rainbow, we must play horrible games ;with interrupt vectors. Copy the problem 2\boldsymbol{x} ;vectors elsewhere.
                   002F 1E
0030 06
0031 33 C0
0033 8E D8
                                                                                                   push
                                                                                                                   ds
                                                                                                   push
xor
                                                                                  ;Setup Winnie/Xcomm vectors here (also set XOPTION)
                   0035 B8 EE00
0038 &E C0
003A 26: F7 06 1FFE 0100
0041 75 11
                                                                                                  mov
                                                                                                                   ax, offset sysram
                                                                                                                   es,ax
es:word ptr syspar,bit&
optnop
                                                                                                  mov
test
jnz
                    0043
0045
0047
0049
004B
004D
004F
                              B3 02
E4 68
24 E0
3C A0
75 02
B3 01
                                                                                                  mov
in
and
cmp
jnz
mov
                                                                                                                   b1,xcprsnt
                                                                                                                                                   ;assume XCOMM
;check winnie present
  118
119
120
121
122
123
                                                                                                                  al, ben
al, oEOh
al, oAOh
xoptini
bl, wprsnt
                                                                                  xoptini:
                              2E: 88 1E 0000 E
                                                                                                                   cs:xoption,bl ;set option byte
                                                                                                  mov
 The Microsoft MACRO Assembler - MSDOS driver initialization
                                                                                        02-20-84
                                                                                                                  PAGE
  124
                   0054 FA
0055 33 CO
0057 8E CO
0059 BB 0090
                                                                                  optnop: cli
  1112722901123456789041234456789012345567890612345667890123475666645678901234756778
                                                                                                                                                   ;Ints off here
                                                                                                                                                   ;ES: base 0
;base of INT 24
                                                                                                   MOV
                                                                                                                   es,ax
bx,24h+4
                   005C C7 07 0000 E
0080 8C 4F 02
                                                                                                  mov
mov
                                                                                                                   word ptr O[bx], offset RUPT7201 ;comm port handler word ptr O[bx], cs
                   0063 2E: 80 3E 0000 E 01

0069 75 08

006B C7 47 04 0000 E

0070 8C 4F 06

0073 2E: 80 3E 0000 E 02

0079 75 08

007B C7 47 04 0000 E

0080 8C 4F 06
                                                                                                  cmp
                                                                                                                   cs:xoption,wprsnt
                                                                                                                                                                                   :Winnie?
                                                                                                  jne
mov
mov
                                                                                                                   notwini
word ptr 4[bx],offset inthd1
word ptr 6[bx],cs
                                                                                                                   cs:xoption,xcprsnt
                                                                                                                                                                                   ; X C D MM
                                                                                                                   notxcomm word ptr 4[bx],offset XRUPT7201 ;XCOMM handler word ptr 8[bx],cs
                                                                                                   jne
                                                                                                   mov
                                                                                                   mov
                    2800
                                                                                   notxcomm:
                             C7 47 OC 0000 E
8C 4F OE
                                                                                                                   word ptr 12[bx],offset z80isr
word ptr 14[bx],cs
                    0083
                                                                                                   mov
                   008B 89 0010
008E BE 0080
0091 BF 0100
0094 F3/ A5
                                                                                                                  cx,8*2
si,20h*4
di,vert*4
                                                                                                                                                  ;8 vectors
;copy vert.[20h to 27h]
;to int 40h-47h
                                                                                                  mov
                                                                                                  mov s
mov d
rep movsw
                   0086 BE 00A0
0089 BF 0080
009C A5
008D A5
                                                                                                  mov
                                                                                                                                                   :move INT 40
                                                                                                                   si,28h*4
di,rom*4
                   OOSE BF OOA4
OOA1 BS OOOO E
COA4 AB
OOA5 8C C8
OOA7 AB
                                                                                                   mov
                                                                                                                   di,29h*4 ;:
ax,offset fastcon
                                                                                                                                                   ; set FASTCON
                                                                                                  mov
stosw
mov
                                                                                                   stosw
                              BF 00B0
B8 0000 E
AB
8C C8
AB
                   OOA8
                                                                                                  mov
                                                                                                                  di,clk_int*4 ;set RTC,
ax,offset clkisr
                   OOAE
                                                                                                  Stosw
mov
stosw
                    00AF
00B1
                   0082
0085
0088
0089
0088
                               BF 0190
B8 0000 E
AB
8C C8
AB
                                                                                                                  di,profile*4 ;set RTC user vector ax,offset intret ;set to iret
                                                                                                  mov
                                                                                                   mov
stosw
                                                                                                   mov
                                                                                                   stosw
                   OOBC BF 0194
OOBF B8 0000 E
OOC2 AB
OOC3 8C C8
OOC5 AB
                                                                                                   mov
                                                                                                                  di,dskio*4 ;disk io,
ax,offset physdisk
                                                                                                  mov
stosw
                                                                                                                  ax,cs
                                                                                                   MOV
                                                                                                   stosw
```

The Microsoft MACRO Assembler - MSDOS driver initialization ;re-enable ints OOCS FB sti The Microsoft MACRO Assembler - MSDOS driver initialization 02-20-84 PAGE 1-6 page ;Interrupt vector relocation checks...;checks to see if ri00 A or ri00 B -;if ri00 b then if ROM version 5.02+, then;relocate vectors OOC7 55 OOC8 BF OO1A OOC8 BA O109 R OOCE 8C CD OOD0 CD 28 OOD2 5D bp di,lah dx,offset temp bp,cs 40 ;make sure frame ptr is preserved ;Rom version # check command ;check buffer ;set frame ptr to roms ;invoke firmware ;and restore the frame ptr push mov mov int POP bр cs ;set ds to cs for convenience ds byte ptr R100,0 ;assume 100a byte ptr temp.0 ;100A doesn't return anything ioi5 ;and don't go any further push pop mov cmp je ax,word ptr temp; fetch digits
al,ah; swap bytes?
ax,'05'; check first rom #
ioi5; just bail out
ax,word ptr temp+3; get 2nd ROM #
al,ah; swap bytes
ax,'02'; check other ROM # OOE1 A1 0109 R
OOE4 86 C4
OOE5 3D 3035
OOE9 72 18
OOEB A1 010C R
OOEE 86 C4
OOF0 3D 3032
OOF3 72 11 mov xchg cmp jb mov xcha ; We now have a 100B with current RDMs; relocate the hardware vectors byte ptr r100,1 ;tell 'em that type we are al,20h ah,0A0h ; di,1Ch ;set up for rom vector relect,18h ; 40 ;and tell the rom OOF5 CS OOFA BO OOFC B4 OOFE BF O101 B9 O104 CD O106 O106 O7 O107 1F O108 C3 C6 06 0000 E 01 B0 20 B4 A0 BF 001C B9 0016 CD 28 shvvec: mov mov mov mov ;set up for rom vector relocate mov i nt 1015: es ds pop ret 0108 00 00 00 00 00 00 00 00 00 00 00 00 db 0,0,0,0,0,0,0,0,0,0,0,0,0,0 0117 code ends

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PAGE

N a m e	The Microsoft MACRO Assembler - MSDOS driver initialization	02-2	10-84	PAGE	Symbols-1
N a m e	Segments and groups:				
N a m e	Name	Size	align	combine	class
N a m e	CGROUP		BYTE	PUBLIC	'CODE'
Number   0040	Symbols:				
Number 0042   Number 0044   Number 0004   Number 0100   L NEAR 001E   CDE   L NEAR 0000   CDE   Exte   L NEAR 0000   CDE   Exte   CO   V WORD 0000   CDE   Exte   CO   V WORD 0000   CDE   Exte   CO   V WORD 0000   CDE   Exte   CO   CO   CO   CO   CO   CO   CO   C	N & m &	Type	Value	Attr	
Number   0044   Number   0100   L NEAR   0016   CDDE   L NEAR   0000   CDDE   Exteres   CO.   L NEAR   0000   CDDE   Exteres   CDDE	AUXDP				
Number   O100   L NEAR   O010   L NEAR   O010   CDDE   Exte	AUXP				
L NEAR 001E CDDE	BITA				
L NEAR 002E CDDE	CLK60			CODE	
V WORD 0000 CDDE Exte V SYTE 0000 CDDE Exte V SYTE 0000 CDDE Exte Number 002C Number 002S Number 0085 Number 0085 L NEAR 0000 CDDE Exte L NEAR 0083 CDDE L NEAR 0083 CDDE L NEAR 0084 CDDE L NEAR 0084 CDDE L NEAR 0000 CDDE Exte Number 0041 Number 0040 Number 0040 Number 0041 Number 0042 Number 0042 Number 0042 Number 0042 Number 0040 Number 0047 Number 0047	CLKISR				External
V BYTE 0000 CDDE Exte  V BYTE 0000 CDDE Exte  Number 002C  Number 004S  Number 006S  Number 006S  Number 0000 CDDE Exte  L NEAR 0000 CDDE Exte  Number 0010  Number 0011  Number 003 CDDE  L NEAR 0000 CDDE Exte  Number 0043  Number 0043  Number 0043  Number 0044  V BYTE 0000 CDDE Exte  Number 0018  L NEAR 0000 CDDE Exte  Number 0018  L NEAR 0000 CDDE Exte  Number 0041  Number 0042  Number 0045  Number 1FFE  Number 0040	CLKX1				External
V BYTE 0000   CDDE   Extermination   Number 0022	CLK_60_50				External
Number 0043	CLK_ADJ				Externa1
Number 0045	CLK_INT				
Number 0045	DSKIO.				
L NEAR 0000 CDDE Exte  L NEAR 0105 CDDE  L NEAR 0105 CDDE  L NEAR 0000 CDDE G10b  L NEAR 0011  L NEAR 0013 CDDE  L NEAR 0003 CDDE  L NEAR 0003 CDDE  L NEAR 0000 CDDE Exte  Number 0041  Number 0041  Number 0041  Number 0043  Number 0043  Number 0044  Y BYTE 0000 CDDE Exte  Number 0055  L NEAR 0000 CDDE Exte  Number 0016  L NEAR 0000 CDDE Exte  Number 0041  Number 0042  Number 0042  Number 1FFE  Number 0042  Number 1FFE  Number 1FFE  Number 1FFE  Number 0040	EXCOM	Number	0045		
L NEAR 0000 CDDE	FASTCON				External
L NEAR 0105 CODE	INTRET				External
Number 0010	IOI5	L NEAR			
Number 0011	IDINIT			CODE	Global
L MEAR 0073 CODE L NEAR 0083 CODE L NEAR 0084 CODE L NEAR 0000 CODE Exte Number 0041 Number 0043 Number 0084 V BYTE 0000 CODE Exte Number 0018 L NEAR 0000 CODE Exte Number 0018 L NEAR 0000 CODE Exte Number 0041 Number 0041 Number 0041 Number 1FFE Number 1FFE Number 1FFE Number 1FFE Number 1FFE Number 0040 Number 0040 L BYTE 0108 CODE V BYTE 0000 CODE Exte Number 0040 Number 0040 Number 0040 Number 0040 Number 0040 Number 0040 Number 0001 Number 0002 L NEAR 006F CODE V BYTE 0000 CODE Exte	KSP				
L NEAR 0054 CDDE   L NEAR 0000 CDDE   Number 0041   Number 0043   V BYTE 0000 CDDE   L NEAR 0000 CDDE   L NEAR 0000 CDDE   L NEAR 0005 CDDE   Number 0041   Number 0041   Number 0042   Number 0042   Number 1FFE   Number 1FFE   Number 1FFE   Number 1FFE   Number 0045   L BYTE 0000 CDDE   Exte   Number 0040   L BYTE 0000 CDDE   Y BYTE 0000 CDDE   Exte   Number 0040   Number 0040   Number 0040   Number 0040   Number 0040   Number 0040   Number 0001   Number 0002   L NEAR 006F CDDE   Exte   Crosoft MACRO Assembler   O2-20-84   PAGE Symb	NOTWINI.			CODE	
L NEAR 0000 CDDE Exte Number 0041 Number 0043 Number 0084 V BYTE 0000 CDDE Exte Number 0018 L NEAR 0000 CDDE Exte Number 0018 L NEAR 0000 CDDE Exte Number 0041 Number 0041 Number 0042 Number 1FFE Number 1FFE Number 1FFE Number 1FFE Number 1FFE Number 1FFE Number 0040 Number 0001 Number 0001 L NEAR 004F CDDE V BYTE 0000 CDDE Exte V BYTE 0000 CDDE Exte Number 0001 Number 0000 CDDE Exte ON Number 00047 Number 00047	NOTXCOMM				
Number   0041   Number   0043   Number   0043   Number   0064   V BYTE   0000   CDDE   Exte   Number   0018   L NEAR   0000   CDDE   Exte   Number   0041   Number   0041   Number   0042   Number   0042   Number   EFE   Number   EFE   Number   EFE   Number   0000   CDDE   Exte   Number   0000   CDDE   Exte   Number   0040   Number   0040   Number   0040   Number   0001   Number   0002   L NEAR   0045   CDDE   Exte   V BYTE   0000   CDDE   Exte   Exte   V BYTE   0000   CDDE   V BYTE   0	DPTNOP				External
Number 0043	PRNDP			CODE	EXCUPING
V BYTE 0000 CDDE Exte   Number 0018   Exte   Number 0018   Exte	PRNP				
Number 0018	PROFILE				External
L NEAR OOFO CDDE Exte  L NEAR OOFS CDDE  Number 0041  Number 0041  Number 1FFE  Number 1FFE  Number 1FFE  Number 0000 CDDE  V BYTE 0109 CDDE  V BYTE 0000 CDDE  Number 0040  Number 0040  Number 0040  Number 0001  Number 0002  L NEAR 004F CDDE  V BYTE 0000 CDDE Exte  Crosoft MACRO Assembler  driver initialization  L NEAR 0000 CDDE Exte	ROM			CODE	EXTERNAI
Number 0041	RUPT7201				Externa1
Number 0042	SHVVEC			CODE	
Number   IFFE   Number   EFE	SPARE42				
Number 0045	RVSPAR	Number			
Number 0045	SYSRAM				
Number 0045	TEMP				External
Number 0040 Number 0001 Number 0002 L NEAR 004F CODE V SYTE 0000 CODE Exte	UART				EXCOLUGI
Number 0002 L NEAR 004F CODE V BYTE 0000 CODE Exte	VERT				
L NEAR 004F CODE V BYTE 0000 CODE Exte	WPRSNT				
Prosoft MACRO Assembler 02-20-84 PAGE Symbolic original control of the control of	XOPTINI			CODE	
driver initialization  L NEAR 0000 CDDE Exte	XOPTION	V BYTE	0000	CODE	External
driver initialization  L NEAR 0000 CDDE Exte					
driver initialization  L NEAR 0000 CDDE Exte					
driver initialization  L NEAR 0000 CDDE Exte					
driver initialization  L NEAR 0000 CDDE Exte				***************************************	
driver initialization  L NEAR 0000 CDDE Exte					
D1 L NEAR OOOD CDDE Exte	The Microsoft MACRO Assembler	02-2	0-84	PAGE	Symbols-2
Number 0047	- MSDDS Griver initialization				
Number 0047					
Number 0047 L NEAR 0000 CODE Exte	KRUPT7201			CODE	External
L NEAR COOL CODE EXTE	780			CODE	External
		- NEMK	5550	COSE	CALU! HE !

Warning Severe Errors Errors O O

- MSDOS driver initialization					
Symbol Cross Reference	1	# is de	efiniti	on)	Cref-1
AUXDP	56# 58# 46#				
AUX_PRN	68#	113			
CGROUP	12	15	15		
CLKSO	8 8 1 9 #	92# 163			
CLKX1	97# 22#	9.4			
CLK_ADJ	22# 22#	93 95			
CLK_INT	32# 12	162 14#	14	227	
COMDMA	45#				
DSKIO	33#	174			
FASTCON	19#	157			
INTHDL	20#	135			
INTRET	19# 199	169	208	218#	
IOINIT	17	73#		-	
KDP	54# 55#				
NOTWINI	134	137#			
NOTXCOMM	139	142#			
PHYSDISK	20#	175			
PRNDP	57# 59#				
PROFILE	31#	168			
R100	18# 50#	197 152	212		
RUPT7201	21#	130			
SHVVEC	212#				
SPARE42	44# 67# 66#	113			
SYSRAM	190	111	201	205	223#
TICKER	22#	96	201	205	£ £ 3#
UART	48#				
VERT	42#	148			
- MSDOS driver initialization					
Symbol Cross Reference		(#isd	efiniti	on)	Cref-2
WPRSNT	63#	121	133		
XCPRSNT	64# 120	116 122#	138		
XOPTION	18#	123	133	138	
280	49#				

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Guidelines for Producing Translatable Products

### First Printing, November 1984

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### CHAPTER 1

### INTRODUCTION

## 1.1 WHO SHOULD READ THIS DOCUMENT?

If you are a software developer or documentation writer, read this document before starting work on your product. This will ensure that international requirements are built in, rather than added after the development of a version for one country.

### 1.2 WHAT THIS DOCUMENT WILL TELL YOU

The first chapter of this document gives an overview of topics that can affect the final quality of a translated product. These topics are then presented in greater detail in the remainder of the document, with particular emphasis on developing software for translation and writing text for translation.

### 1.3 WHY BUILD TRANSLATABILITY INTO THE DESIGN?

Sales of DIGITAL's products are increasing to users whose native language, customs, and conventions are different from those of the developers of the product. These users are frequently unfamiliar with computers.

If international requirements are not taken into consideration, translation is likely to be difficult and expensive. The key is to design a core product that does not have to be altered to be translated.

Products to be translated must be developed in three stages:

ldentification of all international requirements in the following categories:

#### INTRODUCTION

- Software
- Documentation
- Packaging and distribution
- Development of the base system to meet the requirements of one country/market without putting obstacles in the way of international requirements.
- 3. Adaptation of the base system to meet the requirements of other countries/markets.

#### 1.4 SOFTWARE

Your product may appear in one country in more than one language. Therefore it may have to include the ability to communicate in more than one language, and to switch from one language to another easily.

Also, functions of many applications differ from country to country. This means that different versions of the software will be required. For example, an accountancy application must conform to the accountancy practices of the target country.

## 1.5 DOCUMENTATION

The documentation that DIGITAL provides in countries outside the U.S. depends on the product and the country. Sometimes only user documentation is translated; sometimes nothing is translated. The factors that influence translation include:

- Legal requirements
- National standards
- General knowledge of the English language in the target country
- Availability of English-speaking product support and service personnel
- Nature of the product

### INTRODUCTION

## 1.6 PACKAGING AND DISTRIBUTION

Translated products may be packaged as:

- Distinct versions of a base product. One kit is then developed for each language by adapting the base product. Each language's version of the product is then maintained and supported separately.
- Translated updates to a base product. This leads to logistics problems of support and maintenance once there is more than one version of the product in the field.

#### CHAPTER 2

#### DEVELOPING SOFTWARE FOR TRANSLATION

This chapter describes how to design software so that it can be translated with a minimum of change to the original product.

When developing software that is to be translated, follow these guidelines:

- Allow for the full DIGTAL Multinational Character Set.
- Isolate user text in modules separate from the code.
- Allow for expansion of the text.

Specific guidelines are set out in the following sections.

### 2.1 CONSTRUCTING TRANSLATABLE SOFTWARE

The following topics are discussed:

- Construction of messages and commands
- How to handle errors
- Designing Help
- Representing characters of countries outside the U.S.

## 2.1.1 Screen Text

To make translation easier, isolate all text used to communicate with the user. Observe the following guidelines:

- Hold all messages as variable-length strings in separate modules or files. Do not embed messages in code.
- Do not modify messages by overwriting or by inserting English text.
- Try to store messages as one unit. If this is not possible, bear in mind the following points:
  - An English message which can be split neatly into, say, three parts (see the example below) may not divide neatly into the same number of distinct parts after translation.
  - The order in which the different parts can be displayed must be completely flexible, to take into account a different sentence structure in another language.

For example, suppose you stored an error message in three parts, as follows:

error n input exceeds the quantity allowed at line n

and the parts were translated into German. If you then wanted to display them in the order:

Error n, input exceeds the quantity allowed at line n.

the resulting message in German would be ungrammatical.

- Variables for which different values will be inserted at run time must be allowed to occur at any point within any of the parts of the message.
- Do not use the same piece of text in different contexts. Although the text may be the same in English, it may be different in another language. For example, consider the two messages:

Printer has been disconnected Device has been disconnected

If the text "has been disconnected" was stored as one unit, and the words "Printer" or "Device" were inserted as appropriate, there would be no problem in English, but in French the participle "disconnected" would have to agree with the gender of the noun; and in this case the nouns have different genders.

• Follow the GUIDELINES FOR WRITING TEXT in Chapter 3.

Because translated text is frequently longer than the original (it may occupy 25% to 50% more space), it is important to take the following points into consideration:

• If you want to display text at a particular position on the screen, do not store the position in the code. The length of the text will almost certainly change when it is translated; therefore the system may need to display the translated text at a different position on the screen.

You could either:

- store the position in the same file as the text. This can be changed when the text is translated.
- use the length of the text as a parameter for calculating its display position.

An example of a routine which must be able to handle differing lengths of text is one which centres text:

English version:

RAINBOW 100 Diskette Copy Program V1.0 Press EXIT to quit

French version:

RAINBOW 100
Programme de copie de disquette V1.0
Appuyez sur <SORTIE> pour terminer

- A routine that displays text must be capable of handling more than one full screen of text at a time. In addition, you must design the routine to allow the user to display the text one page at a time.
- Tables that are very tightly packed with information are difficult to format on the screen when translated. In order to accommodate the table on the screen, the translator may be obliged to use obscure abbreviations.

One solution is to split the table into several different displays on separate screens. Another is to arrange the table in rows as opposed to columns.

For example, use:

Surname	! n	n	n	n
Age	! n	n	n	n
Nationality	! ! n	n	n	n
Date of Birth	! ! n	n	n	n
Occupation	: ! n	n	n	n
Salary	! ! n	n	n	n

rather than:

Surname	Age	Nationality	Date of Birth	Occupation	Salary
n	n	n	n	n	n
n	n	n	n	n	n
n	n	n	n	n	n
n	n	n	n	n	n
n	n	n	n	n	n

### 2.1.2 Commands

Making commands easy for the user to understand also helps translation. Employ the user interface that the system supports for commands, and observe the following guidelines:

- All command names, qualifier names, and qualifier values must be table-driven, so that they can be easily replaced with translated text.
- Checking of user responses must be table-driven.
- Consider the implications of translation before using English sentences as input. For example, can the software be modified easily to recognize that "oui" is the same response as "yes"?

Use one or more of the following methods to select commands from menus:

- Positioning the cursor on the command.
- Selecting a number.
- Selecting the one or more letters of the command. In this case check the letter or letters against a table of valid commands. Do not hard-code them. Allow also for the selected letter(s) to occur at any position within the command, since after translation, several commands in a menu might start with the same word. Remember that you may need to use more letters to make the translated command unique.

## 2.1.3 Handling Errors

Use the system-supported user interface for reporting errors.

Ensure that the error handler can use parameters in any order.

## 2.1.4 Designing Help

Follow the GUIDELINES FOR WRITING TEXT in Chapter 3.

### 2.1.5 Representing Characters Of Countries Outside The U.S.

There are two main ways in which products running on DIGITAL hardware represent alphabetic characters not present in ASCII.

- Using the DIGITAL Multinational Character Set. This is the way that new software products represent alphabetics not present in ASCII. The DIGITAL Multinational Character Set is represented using 8 bits.
- 2. Using the 7-bit codes standardized for the relevant countries (just as ASCII is the U.S. standard). These codes are indistinguishable from ASCII. Up to 11 characters that appear in ASCII are replaced by other characters depending on the language being represented. These character sets are called National Replacement Character Sets. They are used extensively in Europe. The Rainbow 100+ supports the NRC sets for applications doing CONSOLE IN/CONSOLE OUT (level 2) character Input/Output.

For character-coded data in particular, your software should:

- Not mask the 8th (most significant) bit of a byte, or use it for special purposes, if the byte may contain character data.
- Permit at least all graphic characters in text literals and comments.
- Allow collating sequences to be table-driven. To be completely accurate, collation needs to use a two-character lookup, because, for example, German o-umlaut is regarded as "oe" and u-umlaut is regarded as "ue".

Appendix B lists the characters that you should use when characters in addition to the 94 graphic characters of ASCII are needed.

### 2.2 LANGUAGE ISSUES RELATING TO FORMATS

The format of certain types of information varies from language to language. This section indicates how to take some of these different formats into consideration, including:

- Numbers
- Dates
- Time
- Currency
- Addresses and telephone numbers
- Proper names and titles

#### 2.2.1 Numbers

Numbers may be displayed in several formats. Ensure that the user can vary the format, and that any default can be changed to suit the target language.

The format of a number may include any of the following:

- + or blank indicating a positive number, or indicating a negative number.
- The + and may be either prefix or suffix Prefix signs are always directly beside the first significant figure.

Parentheses denoting a negative number. For example, (2356) equals -2356. This convention is chiefly used in financial applications.

Numbers may be separated in two ways:

1. Thousands - period, comma, null.

For example:

- 2.345
- 2,345
- 2 345
- 2. Decimal point period, comma.

For example:

1.34 (English)

1,34 (Continental European)

If your product allows numbers to be entered in a tabular form, do not separate individual table entries with a period or comma. Instead, use a space or a semicolon. For example:

123.456 876.886 956.907

892.654 174.987 217.767

563.982 786.653 543.921

#### 2.2.2 Dates

Where your product displays the name of a day or month as letters, you must allow sufficient storage and display space to accommodate these names in other languages.

For example, this table shows the maximum number of characters you must reserve for storage and display for French, German, and Dutch. Note that month names are not always abbreviated.

	French	German	Dutch
Longest day name	8	10	9
Longest month name	9	9	9

The position of each component in a displayed date may be varied or it may be omitted completely. The components required are described below.

- YEAR 2 or 4 digits (two digits are frequently used)
- MONTH NUMBER number in range 1 to 12
- MONTH NAME Allow enough space for the name of the month not to be abbreviated, because, for example in French, a three-letter abbreviation of month names results in confusion between "juin" and "juillet"
- ORDINAL-DAY NUMBER day number as an ordinal. For example:

 ORDINAL-DAY - day number as an ordinal in words. For example:

> First, Second (English) premier, deux (French) ersten, zweiten (German)

- ARTICLE for example: the, le, den
- DAY NAME Sunday through Saturday (English)
   dimanche through samedi (French)
   Sonntag through Sonnabend (or Samstag) (German)
- DAY NUMBER 1 through 31

• Components may be separated by various editing characters including (at least) hyphen, comma, period, space, and slash.

For example:

26-Feb-84 (DIGITAL standard format for dates)

jeudi le premier mars 1984

14/12 84 (European)

84.11.17 (ISO Standard)

6/27/84 (USA)

1/5 (means 1st May or 5th January)

March 1984

Saturday 3rd March

Donnerstag (German, Thursday)

Because some formats lead to confusion between the day and the month, it is best to use a format that includes the month name.

### 2.2.3 Time

Time may be displayed in either a 12-hour or 24-hour format. A time comprises a number of components:

- HOURS One or two digits. Give the option to have a blank or a leading zero.
- MINUTES Two digits with leading zero displayed. If the HOURS field is absent, a leading zero may be suppressed.
- SECONDS Two digits with leading zero displayed. If the MINUTES field is absent, a leading zero may be suppressed.
- 1/100 SECOND In the absence of other components, these are displayed in the form 00.nn. In conjunction with other components, the 1/100 seconds follow the second component, separated by a period. For example, four minutes, three and one-half seconds past two is displayed as 02:04:03.50 in 24-hour clock format.

The HOURS, MINUTES and SECONDS components must always appear in the order shown and no gaps are allowed. That is, HOURS

and SECONDS is not allowed but HOURS and MINUTES is.

- AM/PM AM indicates morning. PM indicates afternoon. These are used in 12-hour systems only. They are always shown as capital letters. In some Scandinavian countries, M is used to indicate noon.
- ZONE allow up to four characters to indicate the time zone. For example: EST, GMT, MST, CET.
- Separating characters may be placed between the components. These include period, colon, comma, space, and null.

For example:

11 P.M.

13:56:08.40

14.06 hours, GMT

A combination of date and time can also be shown in ISO (International Standards Organization) format. The standard ISO format is a 14-character numeric string, which can be stored as:

### YYYYMMDDHHMMSS

This shows year, month, day, hour, minute, and second with no separating characters, although leading zeros may be inserted in the string where an entry is less than 10. The time is specified on a 24-hour clock, according to the standard specified by the local time zone. Do not assume a time-zone differs from your time-zone by an integral number of hours: for example, Newfoundland, Canada is normally 1 hour 30 minutes later than Eastern Standard Time.

## 2.2.4 Currency

A number may be displayed as an amount of money using the following components:

- A text string of up to four characters for example, \$, Fr, or pts. The text string may occur before or after the numbers.
- Sign placement prefix floated or suffix
- Decimal point character
- Thousands character
- Number of fractional decimal places
- Scaling factor (normally 1)

The decimal point and the thousands convention for currencies may differ from those used for numbers. For example, French usage is 1.746,23 for numbers but can be Fr1746.23 for money.

For any currency, there may be suffixes after integer amounts which indicate that the preceding number will be scaled before use in arithmetic. For example, thirty U.S. cents needs to be divided by 100 to give .3 in dollars (\$0.30). This is indicated by a scaling factor of 0.01. This method could also be used to handle, say, millions of dollars (\$M) by using a scaling factor of 1,000,000.

Note also that the characters and digits representing some currencies expand on conversion, and therefore require more display space. For example: \$1 becomes 1380 lires. You may also need to distinguish between the currencies of countries that use similar names. For example, US dollars and Canada dollars; French francs and Swiss francs.

### 2.2.5 Addresses And Telephone Numbers

Allow sufficient space for different layouts of addresses and telephone numbers.

• The order of the components of an address differs from country to country.

For example,

England

Mr S. B. Turner, (title, initials, surname)
55, High Street, (number, street)
Grantham, (town)
Lincolnshire, (county)
GRI OBT (post code)
England (country)

Germany

Herr Dipl. Ing. Schmidt (title, professional title, surname)
Stolbergerstrasse 90 (street, number)
D-2000 Hamburg 55 (post code, town, district code)
FDR (country)

France

Madame Dupont Claudette (title, surname, first name)
17, Rue Louis Guerin (number, street)
F-69626 Lyon-Villuerbanne (post code, town, suburb)
France (country)

NOTE

Not all post codes are numeric. For example:

RG2 OSU

 Not all telephone numbers are the same length or have the same format. Telephone numbers often include special characters to separate different components. Commonly used separators are square brackets, parentheses, hyphens and spaces. For example:

[1] - (603) -884-1234

(0734) 868711

Note also that the same number could be represented in different ways depending on whether it was for national or international use. For example:

The "+" means that you should dial your own international prefix. This varies from country to country.

### 2.2.6 Proper Names And Titles

Allow for non-alphabetic characters in proper name and title fields (for example, accented letters, apostrophes, and hyphens), to take into account users with names such as de la Bassetière, D'Agostino, Torres-Ferrer. Allow for different formats in title fields, to take into account such titles as Herr Dipl. Ing.

## 2.3 LANGUAGE ISSUES RELATING TO CHARACTER SETS

This section covers three types of collating sequence, and refers to the DIGITAL Multinational Character Set used by the Rainbow personal computer.

- Character string comparisons
- Collating the DIGITAL Multinational Character Set
- Collating sequences for European countries

## 2.3.1 Character String Comparisons

Where collated output is not required, character strings are collated according to the numerical ordering of the DIGITAL Multinational Character Set. This can occur, for example, in programming languages, indexed file key processing, sort/merge and other utilities.

## 2.3.2 Collating The DIGITAL Multinational Character Set

These rules are used where the language of the text is not known:

- The letters AE diphthong, 0 with slash and A with ring, appear in that order, after Z.
- The letter N with a tilde accent appears immediately after N. It is treated as a separate letter falling between N and O.
- German small sharp s and OE ligature are treated as 2-character sequences, "ss" and "OE" respectively. If storage or efficiency is a problem, then German small sharp s is treated as a separate letter between s and t, and OE ligature is treated as a separate letter between the N with a tilde accent and O.

Apart from the letters listed above, the letters in the Multinational Character Set are treated as groups of letters with the same basic collating value. Within a group, small letters are ordered according to their numeric value, and capital letters immediately follow the corresponding small letter. For example, the group for the letter A is:

Small a
Capital A
Small a with grave
Capital A with grave
Small a with acute
Capital A with acute
Small a with circumflex
Capital A with circumflex
Small a with tilde
Capital A with tilde
Small a with diaeresis/umlaut
Capital A with diaeresis/umlaut

The whole group is viewed as being equivalent to A.

### 2.3.3 Collating Sequences For European Languages

Unless the rules for a particular language state otherwise, the following rules apply:

- AE diphthong and OE ligature are treated as 2-character strings, "AE" and "OE" respectively.
- German small sharps is treated as the 2-character string "ss".
- N with tilde, 0 with slash and A with ring are treated as if they were separate characters with diacritical marks.
- All letters with diacritical marks are treated as for collating the Multinational Character Set, as described in Section 2.3.2.

Dutch, English, French, Italian, and Portuguese have no additional rules.

Although the above rules apply generally, there may be some exceptions in certain countries. For example, some countries have special rules for collating proper names. For instance, when collating proper names in German, A with diaeresis/umlaut, 0 with diaeresis/umlaut and U with diaeresis/umlaut are treated as 2-character strings, "AE", "OE" and "UE" respectively.

### Danish and Norwegian:

- U with diaeresis/umlaut is treated as if it were Y.
- A with diaeresis/umlaut is treated as if it were AE diphthong.
- 0 with diaeresis/umlaut is treated as if it were 0 with slash.
- AE diphthong, 0 with slash and A with ring appear as separate letters, in that order, after Z.

### Finnish and Swedish:

- U with diaeresis/umlaut is treated as if it were Y.
- 0 with slash is treated as if it were 0 with diaeresis/umlaut.
- A with ring, A with diaeresis/umlaut and 0 with diaeresis/umlaut appear as separate letters, in that order, after Z.

## Spanish:

- All letters with diacritical marks, except N with tilde, are treated as though the diacritical mark is not present.
- The string CH is treated as a single letter between C and D.
- N with tilde is treated as a single letter after N.
- The string LL is treated as a single letter between L and M.

#### 2.4 DIACRITICAL INSENSITIVITY IN SEARCHES

Some text editors use a technique called "diacritical insensitivity" in searches. Just as case-insensitive searching allows small "a" to match capital "A", diacritical-insensitive searching allows "a" without a diacritical mark to match "a" with a diacritical mark.

If your product uses the Multinational Character Set, diacritical insensitivity could be included as a function in string-matching algorithms. However, the user must be given the option of disabling this function.

If the editor is in case-insensitive mode, the tables for upper- and lower-case are effectively combined. Thus, a search for "a" will match every capital and small "a", with and without a diacritical mark.

The correspondences between characters for diacritical-insensitive searching are listed in Appendix A. Note that although a search for upper-case "A" will match upper-case "A" with a diacritical, the converse is not true. A search for upper-case "A" with a diacritical will NOT match upper-case "A".

### CHAPTER 3

## GUIDELINES FOR WRITING TEXT

A clear writing style is of utmost importance in any technical text, especially if the text is to be translated.

Errors produced during translation can sometimes be attributed to inconsistent vocabulary and poor sentence construction in the original text, combined with a lack of technical knowledge on the part of the translator.

This chapter describes how to design screen text for translation, and how to improve the quality of printed text. It also outlines some areas which require special consideration when writing for translation.

The following DIGITAL publications discuss more fully some of the topics covered in this chapter:

- Writing for the Reader
- Personal Computer Documentor's Guide
- Software Publications Style Guide

## 3.1 DESIGN OF SCREEN TEXT

Well-designed screen displays make any software product simpler, and more pleasant to use. They also make translation easier.

## 3.1.1 Text Of Restricted Length

Screen text is often restricted in length. For example, an error message may be restricted to one line of the screen. Allow for the translated version being at least 30% longer than the original.

For single sentences or phrases, try to reserve at least 50% extra space; otherwise you impose restrictions on the translation that could result in an ambiguous, or even meaningless, translated message.

Do not, however, restrict the length of the original English, in order to end up with a short translated message. Always use as many words as are necessary to make the original message unambiguous.

If you do need to abbreviate an English message, supply the translator with the full text, and tell him what the restrictions on the length of the message are.

Other ways of shortening text may cause problems in another language. For example:

### Acronyms

Do not use acronyms, because they cannot be translated. If an English acronym is retained in a foreign language, there is a risk that it might be similar to a word with negative associations.

### Abbreviations

In general, avoid abbreviations.

In particular, avoid abbreviating words that will remain in English. A translator could easily misinterpret such abbreviations. It is acceptable to use internationally recognized (SI) unit symbols for units of measurement.

### 3.1.2 Short Words

Short, common words are easier to read and understand. Do not use computer jargon. It can confuse and intimidate users.

Use Instead of

facilitate assist, help change modification start activate, initiate end terminate improves enhances show demonstrate discontinue stop employ, utilize use

wrong erroneous

Use a short word in place of a long one only when you are sure that the two words convey exactly the same meaning. In some cases, a longer word may provide a clearer contrast with some other term you have used, or may provide a more precise meaning. In these cases, it is better to use the long word.

#### 3.1.3 Short Sentences

Brief, simple sentences are generally easier to understand than those with several clauses.

When designing menu items and error messages, beware of omitting words for the sake of brevity. For instance, do not omit "the" or "a". This telegraphic style may make it impossible for the translator to tell whether a word is being used as a verb, an adjective or a noun.

For example, does "set to lock at next level" mean "set to the lock which is at the next level" or "set this so that it locks at the next level"?

This style can also obscure who is to perform a certain operation, or the time at which it is performed. For example: "scratch files deleted? (Yes or No)" might mean "have you deleted the scratch files?" or "do you want the scratch files to be deleted?".

### 3.1.4 Affirmative Sentences

Affirmative statements are generally simpler to understand than negative statements. Using this form clarifies the meaning and helps the translator to make an accurate translation.

Use Instead of

Complete your entry before Do not return to menu before returning to the menu. completing entry.

### 3.1.5 Active Voice

The active voice is usually easier to understand than the passive Chapter 7 of the DIGITAL publication Writing for the Reader discusses the use of voice in detail.

Use

Instead of

The EXAM switch verifies the contents of memory.

The contents of memory are verified by the EXAM switch.

## 3.1.6 Strings Of Nouns

Do not string nouns together as if they were adjectives, to produce long titles or names. Often, it is not clear which words qualify which. Such titles and names are hard to read and often almost impossible to translate.

Use

Instead of

A form for sorting the table Table Sort Description Form

#### 3.1.7 Stacking Words

Do not stack words. Stacked words require more eye movements and make text harder to read.

Use

Instead of

Employee's Name:

Employee's Name:

In addition, stacked words can lead to misinterpretation on the part of the translator, who may not realize that the words are to be translated as one unit. For example, if a part of a diagram were labelled:

> Possible Load Exclusion Records

the translator could interpret this as two distinct items. This would lead to a mistranslation.

## 3.1.8 Displaying Columns And Alphanumeric Character Strings

Three to five spaces between columns make data, especially numbers, easier to read.

Use		Instead of					
1066	1840	1918	1066 1840 1918				
1492	1877	1928	1492 1877 1928				
1563	1901	1929	1563 1901 1929				
1701	1907	1963	1701 1907 1963				

Where appropriate, put strings of alphanumeric characters in groups to make them more legible.

Use				Instead of
ABCD	_	_		ABCD123456789
BCDE				BCDE234567890
CDEF	345	678	901	CDEF 345678901

## 3.1.9 Standardize Input Of Data

Standardize input of data for consistency and minimum confusion. Try to use an unambiguous format for dates. Use the month names, but make sure that the product can accept the translated names.

Use		Instead of	
Date of Birth:	24/July/55	Date of Birth:	24/07/55
Date of Hire:	01/February/79	Date of Hire:	February 1, 1979

## 3.1.10 Aligning Lists And Displays

Generally, left-aligned, vertical lists increase legibility.

Use	Instead of
Monday Tuesday Wednesday Thursday	Monday, Tuesday, Wednesday, Thursday, Friday
Friday	

Data-entry fields often have two parts: a name, and a part that the user can modify. If both parts are left-aligned, the text is usually more legible.

Use Instead of

Account Name: The Widget Company Account Name: The Widget Company Street Address: 123 Maple Street Street Address: 123 Maple Street

City, State: Boston, MA City, State: Boston, MA

Post Code: 02100 Post Code: 02100

### 3.1.11 Use Of Capital Letters

Use a mixture of capital letters and small letters for running text.

Use Instead of

Text containing capital letters and small letters is easier to read than text containing all capital letters. Use capital letters sparingly to HIGHLIGHT special terms.

ALL CAPITAL LETTERS ARE MUCH HARDER TO READ THAN A MIXTURE OF CAPITAL LETTERS AND SMALL LETTERS. THE WORDS BLEND TOGETHER AND LOSE IMPACT.

## 3.2 IMPROVING THE QUALITY OF PRINTED TEXT

This section presents guidelines for improving the quality of the English text of technical manuals. These guidelines apply at all times, not only when writing text for translation. Any increase in writing quality will help the translator.

## 3.2.1 Logical Sequence

Describe events in the order in which they occur. A logical sequence helps to keep the meaning clear and makes the translator's job easier.

Use Instead of

Select the line number and Press DO after selecting the line press DO. number.

#### 3.2.2 Consistency

Keep to one idea within each paragraph or section. Try to keep to one tense and person within each book. Use the passive voice selectively.

Try to identify the typical user of the manual and choose the most suitable tense and person for that user. Generally, keep to this throughout the manual. Follow these guidelines:

- Use only one term to refer to the same concept.
- Do not use slang or jargon.
- Use terms consistently:
  - within a manual
  - across manuals

Remember that although the translator's command of English should be high, he or she encounters the text in circumstances very different from those of the English-speaking user. For instance, the translator will not always have the opportunity to experiment with the product in the way a user does.

Maintain your own glossary of terms with special meaning, and give a copy of this to the translator. Include titles of applications, routines, and specialized terms. Where possible, check that your use of terms is consistent with their use in other manuals. It is possible that the translator may refer to, or be familiar with, other related technical manuals.

#### 3.2.3 Coherence

Always try to link the sentences in a paragraph, using words such as, "also", "but", "however", "similarly", and so on. Chapter 4 of Writing for the Reader discusses in detail how to achieve coherence in your writing.

Since it is possible that the translator will not have access to a computer, or to the product you describe, the translator cannot experiment with the system and may not be able to form a mental picture of some of the things you describe. If there are two possible meanings to what you are saying, the translator will have to render one of the two meanings into the foreign language. For example, consider the following use of the word "may":

Strings must have matching quotation marks or no

quotation marks at all, and numbers MAY have quotation marks.

This is open to the following interpretations:

... and numbers must not have quotation marks.

or

... and numbers do not necessarily have to have quotation marks.

Clearly, if the translator chooses the wrong interpretation, the meaning of the sentence is changed dramatically. Even if your text appears clear to an English-speaking reader, always try to look for any possible ambiguity, which could lead to misinterpretation by the translator.

#### 3.2.4 Sentence Construction

Use short introductory clauses. This places little strain on the reader's short-term memory.

Place the subject and verb of a sentence as close together as possible and use short sentences of 25 words or less. If you have to use a long sentence, use commas to clarify your meaning.

See Chapter 6 of Writing for the Reader for a discussion about how to avoid writing complex sentences.

## 3.2.5 Clarity

When writing English text, try not to use two words together that have more than four syllables.

Do not use two or more negative words in the same sentence. Use words with a concrete meaning, rather than abstract words.

Do not use abbreviations and acronyms.

Do not use Latin abbreviations, terms or phrases. Instead, use the following equivalents:

c, ca, circa about
cf. compare
e.g. for example; for instance
etc. and so on; and so forth

i.e. that is

via by means of; by using

viz. namely

#### 3.3 SPECIAL CONSIDERATIONS FOR TEXT TRANSLATION

This section covers:

- Culturally specific examples
- Text concerned with handling language
- Use of tables, diagrams, and figures

# 3.3.1 Culturally Specific Examples

The methods described in Section 3.2 were concerned with clarifying meaning for the translator. But there are also ways in which text, when translated literally, may be less suitable for a reader in another country than for an English-speaking reader. For example:

To clarify a new concept, you give an example from everyday life, which uses terms of reference with which readers in your own country are familiar. For example, you might show how a particular accounting package can be used to calculate mortgage repayments. A literal translation of the example will only confuse readers in countries where a mortgage system is not in use.

If there is no alternative but to give specific examples that are unique to their country of origin, ask the translator to substitute as much as possible of the example with local language equivalents.

Draw up a list of words or phrases that need adaptation, and include a specific note if anything in the original must be retained. For example, you might want measurements changed as a general rule, but the word 'inches' kept to refer specifically to the number of characters per inch put out by a printer.

Avoid using puns, metaphors, and similes. It is always difficult, and sometimes impossible, for the translator to find a suitable equivalent for these in another language.

## 3.3.2 Text Concerned With Handling Language

If you describe any operation that involves the order of words in a phrase or the characters in a word, any example you give will almost certainly have to be adapted or rewritten for translation. If you gave as an example, a list of items rearranged in alphabetic order, a translation of the MEANING of those items would not result in words beginning with the same initial letters. The example would therefore be meaningless.

If your list began with the words: apples, bananas.... a literal translation into French would result in: pommes, bananes....

One possible way round this problem might be to use proper names, for example girls' names (Anna, Beatrice, Cleopatra...) which would retain the same initial letters after adaptation into the target language. The attention of the translator must be drawn to special examples like this.

Here are further examples of topics that give rise to the same kind of problem:

• Changes made to a word using the editor.

#### English example:

In the phrase 'a letter form Graham', to change the word 'FORM' into 'FROM', place the cursor over the 'O', type 'R', move the cursor one place to the right, press the "delete character" key.

## Literal translation:

In the phrase '(a) formulaire a lettres Jacques', to change the word 'FORMULAIRE' into 'DE', place the cursor over the '0', type 'R', move the cursor one place to the right, press the "delete character" key.

The translator will obviously be aware that following these instructions will not produce the word DE. However, there is not sufficient information available for the translator to be able to adapt the example. In cases such as this, highlight the example, and give the translator enough information to be able to supply an equivalent example in the target language.

• Use of "wildcards" to search for words without specifying certain character positions.

#### English example:

If you specify 'search for ????BORO', WESTBORO, EASTBORO will be found, NORTHBORO will not.

#### Literal translation:

(If you specify) '(Search for) ????STADT', WESTSTADT, OSTSTADT (will be found,) NORDSTADT (will not.)

Highlight examples like this for the translator, and ask that they adapt them to make sense in the language of the applicable country.

 File names made unique by judicious choice of the first word of the name, so that only a few keystrokes are necessary to identify the file.

#### English example:

If you call your reports REPORT FOR MAY, REPORT FOR JUNE, and so on you will have to type the whole name to identify each document. Using MAY REPORT, JUNE REPORT, and so on, you need only type the first word.

#### Literal translation:

(If you call your reports) RAPPORT MENSUEL DE MAI, RAPPORT MENSUEL DE JUIN, (and so on you will have to type the whole name to identify each document. Using) RAPPORT MENSUEL DE MAI, RAPPORT MENSUEL DE JUIN, (and so on, you need only type the first word.)

Note that the French language does not have the possibility of translating these document names with the month name first. Even if the translator has understood the principle in English, it cannot be reproduced with this same example.

If you know that an example cannot possibly be adapted for a particular language, you should try to find an alternative example in English, wherever possible.

 The ways in which a subfield of a date, identified by its POSITION, may be updated.

### American example:

In the date  $\frac{5}{24}$ , the second field will change at 12 p.m.

#### Literal translation:

(In the date)  $^{1}24/5/82$ , (the second field will change at 12 p.m.)

For examples like this, you should simply highlight the problem for the translator, and ask him or her to deal with it.

 The method of sorting a list of addresses, with the assumption that the house number will appear BEFORE the street name.

#### English example:

To sort a list of addresses so that all the house numbers in the same street are in ascending order, select a NUMERIC FOLLOWED BY ALPHA sort. The result will look like this:

1 High Street
34 High St.
76 High St.
3 Station Rd.

## Literal translation:

(To sort a list of addresses so that all the house numbers in the same street are in ascending order, select a NUMERIC FOLLOWED BY ALPHA sort. The result will look like this:)

Hochstr. 1 Hochstr. 34 Hochstr. 76 Bahnhofstr. 3

Note that even though the translator can see that "Hochstr. I" is not numbers followed by letters, not enough information is available to supply the correct instructions to perform this task.

You should therefore supply supplementary information so that the translator is in a position to adapt the example.

In general, try to avoid basing examples of field manipulation, searches, sorting, and so on, on any of the "language/country specific" categories listed in this section.

If the example could lose its value when certain features of the wording or spelling are not reproduced in translation, make the text as simple and explicit as possible about the principle being illustrated. Insert an extra phrase such as "Consider this example of numeric characters followed by alphabetic characters". In addition, help the translator and the editor in the target country, by supplying them with a list of the sections of your text which are likely to require adaptation of this kind. Add details of the criteria you used in creating the original of these sections. Such notes would include:

- The purpose of the example.
- The restrictions that had been observed in the choice of the original example (for instance, "a file name not exceeding 6 characters").
- Indication of any text that was required ONLY for users operating in English (for instance, if an English example was included to show that the words "through" and "thru" were interchangeable in a certain command).

# 3.3.3 Use Of Tables, Diagrams And Figures

When writing for translation, do not include humorous cartoons or pictures that indicate words directly. For example, a cartoon familiar to American readers might not translate into anything remotely similar in another language.

- Label all parts of flowcharts and diagrams clearly and include a key to explain any symbols used.
- Specifically avoid national symbols, such as mailboxes, flags, baseball, and so on.
- Avoid putting text in boxes within diagrams. This causes problems if the translated text is much longer than the original.

With regard to illustration, number all the parts which require explanation, and then supply a separate key in the text to explain the numbers. This keeps any text to be translated separate from the illustration itself.

Alternatively, consider producing the diagram on one transparency, and the labelling on a second transparency. In this manner, the translated labelling can be overlaid on the original diagram.

# APPENDIX A

# DIACRITICAL CHARACTER MATCHING

This appendix lists those characters in the DIGITAL Multinational Character Set which have diacritical marks. Characters within the same group are treated as variants of the same character. Note that these rules apply when the language is unspecified. More complete rules are given in Section 2.3

Letter	Diacritical Character Group				
A	A .a .A .a . A .a . A .a . A .a	Small Capital Small Capital Small Capital Small Capital	a A a A a A	with with with with with with with	
С	ç ç				cedilla cedilla
E	/E /8 / E / E / E . E . E . E	Small Capital Small Capital Small Capital	e E e E	with with with with with with	grave accent grave accent acute accent acute accent circumflex circumflex diaeresis/umlaut
1		Small Capital Small	i !	with with with	grave accent grave accent acute accent acute accent circumflex

# DIACRITICAL CHARACTER MATCHING

	î ï	Small Capital Small	ı	with	circumflex diaeresis/umlaut diaeresis/umlaut
0	· O · O · O · O · O · O · O · O · O · O	Small Capital Small Capital Small Capital Small Capital	0000000	with with with with with with with	tilde diaeresis/umlaut
U	נ: כ: כ> כ> כ> כ	Small Capital Small Capital Small Capital	0 0 0 0 0	with with with with with with	diaeresis/umlaut  grave accent grave accent acute accent acute accent circumflex circumflex diaeresis/umlaut diaeresis/umlaut
Υ	ÿ ÿ	Capital Small			diaeresis/umlaut diaeresis/umlaut

# APPENDIX B DIGITAL MULTINATIONAL CHARACTER SET

This table describes the graphic characters included in the DIGITAL Multinational Character Set. The first 128 character positions (octal 0 to 177) are exactly the same as the ASCII Character Set. Positions 200 to 237 are addional control characters not described here.

Octal	Grap	ohic character
240 241 242 243 244 245 246 247	; e £ ¥	reserved inverted exclamation mark cent sign pound sign reserved yen sign reserved section sign
250 251 252 253 254 255 256 257	Ø ⊚ **	general currency sign copyright sign feminine ordinal indicator angle quotation mark left reserved reserved reserved reserved
260 261 262 263 264 265 266 267	• ± 2 3 4 4 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	degree sign plus/minus sign superscript 2 superscript 3 reserved micro sign paragraph sign middle dot

# DIGITAL MULTINATIONAL CHARACTER SET

Octal	Grap	hic character
270	1	reserved
271		superscript l
272	9	masculine ordinal indicator
273	>>	angle quotation mark right
274	1/4	fraction one quarter
275	1/2	fraction one half
276		reserved
277	ذ	inverted question mark
300	7A 7A 7A 7A 8	capital A with grave accent
301	Á	capital A with acute accent
302	Â	capital A with circumflex
303	Ã	capital A with tilde
304	Ä	capital A with diaeresis/umlaut
305	Å	capital A with ring
306	Æ	capital AE diphthong
307	ç	capital C with cedilla
310	/B/B/B ,B/ ^-	capital E with grave accent
311	É	capital E with acute accent
312	Ë	capital E with circumflex
313	Ë	capital E with diaeresis/umlaut
314	Ì	capital I with grave accent
315	İ	capital I with acute accent
316		capital I with circumflex
317	ï	capital   with diaeresis/umlaut
320		reserved
321	o; 010· 0· 0· zı	capital N with tilde
322	ò	capital O with grave accent
323	ó	capital 0 with acute accent
324	ò	capital O with circumflex
325	อี	capital O with tilde
326	ö	capital O with diaeresis/umlaut
327	Œ	capital OE ligature
330	ø	capital O with slash
331	٠; u; u ، u ، u ، A	capital U with grave accent
332	ú	capital U with acute accent
333	Ã	capital U with circumflex
334	ŭ	capital U with diaeresis/umlaut
335	Ÿ	capital Y with diaeresis/umlaut
336	•	reserved
337	ß	German small sharp s
:	,-	

# DIGITAL MULTINATIONAL CHARACTER SET

Octal	Graphic character			
340 341 342 343 344 345 346 347	`a'a `a `a aa C	small a with grave accent small a with acute accent small a with circumflex small a with tilde small a with diaeresis/umlaut small a with ring small ae diphthong small c with cedilla		
350 351 352 353 354 355 356 357	70 / 0 . 0 . 0	small e with grave accent small e with acute accent small e with circumflex small e with diaeresis/umlaut small i with grave accent small i with acute accent small i with circumflex small i with diaeresis/umlaut		
360 361 362 363 364 365 366 367	lc /0 00 00 to :0 8	reserved small n with tilde small o with grave accent small o with acute accent small o with circumflex small o with tilde small o with diaeresis/umlaut small oe ligature		
370 371 372 373 374 375 376	ייי פי עי עי ני ייי פי עי ני	small o with slash small u with grave accent small u with acute accent small u with circumflex small u with diaeresis/umlaut small y with diaeresis/umlaut reserved reserved		

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